

UHF RFID ANTENNA IMPEDANCE  
MATCHING TECHNIQUES

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Master of Science in Electrical Engineering

by  
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## ABSTRACT

### UHF RFID Antenna Impedance Matching Techniques

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Radio Frequency Identification (RFID) systems use electromagnetic signals to wirelessly identify and track RFID-tagged objects. A reader transmits a carrier wave request signal to an RFID tag, which then transmits a unique identification signal back to the reader. Applications include supply chain inventory management, automated toll booth fee systems, sports event timing, restricted access control, pet monitoring and retail theft prevention. An RFID tag includes an antenna connected to a Radio Frequency Integrated Circuit (RFIC). RFID tags in the ultra-high frequency (UHF), industrial, scientific and medical (ISM) 902-928MHz band and global Electronic Product Code (EPC) 860-960MHz band are powered passively (power extracted from carrier wave) and cost less than 15 cents per tag. Low cost UHF ISM RFID tags are an effective solution for tracking large inventories. UHF ISM tag antennas are typically planar dipoles printed onto a plastic dielectric substrate (inlay). Power exchange and transmit range is maximized when a tag antenna's input impedance is conjugate matched to the RFIC input impedance. Since RFIC input impedance includes capacitive reactance, optimized antenna input impedance includes compensating inductive reactance.

The T-match network (Figure 0-1) [2] adds inductive matching microstrips to conjugate match the RFIC. Narrowband ( $\pm 1.5\%$  of center frequency) and broadband ( $\pm 5\%$  of center frequency) lumped element designs [3] also use inductive matching strips. Narrowband, lumped element design is accomplished through Smith Chart matching assuming lumped antenna elements. The broadband lumped element design is accomplished through a circuit transformation to an equivalent network and tuning the transformed circuit to resonate from 865MHz to 955MHz, with a center frequency of 910MHz.



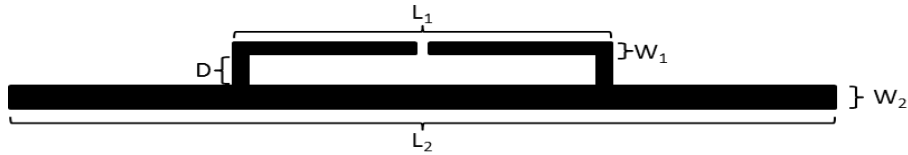


Figure 0-1. T-Match Network

This thesis demonstrates a start-to-finish design process for narrow ( $\pm 1.5\%$  of center frequency) and broadband ( $\pm 5\%$  of center frequency) RFID tag antennas [3].

Furthermore, antenna matching element geometries are parametrically swept to characterize input impedance frequency response. Thesis accomplishments include (a) narrow and broadband antenna designs, (b) Keysight's Advanced Design System (ADS) Momentum simulations, (c) antenna fabrication, and (d) differential probe impedance setup and antenna impedance measurements. Additional items include (e) impedance adjustments (f) tag range testing and (g) narrow vs. broadband matching technique comparisons. Antennas were fabricated in Cal Poly's Graphic Communication Department by silk-screening silver conductive ink onto DuPont Melinix Polyethylene Terephthalate (PET) plastic. Impedance simulations are compared to fabricated antenna impedance measurements and range testing results.

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## 1. INTRODUCTION

### 1.1: Ultra High Frequency (UHF) RFID Introduction

Radio Frequency Identification (RFID) is a wireless identification and tracking system. Applications include manufacturing supply chain inventory tracking, animal tracking, retail theft prevention, toll fees and sporting event timing. An RFID system includes a reader that tracks one or more tags (i.e.: transponders). The reader wirelessly transmits a carrier wave (uplink) to the tag (Figure 1-1a). A tag is a Radio Frequency Integrated Circuit (RFIC) affixed to a portable antenna. The tag backscatters a unique identification signal (downlink) to the reader (Figure 1-1b).

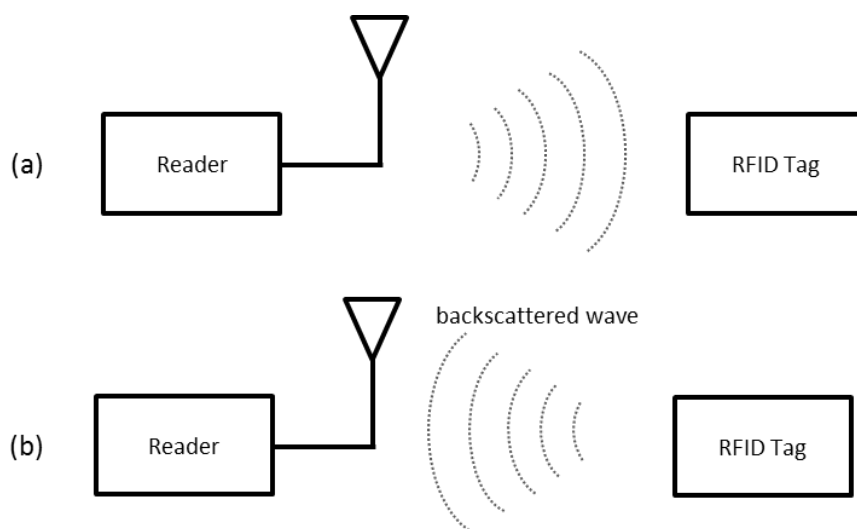


Figure 1-1: (a) Reader Transmitting Carrier to Tag, (b) Tag Backscattering to Reader

RFID tags are classified into three types: active, semi-active and passive. An active tag is battery powered and periodically transmits identification information without a reader's request. A semi-active tag transmits a battery-powered identification signal at the reader's request. A passive tag (Figure 1-2) uses the reader's carrier wave to power the RFIC and transmit the backscatter signal. Passive tags include a passively powered RFIC attached to an antenna. The antenna is printed onto a dielectric substrate; an "inlay." Passive RFICs require  $6\mu\text{W}$ - $30\mu\text{W}$  to operate internal circuitry. Read range is limited to  $\sim 4\text{m}$  while maintaining reader transmit power levels within  $4\text{W}$  EIRP Federal Communications Commission (FCC) regulations. By comparison, semi-active and

active tags communicate over a 100m read range. However, passive tags are cost effective (5 to 15 cents per tag) and disposable. Semi-active tags cost \$10 to \$50 and active tags cost \$15 to \$100 per tag. This thesis focuses on passive tags operating in the ultra-high frequency (UHF), industrial, scientific and medical (ISM) 902MHz-928MHz band and the global Electronic Product Code (EPC) 860MHz-960MHz band.

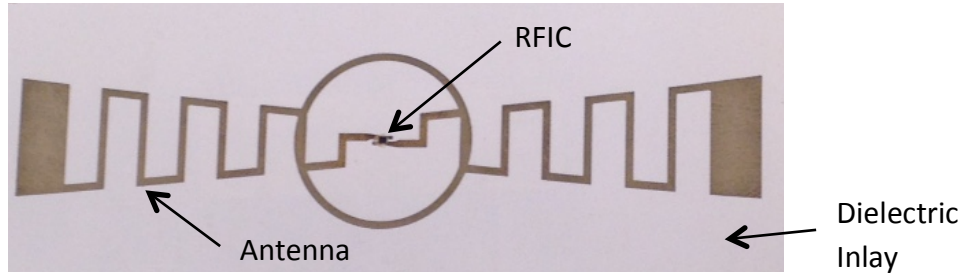


Figure 1-2: RFID Tag: Antenna RFIC and Inlay

## 1.2: Maximum Transmit Power

Power transfer is maximized to a finite, reactive load when the generator input impedance equals the load impedance complex conjugate [4] (Figure 1-3). For passive tags, the generator and load impedances correspond to the antenna and RFIC, respectively.

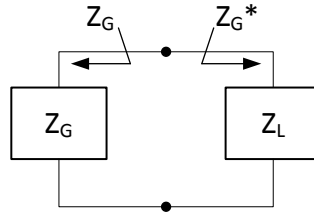


Figure 1-3: Maximum Power Transfer Theorem

Antenna input impedance is denoted  $Z_A = R_A + jX_A$  where  $R_A$  and  $X_A$  are the input resistance and reactance, respectively. RFIC input impedance is  $Z_{IC} = R_{IC} + jX_{IC}$ . The antenna generator, modeled as a Thevenin equivalent circuit, and RFIC load are shown in Figure 1-4. Circuit current  $I$  is [5]:

$$I = \frac{V_A}{(R_A + R_{IC}) + j(X_A + X_{IC})} \quad (1-1)$$

The maximum power delivered to the RFIC load (when  $Z_{IC} = Z_A^*$ ) is:

$$P_{MAX} = \frac{|I|^2 |Z_A|^2}{8 R_A} = \frac{|V_A|^2}{8 R_A} \quad (1-2)$$

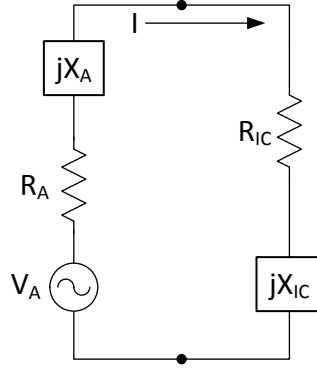


Figure 1-4: Thevenin Equivalent Generator  $V_A$  and Antenna Impedance  $Z_A$

The actual power delivered to the RFIC is:

$$P_{IC} = \frac{1}{2} |I|^2 R_{IC} = \frac{1}{2} \frac{|V_A|^2}{[(R_A + R_{IC})^2 + (X_A + X_{IC})^2]} R_{IC} \quad (1-3)$$

Power transmission coefficient 'τ' is the ratio of actual RFIC delivered power ( $P_{IC}$ ) to the maximum power ( $P_{MAX}$ ) [5]:

$$\tau = \frac{P_{IC}}{P_{MAX}} = \frac{4R_A R_{IC}}{[(R_A + R_{IC})^2 + (X_A + X_{IC})^2]} \quad (1-4)$$

Using the identity:

$$4R_A R_{IC} = |Z_{IC} + Z_A|^2 - |Z_{IC} - Z_A^*|^2 \quad (1-5)$$

and substituting (1-2), (1-3) and (1-5) into (1-4) yields:

$$\tau = 1 - \left| \frac{Z_{IC} - Z_A^*}{Z_{IC} + Z_A} \right|^2, \quad 0 \leq \tau \leq 1 \quad (1-6)$$

Power that is reflected back to the antenna does not reach the RFIC. Reflected power ( $P_{REFL}$ ) is:

$$P_{REFL} = P_{MAX} - P_{IC} \quad (1-7)$$

The power reflection coefficient  $\theta$  is the ratio of reflected power ( $P_{REFL}$ ) to the maximum power available to the RFIC ( $P_{MAX}$ ):

$$\theta = \frac{P_{REFL}}{P_{MAX}} = 1 - \tau \quad (1-8)$$

Substituting the  $\tau$  coefficient from (1-6) into (1-8), the power reflection coefficient is:

$$\theta = \left| \frac{Z_{IC} - Z_A^*}{Z_{IC} + Z_A} \right|^2, \quad 0 \leq \theta \leq 1 \quad (1-9)$$

Voltage reflection coefficient  $s_r$  for lumped element and non-traveling wave interfaces, originally introduced by [6], is the square root of the power reflection coefficient:

$$s_r = \frac{Z_{IC} - Z_A^*}{Z_{IC} + Z_A}, \quad 0 \leq s_r \leq 1 \quad (1-10)$$

Voltage reflection coefficient  $s_r$  for lumped elements should not be confused with the well-known voltage traveling wave reflection coefficient gamma ( $\Gamma$ ). The reflection coefficient  $s_r$  is used at lumped element interfaces (see Figure 1-3).  $\Gamma$  quantifies reflections when a wave traveling on a transmission line (a controlled impedance environment) encounters either another transmission line or a mismatched lumped element load.

### 1.3: Maximum Transmit Range

Maximum transmit range  $R$  (Figure 1-5) is calculated via the Friis Equation [5]. This relation for free space environments is:

$$P_R = P_T G_T G_R L (1 - |s_r|^2) \left( \frac{\lambda}{4\pi R} \right)^2, \quad (1-11)$$

where  $P_R$  is received power,  $P_T$  is transmitted power,  $G_R$  is receiver antenna gain,  $G_T$  is transmitter antenna gain,  $L$  is cable loss and  $\lambda$  is operating frequency wavelength.

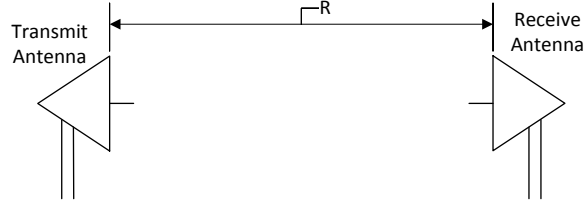


Figure 1-5. Friis Equations System Configuration

Read range is:

$$R = \frac{\lambda}{4\pi} \sqrt{\frac{P_T G_T G_R L (1 - |s_r^2|)}{P_R}} \quad (1-12)$$

Tag read operations require two transmission modes - uplink and downlink. In uplink mode, the tag receives the reader signal. In downlink mode, the tag transmits back to the receiver. Minimum required RFID reader received power is -60dBm; -15dBm to -22.5dBm for the RFIC. Hence, the uplink limits transmit distance. RFID tag antennas are low gain (<2dB), omnidirectional dipoles to minimize read range dependency on tag orientation. Transmit antennas are circularly or linearly polarized patch antennas with gain ranging 5-9dB. Maximum transmit power  $P_T$  is limited by the FCC to 4W Equivalent Isotropic Radiated Power (EIRP) to minimize electrical interference in nearby electrical systems. EIRP is defined as [5]:

$$EIRP = P_T G_T L \quad (1-13)$$

Maximum read range in terms of EIRP is [5]:

$$R = \frac{\lambda}{4\pi} \sqrt{\frac{EIRP * G_R (1 - |s_r^2|)}{P_R}} \quad (1-14)$$

Three parameters available to antenna designers to maximize transmit range include antenna efficiency, impedance matching, and RFIC sensitivity. This thesis focuses on RFIC/antenna impedance matching and RFIC sensitivity.

## 1.4: Passive RFIC Load Impedance

Passive RFICs use CMOS circuitry. The simplified block diagram (Figure 1-6) shows a top-level RFIC design. Three circuit blocks at the RFIC's input operate sequentially: (1) The reader transmits a continuous wave (CW) carrier wave, which is rectified and regulated in the RFIC to power its CMOS circuitry,  $V_{dd}$ . (2) The reader transmits an Amplitude Shift Key (ASK) request which is detected by the RFIC's envelope detector and transferred to the logic unit. (3) Finally, the logic unit transmits a unique identification code that is backscattered through the RFIC's ASK modulator. Both the full-wave rectifier and envelope detector have capacitive input impedances; however, the full-wave rectifier has the largest capacitive reactance ( $-j140\Omega$  to  $-j205\Omega$  for RFICs manufactured by Impinj [7] and Alien [8] [9]).

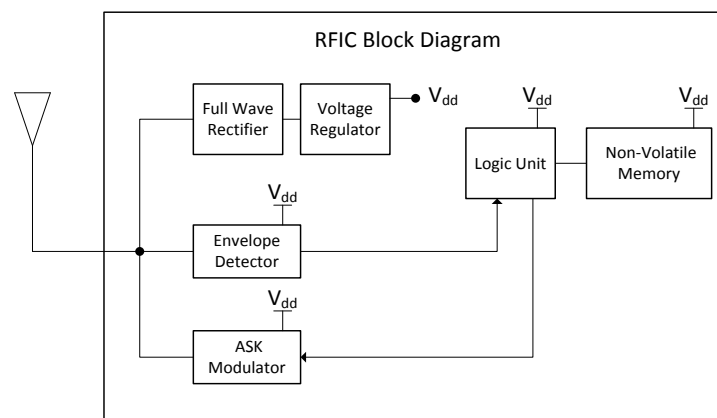


Figure 1-6: RFIC Block Diagram

The equivalent load is a lumped element, shunt resistor and capacitor (Figure 1-7a). Example RFIC input impedances are listed in Table 1-1. Antenna designs require series inductive reactance to conjugate match the RFIC's input capacitance.

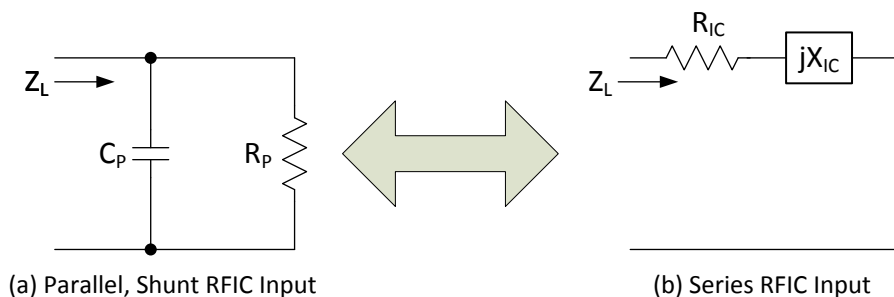


Figure 1-7: RFIC Input Impedance, (a) Norton Equivalent, (b) Thevenin Equivalent

Table 1-1: Passive RFIC Input Impedance at 915MHz

<b>Passive RFIC Input Impedance</b>				
RFIC	Vendor	Shunt Resistance ( $\Omega$ )	Shunt Capacitance (pF)	Total Impedance ( $\Omega$ )
Monza 4	Impinj	1650	1.21	12-j142
Monza 5	Impinj	1800	1.07	15-j161
Higgs-3	Alien Technology	1500	0.85	27-j200
Higgs-4	Alien Technology	1800	0.95	18-j181

### 1.5: Bandwidth Requirements

The 902MHz-928MHz range is an unlicensed FCC ISM band with maximum allowable 4W EIRP transmit power. Most countries adhere to RFID bands (Table 1-2) within the global Electronic Product Code (EPC) regulatory 865MHz-955MHz band. A commercially viable worldwide tag requires a 90MHz bandwidth (input return loss > 10dB).

Table 1-2: RFID UHF Global Bands [10]

<b>RFID UHF Global Bands</b>		
<b>Country</b>	<b>Frequency (MHz)</b>	<b>Power</b>
Australia	920.0-926.0	4W EIRP
Brazil	902.0-928.0	4W EIRP
Canada	902-928.0	4W EIRP
Central America	Typically USA Compliant	
USA	902.0-928.0	4W EIRP
China	920.5-924.5	2W EIRP
EU	865.6-867.6	2W ERP
Israel	915.0-917.0	2W EIRP
Japan	952.0-956.4	4W EIRP
Korea	917.0-920.8	4W EIRP
Mexico	902.0-928.0	4W EIRP
North Africa	Typically EU Compliant	
Russia	916.0-921.0	1W ERP
Saudi Arabia	865.6-867.6	2W ERP
South Africa	915.4-921.0	4W EIRP

## 1.6: Antenna Silk-Screen Printing Method

Passive RFID tag commercial advantages include low manufacturing cost; less than 15 cents per tag. One low-cost RFID antenna manufacturing method is silk-screening, which uses a permeable screen mesh to transfer ink onto a substrate. A mask is printed onto the screen to maintain the ink within the mask outline. The silk-screening process is chosen because it is simple, materials are affordable, and inexpensive equipment can be used. Silk-screening is an additive method; the conductor is added to the substrate, rather than removed. Additive methods reduce conductor cost. This project's antenna fabrication method uses silk-screening Dupont 5025 silver ink (sheet resistivity is 12-15m $\Omega$ /sq/mil) onto on Dupont Melinex® ST504 polyethylene terephthalate (PET) film (sheet thickness is 150 $\mu$ m and dielectric constant  $\epsilon_r=2.9$ ) (Figure 1-8). The printed silver thickness is approximately 1mil. Antennas are printed at Cal Poly's Graphic Communication Department. Commercial silver ink cost is ~\$1 per gram; a printed antenna requires approximately 1mg of ink. Silk-screening accuracy is within 100 $\mu$ m; sufficient for RFID antenna designs. PET film is also readily available.

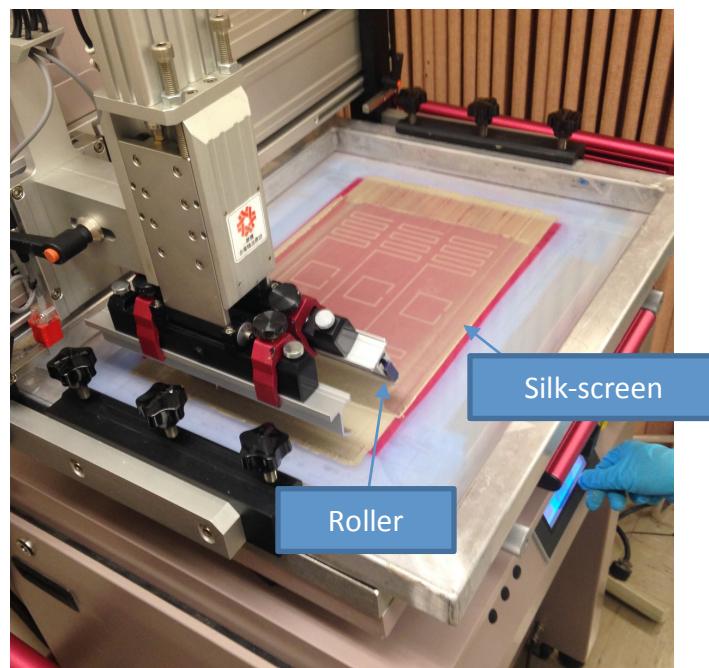


Figure 1-8: ATMA Semi-Auto Screen Printing Press, Roller and 305 Mesh Polyester Silk-Screen on an Aluminum Frame at the Cal Poly Graphic Communications Lab



## 2. UDA MODEL

The T-match antenna was originally designed by Uda [2] as an impedance matching technique for the driving element of a Yagi-Uda antenna array. It is now predominantly used as a conjugate matching method in RFID and microstrip designs. The matching network is tuned by adjusting the antenna geometry defined in Figure 2-1. An important assumption for the impedance derivation is that widths  $W_1$  and  $W_2$  and spacing  $D$  between conductors are electrically small ( $< \lambda/10$ ). Uda's T-match analysis is described in this chapter.

Uda's original T-match and modern day inductive matching networks are similar but the latter can include complex geometries beyond Uda's analysis methods. Figure 2-1 displays the traditional T-match geometry – two parallel conductors  $W_1$  and  $W_2$  (conductors can have differing widths) with a set gap  $D$  between the two structures. For Uda's analysis method gap  $D$  is electrically small ( $D < \lambda/10$ ). Figure 2-2 shows Alien's Squig™ antenna matching network, which cannot be analyzed with Uda's analysis methods because the conductors are neither parallel nor electrically close. Designs similar to Figure 2-2 are called “modified T-match” or inductive loop. Modified T-match analysis is presented in Chapter 3.

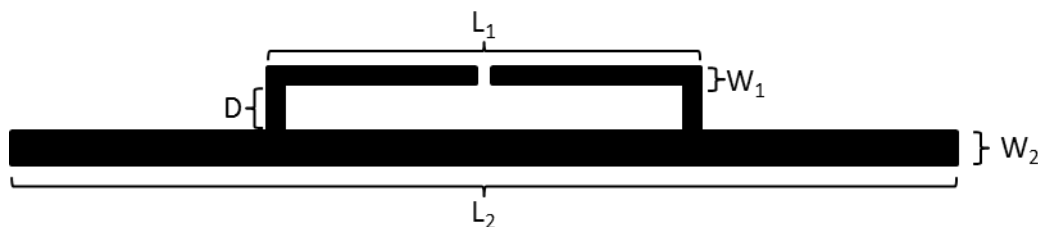


Figure 2-1: T-Match Geometry

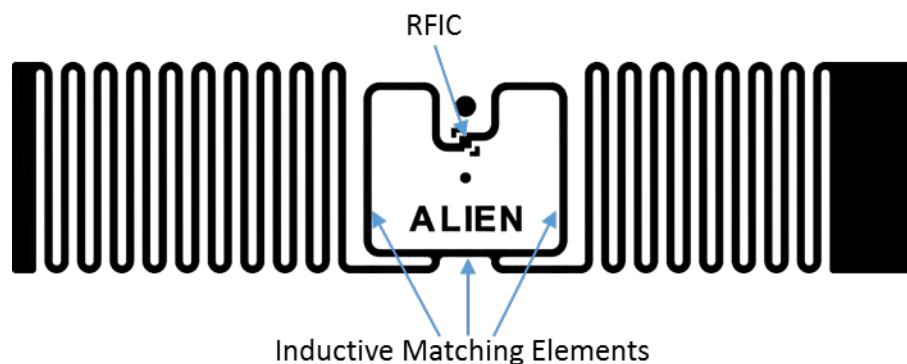


Figure 2-2: Alien Squig™ Inductive Shunt Series Matching Network

## 2.1: T-Match Antenna Input Impedance Using Even/Odd Mode Analysis

The T-match antenna input impedance, similar to the folded dipole, is derived by analyzing even and odd modes. Even-odd mode analysis is the decomposition of electrical networks into super-imposed in-phase and out-of-phase voltage and current excitations to determine the overall circuit response, see Figure 2-3. In even mode, elements are driven by in-phase signals. In odd mode, elements are driven with out-of-phase signals. Opposing voltage sources on the even and odd mode dipole arms cancel when added together, yielding the original circuit. Hence, even and odd-mode dipole arm voltages must be equal magnitude but opposite polarity. The even mode is also represented as (d) the radiating antenna mode, while (e) the non-radiating transmission line matching network represents the odd mode.

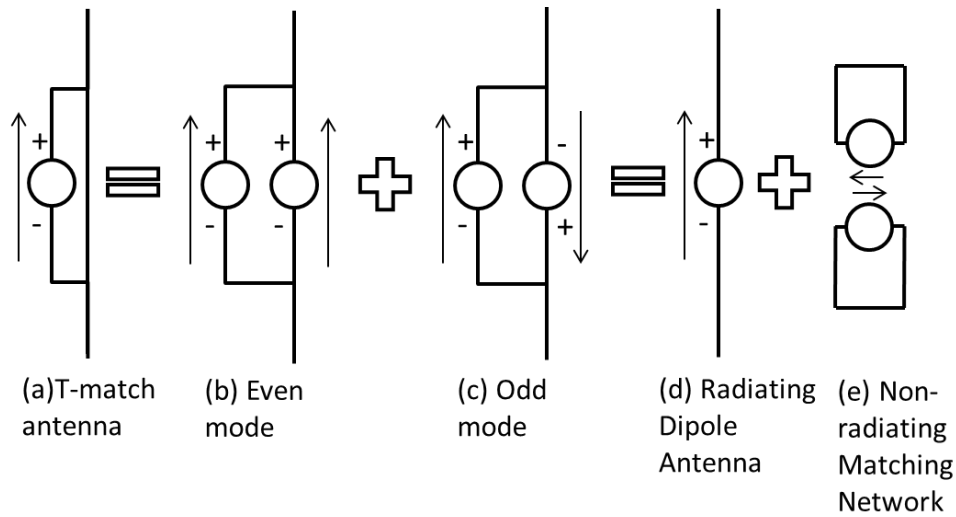


Figure 2-3: T-Match Modes

Even mode branch currents depend on dimensions  $W1$ ,  $W2$  and  $D$  through splitting factor  $\alpha$ :

$$I_{e2} = \alpha I_{e1} \quad (2-1)$$

where  $I_{e1}$  and  $I_{e2}$  are currents through the upper matching network and lower antenna element of Figure 2-4, respectively. The splitting factor  $\alpha$  is discussed in Section 2.2. The two even mode voltage sources are identical to satisfy even and odd mode

superposition. The equivalent circuit (redrawn in Figure 2-5a) uses a single voltage supply  $V_e = V_{e1} = V_{e2}$ . Adding currents  $I_{e1}$  and  $I_{e2}$ , where  $I_{e1} = I_e$  due to symmetry, Ohm's law is used to define even mode impedance  $Z_e$ :

$$V_e = Z_e(I_{e1} + I_{e2}) = Z_e(1 + \alpha)I_e \quad (2-2)$$

The even mode impedance  $Z_e$  is the radiating dipole input impedance. For a  $0.475\lambda$  length cylindrical dipole,  $Z_e$  is approximately  $73\Omega$ .

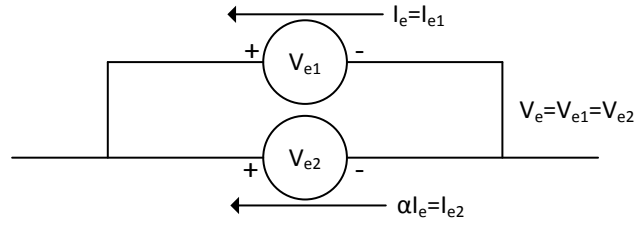


Figure 2-4: Even Mode Circuit

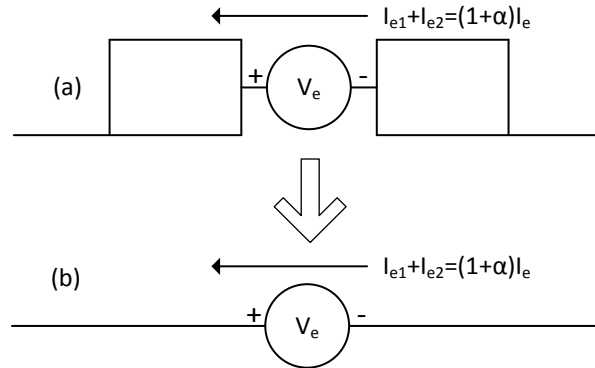


Figure 2-5: (a) Even Mode Circuit Redrawn and (b) Radiating Antenna Equivalent

For odd (transmission line) mode (Figure 2-6a), the two branch currents have equal magnitude and opposite polarities,  $I_{o1} = I_o$  and  $I_{o2} = -I_o$ , form a closed loop. To satisfy conservation of power in even-odd mode analysis,  $V_{e2} + V_{o2} = 0$ . Odd mode voltage sources are related by splitting factor  $\alpha$ . From (D.13):

$$V_{o1} = -\alpha V_{o2} = \alpha V_o \quad (2-3)$$

Each odd-mode, the voltage source is divided into halves (Figure 2-6b) to form an equipotential line on the circuit centerline (Figure 2-6c). The left-half circuit voltage sources  $V_{o1}/2$  and  $V_{o2}/2$  are subtracted to form a single voltage source (Figure 2-7).  $Z_o$

is the odd-mode input impedance into the transmission line.  $Z_{char}$  is the characteristic impedance along the transmission line. The odd-mode circuit is a closed loop with short-circuit termination  $Z_L = 0$ . Ohm's law is applied to determine  $Z_o$ :

$$Z_o I_o = \frac{V_{o1} - V_{o2}}{2} = \frac{(1 + \alpha)V_o}{2} \quad (2-4)$$

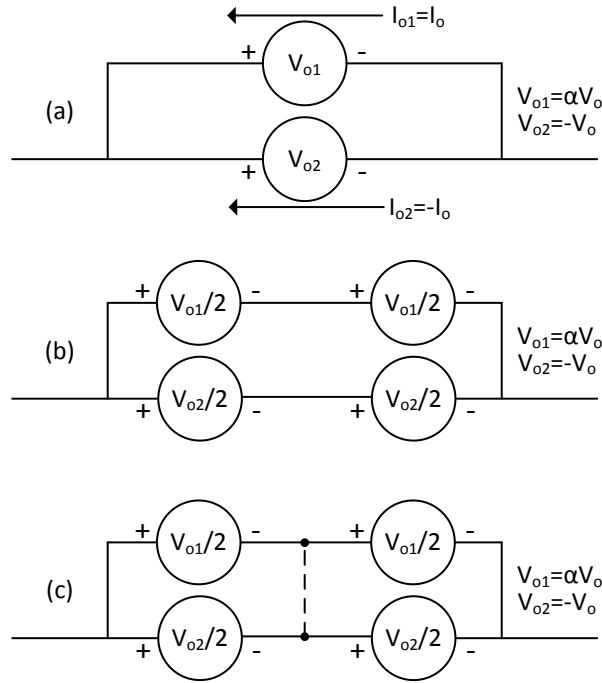


Figure 2-6: (a) Odd-mode Circuit, (b) Circuit Redrawn with Divided Sources, (c) Circuit with Equipotential Line between Sources

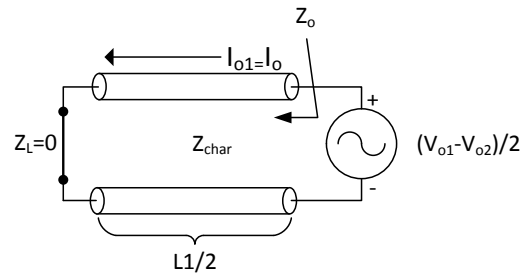


Figure 2-7. Transmission Line Model for Half of Odd-Mode Impedance

The translated input impedance for a lossless transmission line is:

$$Z_{lossless} = Z_{char} \frac{Z_L + jZ_{char} \tan(\beta l)}{Z_{char} + jZ_L \tan(\beta l)} \quad (2-5)$$

where characteristic impedance  $Z_{char}$  is derived using asymmetric coplanar strips defined in Appendix A,  $\beta$  is the wave number ( $2\pi/\lambda$ ), where  $\lambda$  is wavelength,  $l = L_1/2$  is the transmission line length and  $Z_L = 0$  is the load impedance. The odd mode impedance is:

$$Z_o = jZ_{char} \tan\left(\beta \frac{L_1}{2}\right) \quad (2-6)$$

Total voltages and currents (Figure 2-8c) for the matching element ( $V_{T1}$ ,  $I_{T1}$ ) and dipole elements ( $V_{T2}$ ,  $I_{T2}$ ) are derived by adding even and odd mode parameters:

$$V_{T1} = V_{e1} + V_{o1} = V_e + \alpha V_o, \quad (2-7)$$

$$V_{T2} = V_{e2} + V_{o2} = V_e - V_o = 0, \quad (2-8)$$

$$I_{T1} = I_{e1} + I_{o1} = I_e + I_o, \quad (2-9)$$

$$I_{T2} = I_{e2} + I_{o2} = \alpha I_e - I_o \quad (2-10)$$

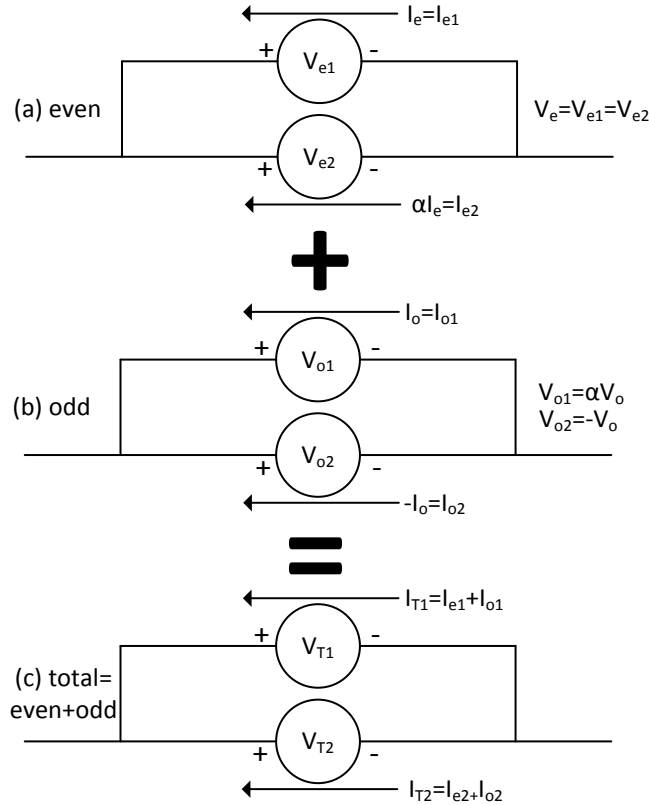


Figure 2-8: (a) Even Modes, (b) Odd Modes, (c) Total Modes

Input impedance is derived by applying Ohm's law to the matching network elements.  
Using  $V_e=V_o$  from (2-8) the input impedance is:

$$Z_{in} = \frac{V_{T1}}{I_{T1}} = \frac{V_e + \alpha V_o}{I_e + I_o} = \frac{(1+\alpha)V_e}{I_e + I_o}. \quad (2-11)$$

Substituting (2-2) and (2-4) into (2-11), and using (2-8), the input impedance is:

$$Z_{in} = \frac{(1+\alpha)^2 Z_e I_e}{I_e + \frac{(1+\alpha)V_o}{2Z_o}} = \frac{2(1+\alpha)^2 Z_e Z_o I_e}{2Z_o I_e + (1+\alpha)V_o} = \frac{2(1+\alpha)^2 Z_e Z_o I_e}{2Z_o I_e + (1+\alpha)^2 Z_e I_e}. \quad (2-12)$$

The input impedance is:

$$Z_{in} = \frac{2(1+\alpha)^2 Z_e Z_o}{2Z_o + (1+\alpha)^2 Z_e} \quad (2-13)$$

This derivation resembles a product over sum of two impedances; hence, the input impedance can be represented as the parallel combination of  $Z_e$  and  $2Z_o$  through a transformer with turns ratio  $= 1+\alpha$ .

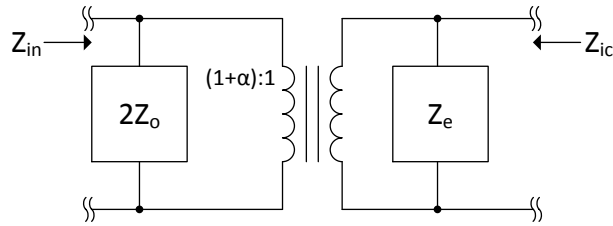


Figure 2-9: T-Match Antenna Equivalent Circuit

## 2.2: Splitting Factor

Uda [2] derives the splitting factor for cylindrical folded dipoles. The splitting factor  $\alpha$  is the even (radiating) mode ratio of currents in the matching and antenna elements, or the odd (transmission line) voltage ratio:

$$\alpha = \frac{I_{e2}}{I_{e1}} = -\frac{V_{o1}}{V_{o2}} = \frac{\frac{dQ_2}{dt}}{\frac{dQ_1}{dt}} \approx \frac{Q_2}{Q_1} \quad (2-14)$$

The splitting factor presented by [2] is:

$$\alpha = \frac{\cosh^{-1} \left[ \frac{D^2 - R_1^2 + R_2^2}{2DR_2} \right]}{\cosh^{-1} \left[ \frac{D^2 + R_1^2 - R_2^2}{2DR_1} \right]} \quad (2-15)$$

where  $D$  is the distance between the two conductors, and  $R_1$  and  $R_2$  are two the radii (Figure 2-10). Following extensive literature review, a derivation for (2-15) could not be found, although several papers use the relation. Reference [11] states that the splitting factor is derived from the capacitance of unequal cylinders derived in [12]. The capacitance between two unequal diameter conductors per unit length (see Appendix B) is:

$$C = 2\pi\epsilon_0 \left[ \cosh^{-1} \left( \frac{D^2 - R_1^2 - R_2^2}{2R_1R_2} \right) \right]^{-1} \quad (2-16)$$

This equation resembles, but is not identical to, the numerator and denominator terms of (2-15).

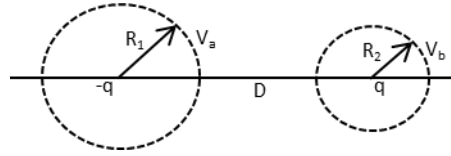


Figure 2-10: Two Parallel, Cylindrical Conductor Cross-Sections with Unequal Radii

A useful splitting factor expression for coplanar strips (Figure 2-11) is derived by comparing the charge ratios in the even antenna mode [13]:

$$\alpha = \frac{Q_2}{Q_1} = \frac{\ln\{4c + 2[(2c)^2 - (W_1/2)^2]\} - \ln\{W_1\}}{\ln\{4c + 2[(2c)^2 - (W_2/2)^2]\} - \ln\{W_2\}} \quad (2-17)$$

where  $c = \frac{1}{4}W_1 + \frac{1}{4}W_1 + \frac{1}{2}D$ .

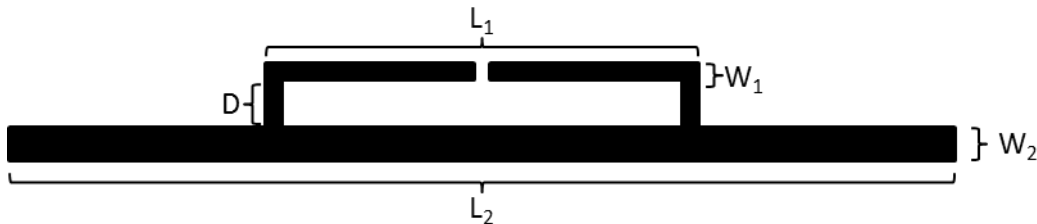


Figure 2-11: Top View of Asymmetric Coplanar Strip

The assumption that the current ratio is equivalent to the charge ratio is true only when the antenna is in a resonant mode. Otherwise, the currents have unequal phases, resulting in a complex splitting factor and inaccurate input resistance ( $\pm 20\Omega$ ) and reactance ( $\pm 50\Omega$ ) derivations [14] [15]. To the author's knowledge, there are no splitting factor derivations that consider non-zero phase differences.



### 3. INDUCTIVE LOOP NETWORK ANALYSIS

Narrow and broadband lumped-element impedance matching methods are applied to RFID antenna designs in [16]. Both techniques represent the dipole antenna, inductive matching strips and RFIC as lumped elements. A symmetric RFID tag is a balanced device with an equipotential line through its center [17] (Figure 3-1). For simplicity, the RFID tag is modeled by its individual components as an unbalanced device [16] (Figure 3-2): an RLC modeled dipole, series and shunt inductor matching elements and an RFIC load comprised of a shunt resistor and capacitor. The shunt inductor and IC load both connect to the equipotential symmetry line. The RLC band-pass filter response is curve-fit to the dipole self-impedance [18] to yield the RFIC equivalent load; a shunt capacitor and resistor.

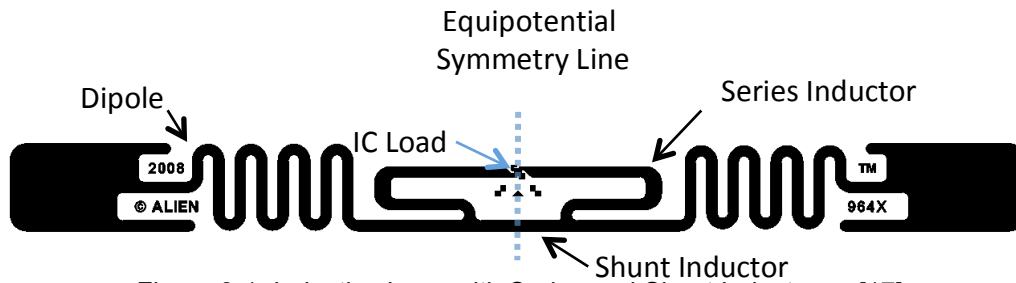


Figure 3-1: Inductive Loop with Series and Shunt Inductance [17]

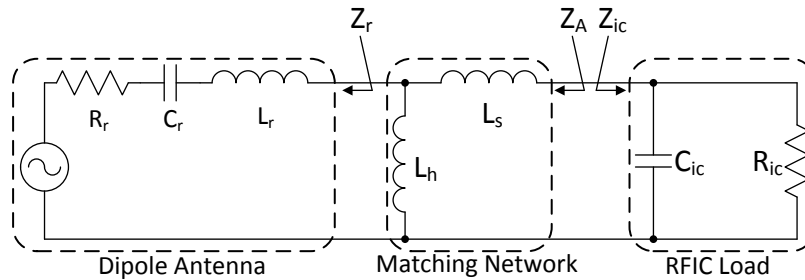


Figure 3-2: Lumped Element Matching Network for RFID Tag [16]

#### 3.1: Dipole Impedance: RLC Equivalent Circuit

The dipole antenna input-impedance is curve-fit to a series and shunt RLC circuit [18] (Figure 3-3). The model is simplified [17] to curve-fit the dipole antenna input impedance to a series RLC band-pass filter network (Figure 3-4).

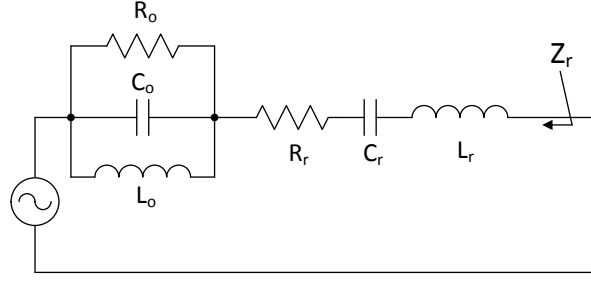


Figure 3-3: RLC Impedance Equivalent [18] Dipole Impedance as Lumped Element Circuit

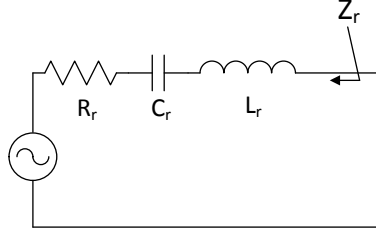


Figure 3-4: Simplified RLC Network; Dipole Reactance as Lumped Element Circuit

Input impedance  $Z_{in}$  for the simplified circuit is:

$$Z_r = R_r + j\omega L_r + \frac{1}{j\omega C_r} \quad (3-1)$$

The antenna resonant frequency  $f_r$  ( $\sim 0.475\lambda$  dipole length) follows an RLC filter:

$$f_r = \frac{1}{2\pi\sqrt{L_r C_r}} \quad (3-2)$$

Radiation resistance is assumed constant; deviation is less than 10% over the operating band. Figure 3-5 shows the RLC fit to simulated dipole reactance is valid only around the first resonance ( $0.4 < L/\lambda < 0.6$ ) where  $L$  is the antenna length and  $\lambda$  is the operating wavelength.

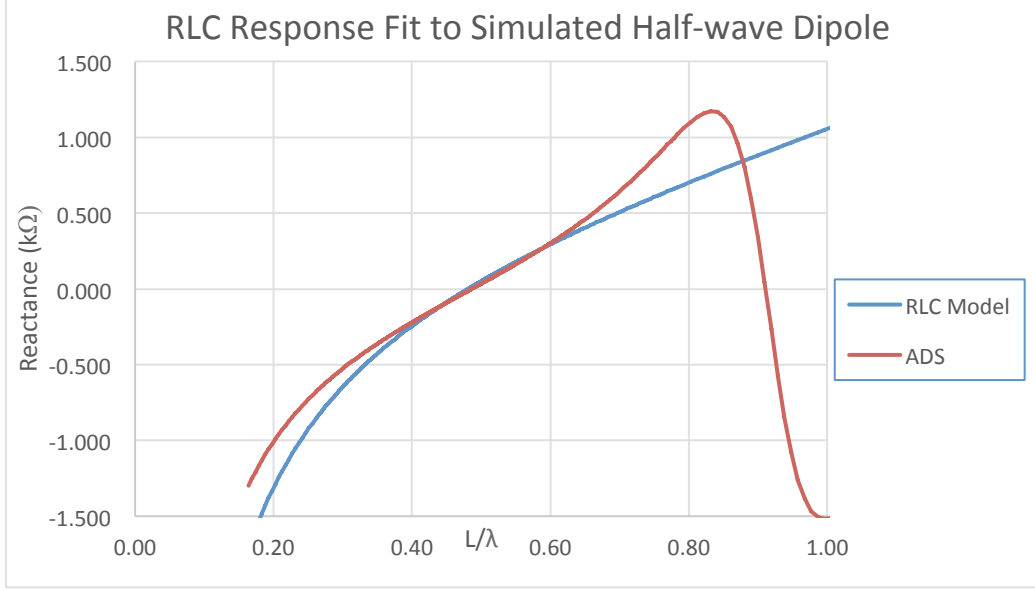


Figure 3-5: Series RLC Response Fit to ADS Simulated Dipole Reactance

Resistance  $R_r$  is approximated as [19]:

$$R_r = 80 \left( \frac{\alpha \pi L}{\lambda} \right)^2 \quad (3-3)$$

where  $L$  is the antenna length,  $\lambda$  is the operating frequency wavelength and  $\alpha$  is:

$$\alpha = \frac{1 - \cos\left(\frac{\pi L}{\lambda}\right)}{\frac{\pi L}{\lambda} \sin\left(\frac{\pi L}{\lambda}\right)} \quad (3-4)$$

The sine integral  $S_i(x)$  is:

$$S_i(x) = \int_0^x \frac{\sin(\tau)}{\tau} d\tau \quad (3-5)$$

The cosine integral  $C_i(x)$  is:

$$C_i(x) = - \int_x^\infty \frac{\cos(\tau)}{\tau} d\tau \quad (3-6)$$

Reactance  $X_r$  is solved by the Induced EMF Method[20]:

$$X_r = 60 \left\{ 2S_i(\beta L) + \cos(\beta L) [2S_i(\beta L) - S_i(2\beta L)] - \sin(\beta L) \left[ 2C_i(\beta L) - C_i(2\beta L) - C_i\left(\frac{2\beta a^2}{L}\right) \right] \right\} \quad (3-7)$$

where  $\beta$  is the wave number ( $2\pi/\lambda$ ), and  $a$  is dipole radius.

### 3.2: Narrowband Matching Design

Narrow band matching maximizes power transfer at the center design frequency. The goal is to tune the RFIC load to the antenna's complex conjugate input impedance. The matching network path is shown on a Smith Chart (Figure 3-7). The process is as follows:

- Plot the RFIC input impedance  $Z_{ic}$  at the center frequency. The final matching target is the antenna conjugate impedance  $Z_r^*$ .
- From  $Z_{ic}$ , add series inductance (clockwise rotation on the constant resistance  $R_{ic}$  circle) to reach the  $G_r$  conductance circle.
- Add shunt inductance (counter-clockwise rotation on the  $G_r$  constant conductance circle) to reach target impedance  $Z_r^*$ .
- Translate shunt and series inductances to microstrip geometry.

To convert between cylindrical radius  $a$  and planar antenna width  $W$ , [20] approximates  $a$ :

$$a = W/4 \quad (3-8)$$

Cylindrical conductor self-inductance is approximated [21]:

$$L = \frac{\mu_o}{2\pi} l \ln(l/a) = \frac{\mu_o}{2\pi} l \ln(4l/W) \quad (3-9)$$

where  $\mu_o$  is the permeability of free-space ( $\sim 4\pi \times 10^{-7} \text{N/A}^2$ ) and  $l$  is microstrip length.

The inductances define shunt and series inductor lengths in Figure 3-2. Shunt inductors are planar strip lengths from the dipole element center to the series inductor launch. Series inductors terminate at the IC pads.

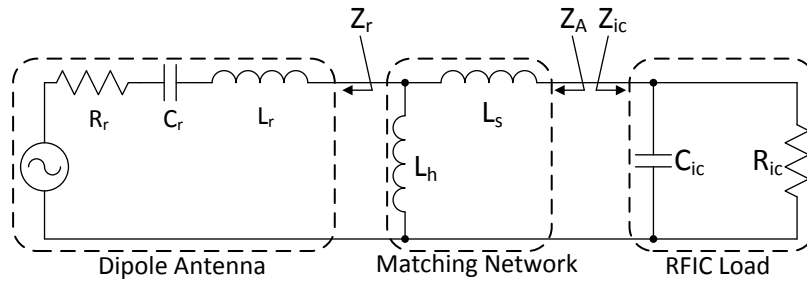


Figure 3-6: RFID Tag Lumped Element Matching Network

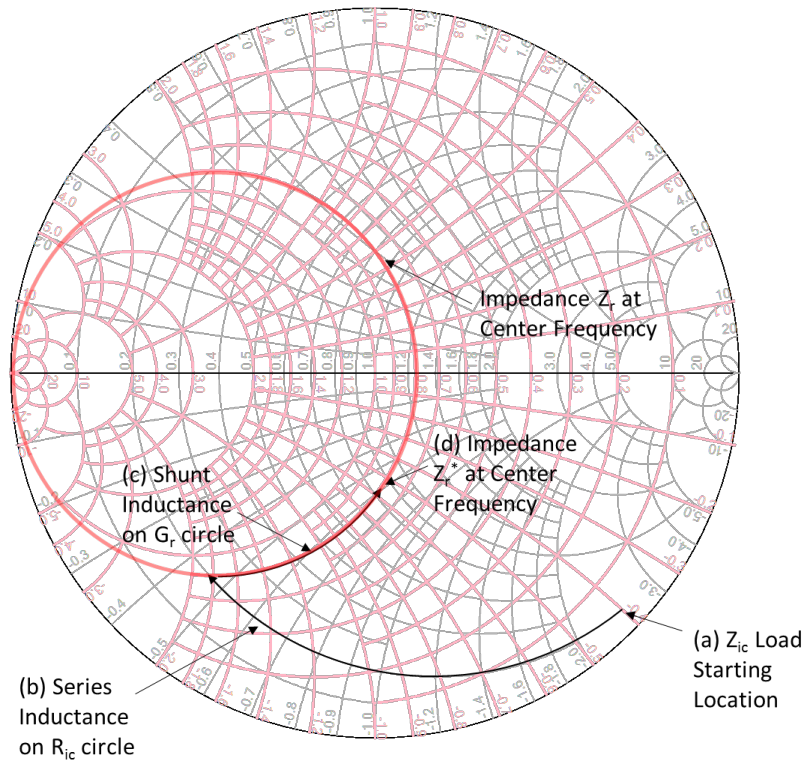


Figure 3-7: Smith Chart Impedance Matching; IC Load to Conjugate Dipole Impedance

### 3.3: Broadband Matching Design

The narrowband design tunes for center frequency matching; however, matching ( $|S_{11}| < -10\text{dB}$ ) over the 90MHz bandwidth is compromised. A transformed matching network and load present an alternative circuit (Figure 3-8), which includes adjacent series and parallel RLC band-pass filters. Series inductors  $L_r$  and  $L_b$  are added to form a single inductor (Figure 3-9).

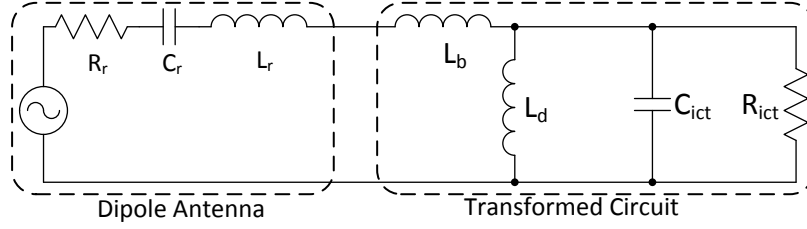


Figure 3-8: Transformed Lumped Element Matching Network, Broadband Match

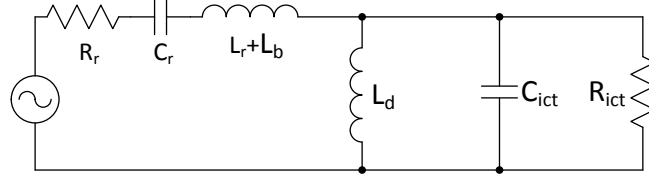


Figure 3-9: Lumped Element RLC Band-Pass Filter, Broadband Match

Define coefficient  $\beta$ :

$$\beta = \frac{L_h}{L_h + L_s} \quad (3-10)$$

Circuit transformations to convert the narrowband lumped element tag design (Figure 3-6) into a broadband lumped element tag design (Figure 3-8) are [16]:

$$Z_{ict} = \beta^2 Z_{ic} \quad (3-11)$$

$$L_d = \beta^2 (L_s + L_h) \quad (3-12)$$

$$L_b = \frac{L_s L_h}{L_s + L_h} \quad (3-13)$$

and are derived in Appendix E. Applying (3-11) to the parallel RC load (Figure 3-9), the shunt resistance and capacitance are:

$$R_{ict} = \beta^2 R_{ic} \quad (3-14)$$

$$C_{ict} = \frac{C_{ic}}{\beta^2} \quad (3-15)$$

The parallel shunt inductor  $L_d$  and capacitor  $C_{ict}$  resonant frequency is:

$$f = \frac{1}{2\pi\sqrt{L_d C_{ict}}} \quad (3-16)$$

Inserting (3-12) and (3-15) into (3-16), the resonant frequency is:

$$f = \frac{1}{2\pi\sqrt{C_{ict}(L_s + L_h)}} \quad (3-17)$$

Maximum bandwidth occurs at minimum frequency difference  $\omega_2 - \omega_1$ :

$$\Delta\omega = \omega_2 - \omega_1 \quad (3-18)$$

The center angular frequency  $\omega_o$  is the minimum and maximum geometric mean:

$$\omega_o = \sqrt{\omega_1 \omega_2} \quad (3-19)$$

The resistance mismatch between  $R_r$  and  $R_{ict}$  determines the maximum center band reflection coefficient. The transformed circuit's target resistance  $R_{ict}$  is:

$$R_{ict} = R_r \frac{1 + \Gamma}{1 - \Gamma} \quad (3-20)$$

where  $\Gamma = 10^{-|S_{11}|/20}$ . To complete the design, solve for either  $C_r$  or  $L_r$ . The transformed series RLC circuit resonant frequency is:

$$\omega_o^2 = \frac{1}{C_r(L_r + L_b)} \quad (3-21)$$

Rearranging (3-21):

$$\omega_o^2 C_r L_r + \omega_o^2 C_r L_b - 1 = 0 \quad (3-22)$$

RLC quality factor  $Q_r$  is:

$$Q_r = \frac{\omega_o}{\Delta\omega_r} = \frac{1}{R_r} \sqrt{\frac{L_r}{C_r}} \quad (3-23)$$

Forming a quadratic equation in  $Q_r$  and substituting into (3-22) yields:

$$L_r^2 + L_r L_b - \left( \frac{Q_r R_r}{\omega_o} \right)^2 = 0 \quad (3-24)$$

Solving the quadratic equation for  $L_r$ , positive values only:

$$L_r = \frac{1}{2} \left( \sqrt{L_b^2 + 4 \left( \frac{Q_r R_r}{\omega_o} \right)^2} - L_b \right) \quad (3-25)$$

which is expressed as [16]:

$$L_r = \sqrt{+ \left( \frac{Q_r R_r}{\omega_o} \right)^2 + \left( \frac{(1 - \beta)(L_s + L_h)}{2\sqrt{2}} \right)^2} - \frac{(1 - \beta)(L_s + L_h)}{2\sqrt{2}} \quad (3-26)$$

$L_r$  yields  $C_r$  by LC resonance (3-2). Antenna quality  $Q_r$  factor is approximated as [22]:

$$Q_r \approx \frac{\omega_o}{2R_r} \frac{\Delta |X_r(\omega_o)|}{\Delta \omega_o} \quad (3-27)$$

where  $D|X(\omega_o)|/D\omega_o$  is evaluated by computing the slope of (3-7) over the range  $0.3 < \lambda < 0.6$  ( $\omega_o = 2\pi \cdot 3 \times 10^8 / \lambda$ ).

Design procedure:

- 1) Choose antenna length based on package size limitations. Solve  $R_r$  (3-3), (3-4).
- 2) Set  $|S_{11}|$  to -10dB (0.316). Calculate  $R_{ict}$  (3-20).
- 3) Find the center angular frequency (3-19).
- 4) The IC load shunt resistance  $R_{ic}$  is the stated shunt resistance on the RFIC datasheet. Find  $\beta$  (3-14).
- 5) Calculate  $L_s + L_h$  using (3-17);  $L_s$  and  $L_h$  from (3-10). Microstrip lengths are calculated from (3-9) by solving for  $l$ .
- 6) Find antenna quality factor  $Q_r$  (3-27).

Finally, find  $L_r$



7) (3-26). The dipole resonant frequency is determined from  $L_r$  and  $C_r$ . The dipole is designed to resonate at this frequency.

A method to design Meander Line Antennas to resonate at a specific frequency is shown in the next section.

### 3.4: Meander Line Antenna

RFID antennas are often size-limited to small packages; a 915MHz half-wave dipole (~16 cm) exceeds the maximum footprint for many applications. One solution is decreased antenna length; however, electrically short dipoles exhibit large capacitive input impedances. This increases required matching network shunt and series resistance lengths. A method for reducing dipole size, while maintaining positive reactance, is to meander the dipole length. A Meander Line Antenna (MLA) [23] is a dipole with vertical and parallel elements to add inductive reactance. The meander sections carry currents traveling in opposite directions and are electrically close ( $<\lambda/10$ ). Hence, meander element radiation is not seen in the far-field. The antenna, similar to a straight dipole, does not radiate on the z-axis (Figure 3-10). The MLA includes meander elements with width  $w$  and height  $h$ . Conductor radius is  $a$ , total length is  $s$  and  $M$  is the number of meander elements.

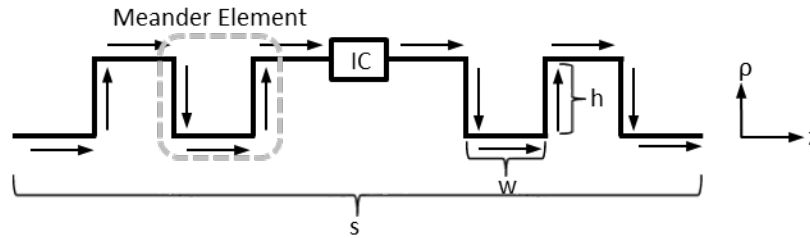


Figure 3-10: Current Distributions and Geometry, Meander Line Dipole

The meander elements  $M$  are approximated as short circuits. This approximation is valid if current magnitude deviates less than 5% over each meander element, which is satisfied when  $M \geq 14$  [24]. The reactance added by each meander section is:

$$X_m = 2\pi f L_m = Z_{char} \tan(\beta h) \quad (3-28)$$

Expanding the  $\tan(\beta h)$  MacLaurin Series in (3-28) to third order:

$$\tan(\beta h) \approx \beta h + \frac{1}{3}(\beta h)^3 \quad \text{for } (\beta h \ll 1) \quad (3-29)$$

The characteristic impedance  $Z_{\text{char}}$  of parallel cylindrical conductors is [23]:

$$Z_{\text{char}} = \frac{\eta_o}{\pi} \ln\left(\frac{2w}{a}\right) \quad (3-30)$$

where  $\eta_o$  is the intrinsic impedance of free-space. Substituting (3-29) and (3-30) into (3-28) yields the inductance of a single meander section:

$$L_m = \frac{\mu_o h}{\pi} \ln\left(\frac{2w}{a}\right) \left[1 + \frac{1}{3}(\beta h)^2\right] \quad (3-31)$$

The self-inductance of a straight conductor of length  $s$  is approximated as [23]:

$$L_s \approx \frac{\mu_o}{2\pi} s \left[ \ln \frac{4s}{a} - 1 \right] \quad (3-32)$$

The total MLA inductance is:

$$L_T = L_s + M L_m \quad (3-33)$$

where  $M$  is the number of meander elements. To determine the self-inductance of a half-wave dipole, substitute  $s=\lambda/2$  into (3-32):

$$L_{\lambda/2} = \frac{\mu_o}{\pi} \frac{\lambda}{4} \left[ \ln \frac{2\lambda}{a} - 1 \right] \quad (3-34)$$

It is assumed that the meander dipole and half-wave dipole have the same resonant frequency; hence, meander dipole  $L_T$  inductance and half-wave dipole  $L_{\lambda/2}$  are equal at resonance:

$$L_s + M L_m = L_{\lambda/2} \quad (3-35)$$

Substituting (3-31), (3-32) and (3-34) for the inductance terms in (3-35):

$$\frac{\mu_o}{2\pi} s \left[ \ln \frac{4s}{a} - 1 \right] + M \frac{\mu_o h}{\pi} \ln\left(\frac{2w}{a}\right) \left[1 + \frac{1}{3}(\beta h)^2\right] - \frac{\mu_o}{\pi} \frac{\lambda}{4} \left[ \ln \frac{2\lambda}{a} - 1 \right] = 0 \quad (3-36)$$

Given known antenna geometry values, the wavelength is determined using MATLAB. For a meander antenna with  $s=10\text{cm}$ ,  $w=2\text{mm}$ ,  $M=14$  and  $h=6\text{mm}$ , the resonant frequency is 910MHz. ADS simulation indicates 934 MHz; a 2.5% error.

## 4. MEASUREMENT TECHNIQUES

### 4.1: Patch Antennas for Range Testing and Sleeve Balun Verification

Air dielectric, probe-fed patch antennas (geometry shown in Figure 4-1) are used as RFID reader transmit and receive antennas and for sleeve balun verification (discussed in Section 4.2), due to simple construction and low cost. An approximation for patch antenna width  $W$  is [25]:

$$W = \frac{c}{2f\sqrt{\epsilon_r}} \quad (4-1)$$

where  $c$  is the speed of light ( $3 \times 10^8$  m/s) in vacuum,  $f$  is the patch antenna center frequency and  $\epsilon_r$  is the effective dielectric constant. The physical patch antenna length  $L$  is [25]:

$$L = \frac{c}{2f\sqrt{\epsilon_r}} + 2\Delta L \quad (4-2)$$

where  $\Delta L$  accounts for electric fields radiating from edge to ground plane (Figure 4-2). An approximation for  $\Delta L$  is [25]:

$$\Delta L = 0.412h \frac{(\epsilon_r + 0.3)(\frac{W}{h} + 0.264)}{(\epsilon_r - 0.258)(\frac{W}{h} + 0.8)} \quad (4-3)$$

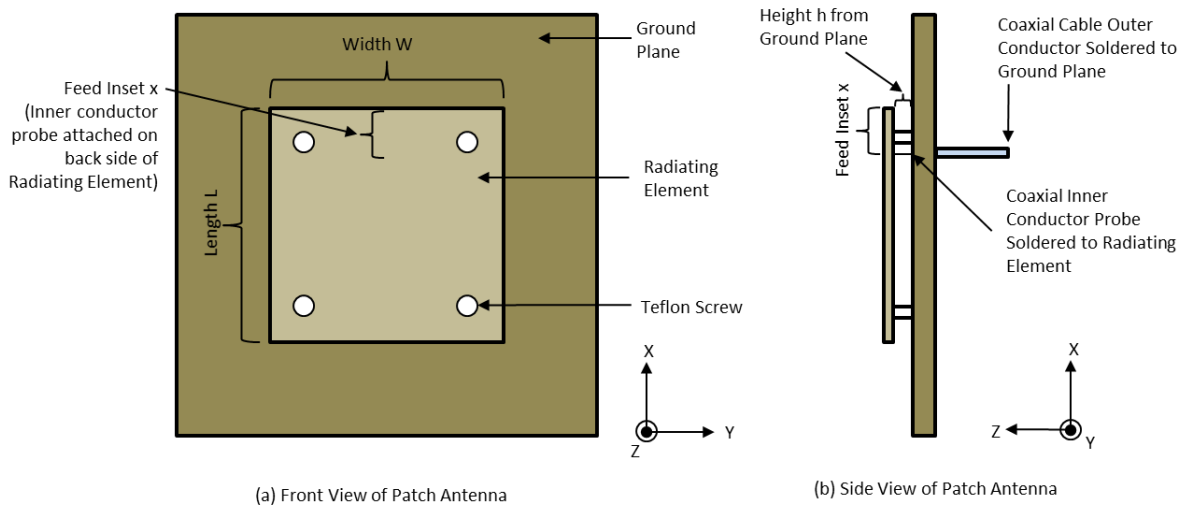


Figure 4-1: Air Dielectric, Probe Fed Patch Antenna Geometry: (a) Patch Antenna Front View, (b) Patch Antenna Side View

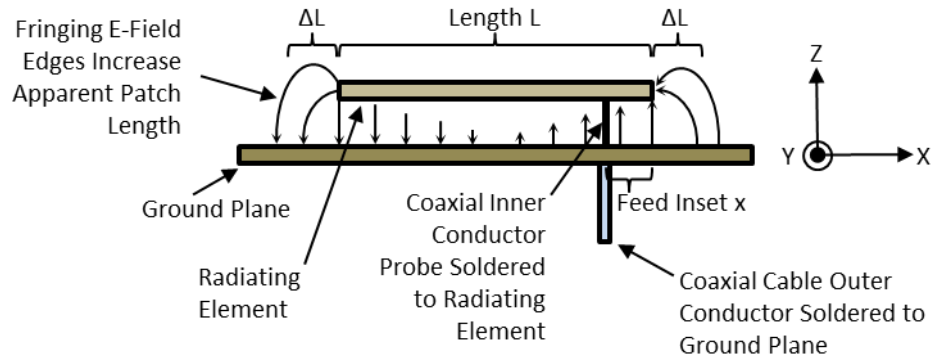


Figure 4-2: Patch Antenna to Ground Electric Fields and Fringing Areas

Two 915MHz air dielectric ( $\epsilon_r=1$ ), probe-fed patch antennas were fabricated (Figure 4-3) for RFID reader transmission and reception. Dimensions are defined in Table 4-1 and 50 $\Omega$  matching data  $|S_{11}|$  is shown Figure 4-4.

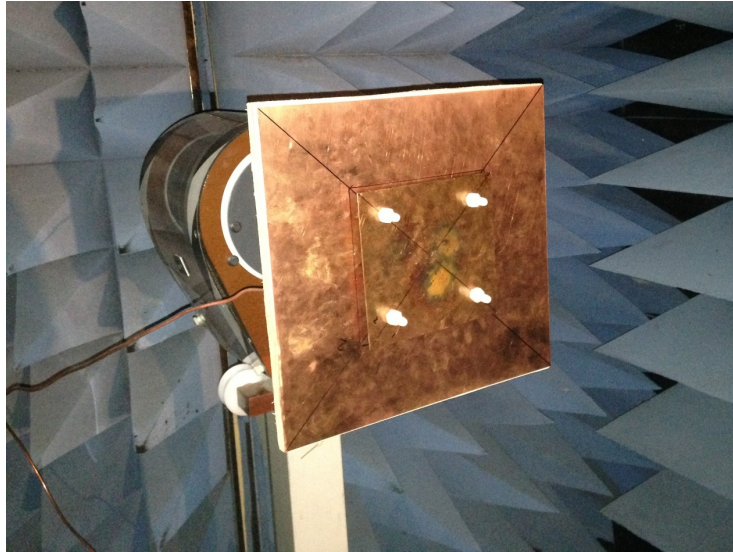


Figure 4-3: Air Dielectric Patch Antenna, 902MHz-928MHz

Table 4-1: Air Dielectric Patch Antenna (902MHz-928MHz) Dimensions

915MHz Air Dielectric Patch Antenna Dimensions		
Width $W$	145	mm
Length $L$	143	mm
Height from Ground Plane $h$	16	mm
Feed Inset Feed $x$	10	mm

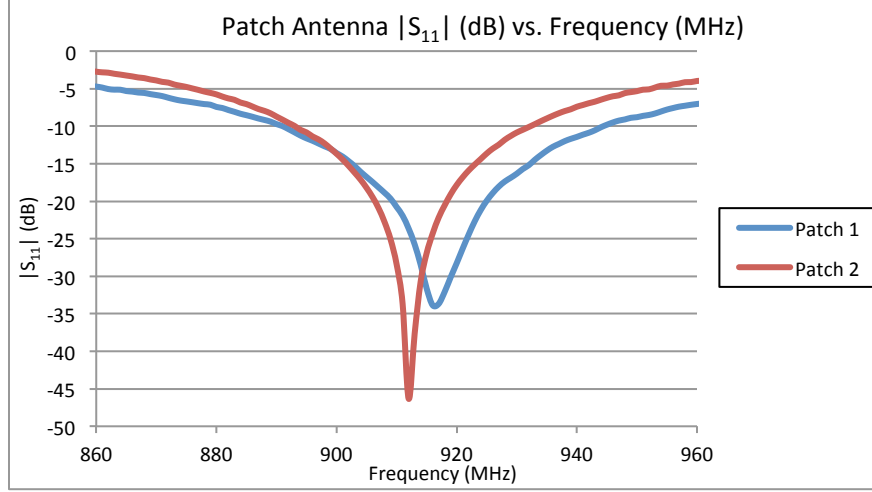


Figure 4-4: Air Dielectric Patch Antenna  $|S_{11}|$  (dB) vs. Frequency (MHz), 902MHz-928MHz

The Friis equation in decibels (dB, dBm) is [20]:

$$P_R = P_T + G_T + G_R + 20 \log_{10} \left( \frac{c}{4\pi R f} \right) \quad (4-4)$$

where  $P_R$  is received power (dBm),  $P_T$  is transmit power (dBm),  $G_T$  is transmit gain (dB),  $G_R$  is receive gain (dB),  $c$  is the speed of light in vacuum ( $3 \times 10^8$  m/s),  $R$  is antenna separation and  $f$  is the operating frequency. Antenna gain calculations assume the two patch antennas are identical; therefore, equal gains. Receive minus transmit power (dBm) is the transmission scattering parameter (dB):  $|S_{21}| = P_R - P_T$ . Solving for  $|S_{21}|$  in (4-4):

$$|S_{21}| = 2G + 20 \log_{10} \left( \frac{c}{4\pi R f} \right) \quad (4-5)$$

where gain  $G$  is equal to  $G_T$  and  $G_R$ . Antenna gain is:

$$G = \frac{1}{2} \left[ |S_{21}| - 20 \log_{10} \left( \frac{c}{4\pi R f} \right) \right] \quad (4-6)$$

Figure 4-5 shows the patch antenna gain over frequency. Expected patch antenna gain is 7dB to 9dB [26].

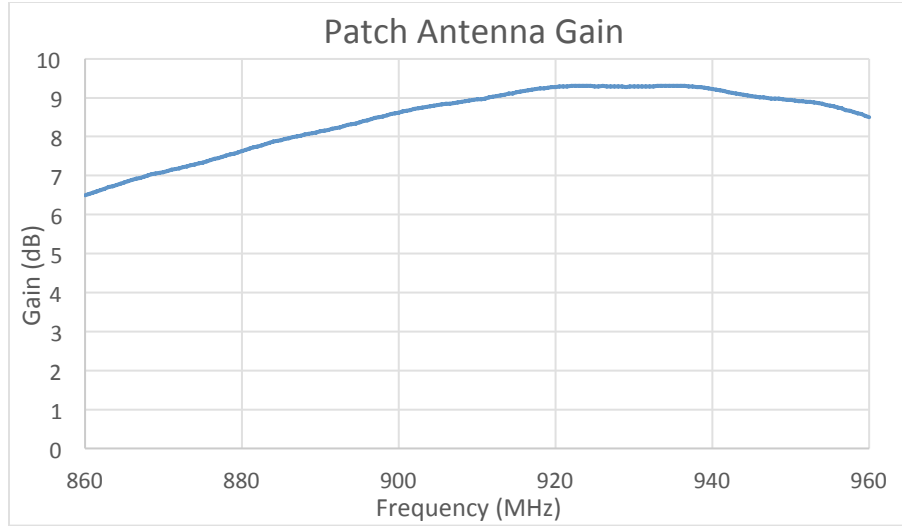


Figure 4-5: Measured Air Dielectric Patch Antenna Gain (dB) vs. Frequency (MHz), 902MHz-928MHz

## 4.2: RFID Antenna Input Impedance Measurement Methods

### 4.2.1: Sleeve Balun Probe

Antenna input impedance is calculated by measuring input reflection coefficient  $\Gamma_{in}$ , on a Vector Network (VNA), and solving for impedance  $Z_{in}$  [1]:

$$Z_{in} = Z_o \frac{1 + \Gamma_{in}}{1 - \Gamma_{in}} \quad (4-7)$$

where  $Z_o$  is the transmission line characteristic impedance. Dipole currents on the two antenna feeds form a differential signal (equal, out-of-phase amplitudes) at the probe (Figure 4-6). Current  $I_{OC}$  flows inside the outer conductor and current  $I_{IC}$  flows on the inner conductor. However, current also flows on the outer conductor's outer surface, parasitic current  $I_p$ , which is included in the impedance measurement. An unbalanced line (coaxial cable) is a single ended line with a ground plane current return path. A balanced line includes two conductors which transmit anti-phase signals. A balun (BALanced-UNbalanced) converts an unbalanced line to a balanced line (or vice-versa) and attenuates the parasitic current. Baluns can also be used to transform transmission line impedances.

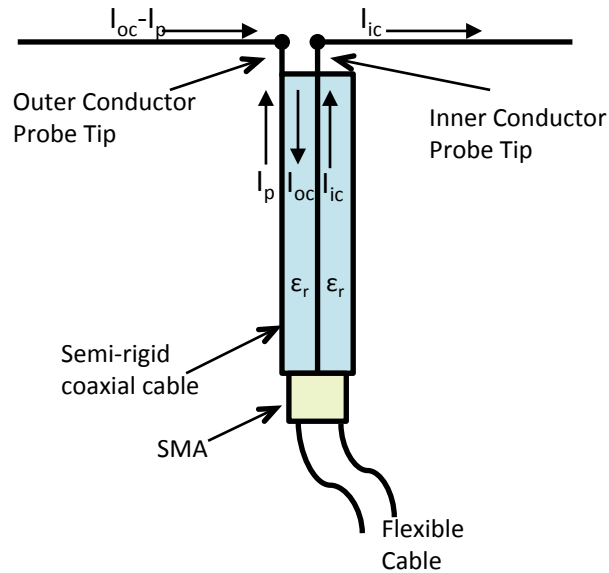


Figure 4-6: Dipole and Cable without Balun

A sleeve (or bazooka) balun is selected for a one-to-one impedance ratio between the balanced and unbalanced lines. The sleeve balun (Figure 4-7 and 4-8) is a quarter-wave ( $\lambda/4$ ) brass tube soldered to the semi-rigid coaxial cable's outer conductor. The two probe tips extend from the inner and outer conductors. Both probe tips connect to a balanced device. At the desired frequency ( $c/\lambda$ ), the short circuit termination creates an open termination at the probe tip outer conductor (Figure 4-9), which blocks current flow on the outer conductor's outer surface. This enables accurate one-to-one impedance measurements.



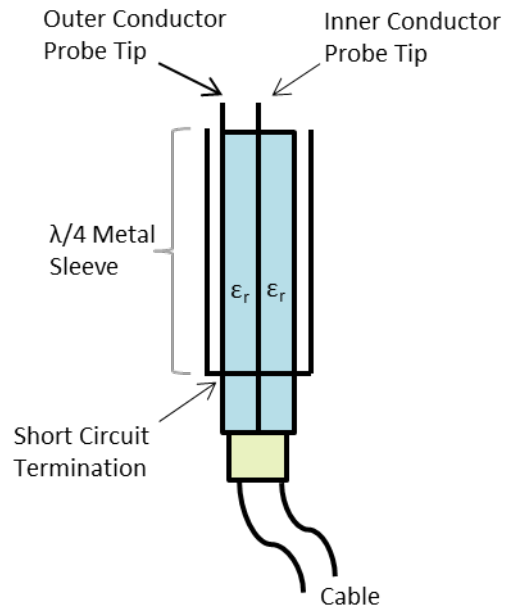


Figure 4-7: Sleeve Balun,  $\lambda/4$  Metal Sleeve Soldered to Outside of Outer Conductor



Figure 4-8: Constructed Sleeve Balun

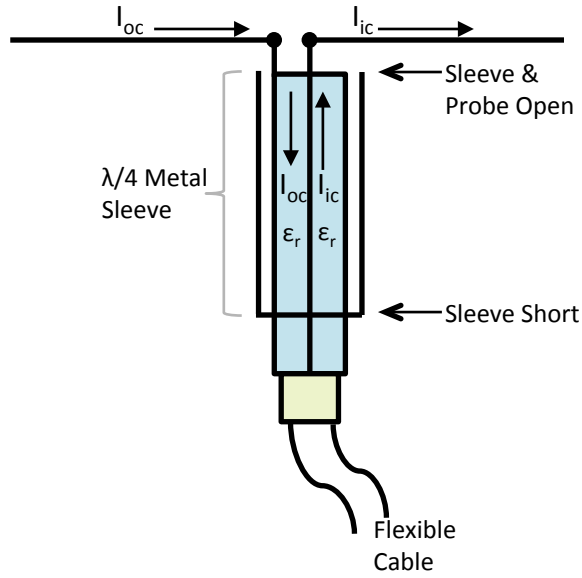


Figure 4-9: Dipole with Sleeve Balun

To test the sleeve balun, 915MHz half-wave dipole leads are soldered to the inner and outer conductor probe tips. The dipole radiation pattern is measured in the E-Plane (Figure 4-10). The half-wave dipole E-field radiation pattern is [20]:

$$\mathbf{F} = \frac{E}{E_{MAX}} \sin(\theta) \hat{\theta} \quad (4-8)$$

where  $E$  is the electric field and  $E_{MAX}$  is the maximum electric field. If the sleeve balun prevents current flow on the probe's outer shielding, no E-field radiates in the dipole null directions ( $\Theta=90^\circ$ ,  $270^\circ$ : see Fig. 4-10). 915MHz half-wave dipole E-plane radiation patterns are measured with and without a sleeve balun (Figure 4-11). The  $\Theta=90^\circ$  and  $\Theta=270^\circ$  sleeve balun/dipole nulls are attenuated 14dB and 12dB, respectively, compared to 'no balun' measurements. Hence, the sleeve balun effectively attenuates the probe's outer conductor current flow.

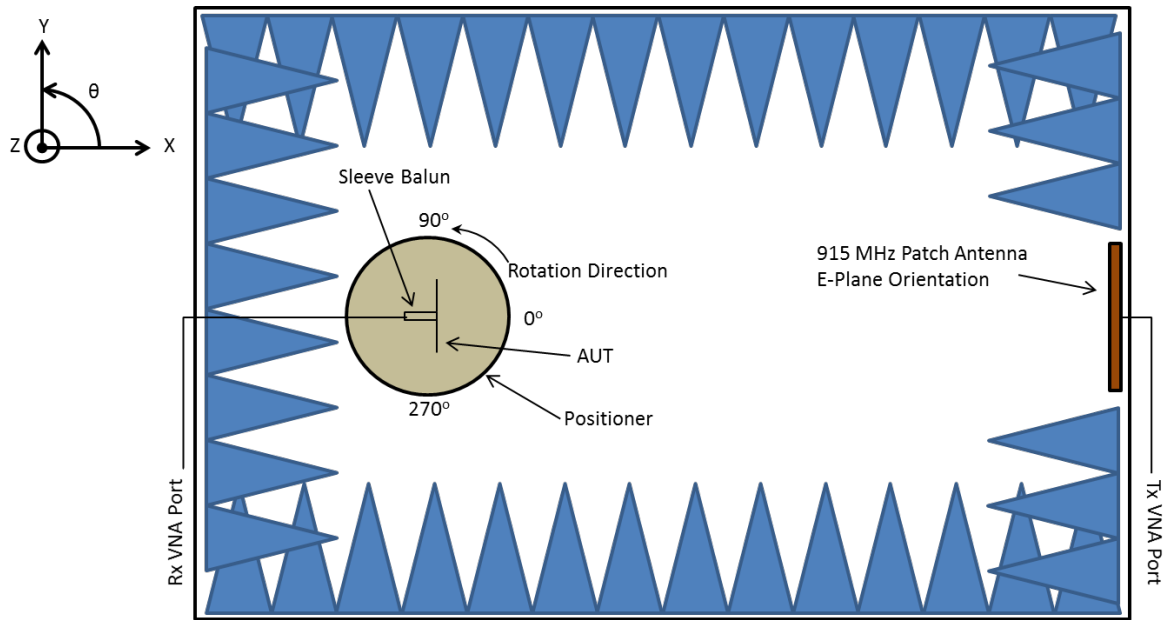


Figure 4-10: Anechoic Chamber Pattern Measurement Setup, Top View

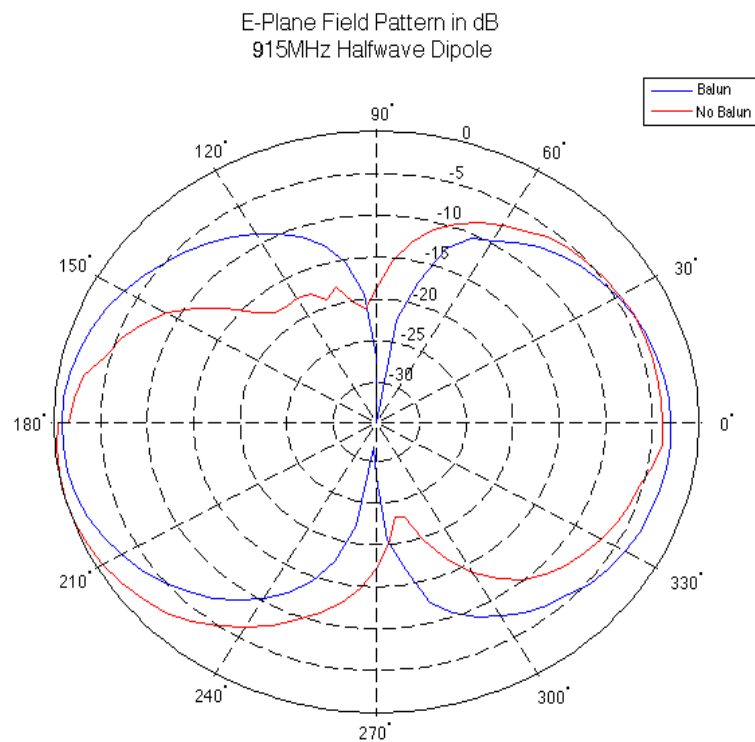


Figure 4-11: Dipole With and Without Sleeve Balun, E-Plane ( $\theta$ ) Field Pattern, dB

#### 4.2.2: Differential Impedance Probe

Antenna input impedance is also VNA-measured with a differential probe (Figure 4-12) [27] by converting two-port S-parameter measurements to input impedance.



Figure 4-12: Differential Probe, Tag Antenna Input Impedance Measurements

To derive input impedance, consider the two-port network in Figure 4-13. The network's Z-parameter matrix is [1]:

$$\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix} \quad (4-9)$$

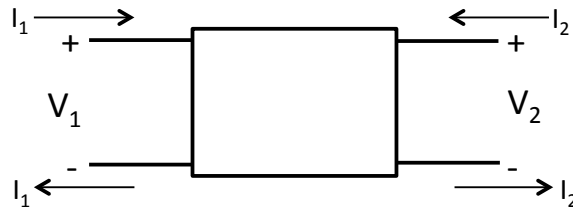


Figure 4-13: Two-Port Network

A dipole antenna is modeled as a current source with source currents  $I_1$  and  $I_2$  exciting a two-port network (Figure 4-14). The input voltage across the feed,  $V_{IN}$ , is the difference between  $V_1$  and  $V_2$ :

$$V_{IN} = V_1 - V_2 \quad (4-10)$$

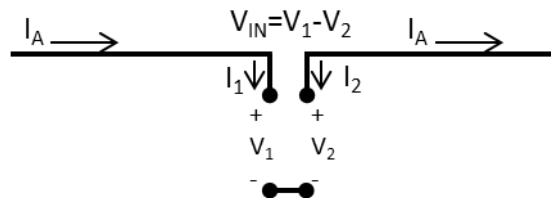


Figure 4-14: Two-Port Network with Dipole Currents

Antenna current ' $I_A$ ' flows in a single direction along the antenna; hence,  $I_1=I_A$  and  $I_2=-I_A$ . Substituting (4-9) into (4-10),  $V_{IN}$  is written in terms of Z-Parameters and  $I_A$ :

$$V_{IN} = (Z_{11} - Z_{12} - Z_{21} + Z_{22})I_A \quad (4-11)$$

The input impedance  $Z_{IN}$  is:

$$Z_{IN} = \frac{V_{IN}}{I_A} = Z_{11} - Z_{12} - Z_{21} + Z_{22} \quad (4-12)$$

Using Z-to-S parameter conversions [1], the input impedance is written in terms of the S-parameters:

$$Z_{IN} = 2Z_o \frac{1 - S_{11}S_{22} + S_{12}S_{21} - S_{12} - S_{21}}{(1 - S_{11})(1 - S_{22}) - S_{12}S_{21}} \quad (4-13)$$

If the antenna is symmetric,  $S_{21}=S_{12}$  and  $S_{11}=S_{22}$ . Input impedance reduces to:

$$Z_{IN} = 2Z_o \frac{1 - S_{11}^2 + S_{21}^2 - 2S_{12}}{(1 - S_{11})^2 - 2S_{21}} \quad (4-14)$$

To construct a differential probe, cut two equal length semi-rigid coaxial cables. In this example, the lengths are 10cm. Attach male SMA connectors to one side of each coaxial cable. Expose 0.5mm inner conductor lengths on the other side (antenna probes). Connect the exposed inner conductor probes to the Antenna Under Test (AUT). Align the probes lengthwise. Solder the probes' outer shielding together, leaving 2-3cm on the SMA side unsoldered.

The differential probe extends beyond the calibration plane (Figure 4-15). S-parameter phase changes along the probe length; the active ports' calibration planes must be transferred to the probe tips (port extension plane, Figure 4-15). Time Domain Reflectometry (TDR) methods determine electrical delay to the port extension plane. The active ports' reference planes are adjusted to the port extension plane.

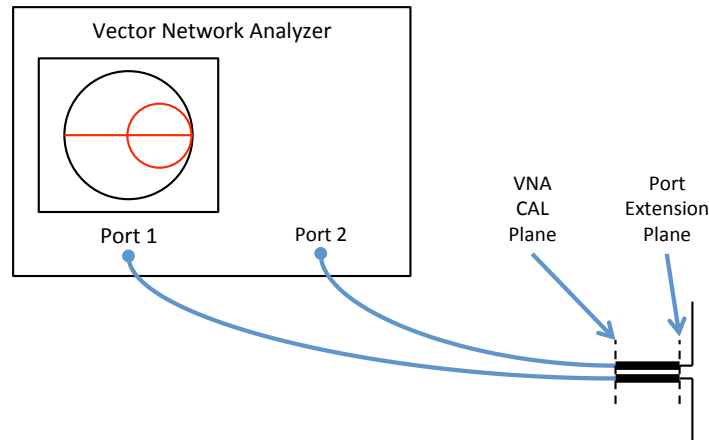


Figure 4-15: VNA Port Extension

#### 4.2.3: Sleeve Balun and Differential Probe Verification

RFID antennas are printed on Dupont Melinex® ST504 polyethylene terephthalate (PET) film (dielectric thickness  $t_\epsilon=150\mu\text{m}$  and dielectric constant  $\epsilon_r=2.9$ ) and bounded by air dielectric ( $\epsilon_r=1$ ) above and below the substrate (Figure 4-16). The structure is defined in Keysight's Advanced Design System (ADS) Momentum Electromagnetic Simulator. A 15.6cm length (s dimension in Figure 4-17) planar dipole is simulated in ADS from 860MHz to 960MHz. Antenna dimensions include microstrip thickness  $t_{Ag}=25\mu\text{m}$ , microstrip width  $W=1\text{mm}$  and  $g=1\text{mm}$  feed gap. The antenna is measured in an anechoic chamber with the sleeve balun and differential probe. Impedance results are shown in Figure 4-18 and Figure 4-19. The sleeve balun antenna resistance is within  $\pm 5\Omega$  of simulations from 880MHz to 940MHz. The differential probe antenna resistance is within  $\pm 5\Omega$  of simulated values over the 860MHz-960MHz band. Both the sleeve balun and differential probe reactances are within  $\pm 5\Omega$  over the 860MHz-960MHz band. Sleeve baluns are narrowband devices within a  $\pm 3.5\%$  bandwidth around the 915MHz center frequency [20]. The differential probe is a broadband (greater than  $\pm 3.5\%$  bandwidth) measurement configuration; bandwidth is limited by the semi-rigid coax and VNA bandwidth. The differential probe measurement configuration is used to measure RFID antennas in Chapter 5.

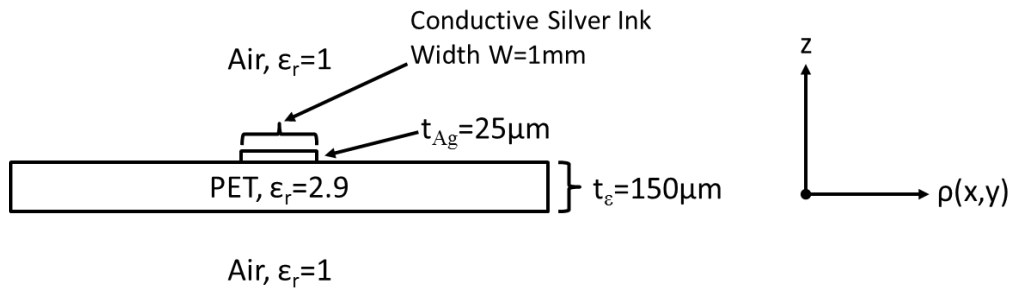


Figure 4-16: PET Substrate Bounded Above and Below by Air Dielectric, Side View

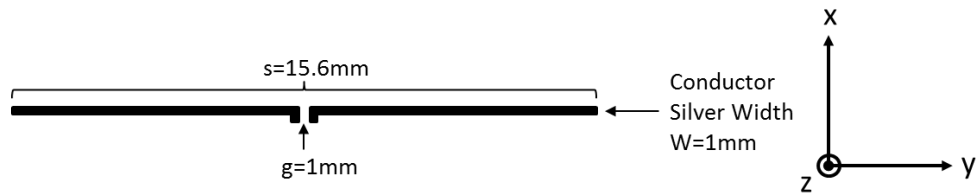


Figure 4-17: Planar Dipole Design, Top View

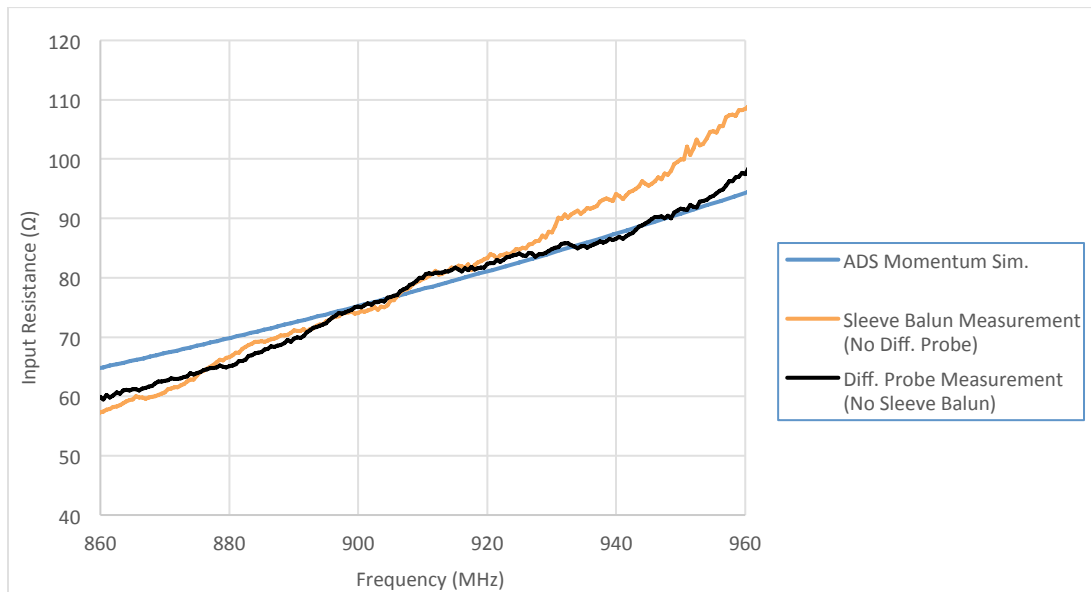


Figure 4-18: Printed Planar Dipole Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

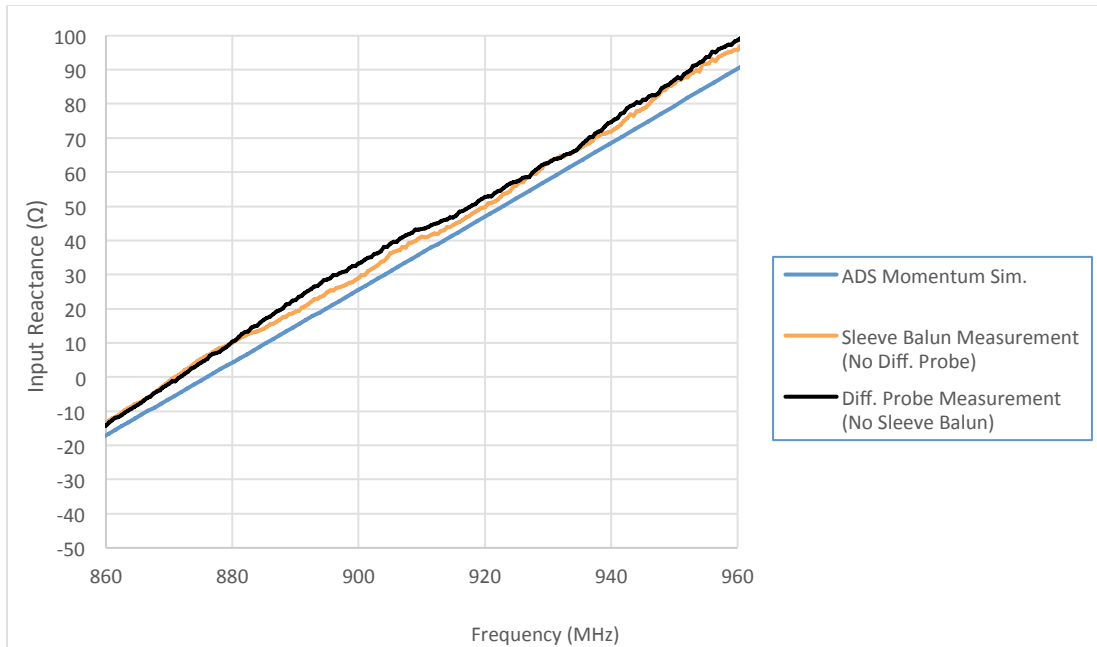


Figure 4-19: Printed Planar Dipole Input Reactance ( $\Omega$ ) vs. Frequency (MHz)



## 5. ANTENNA DESIGNS AND RESULTS

### 5.1: Load Characterization

Alien Technology's Higgs-4 Radio Frequency Integrated Circuits (RFICs) are used to range test antenna designs. The Higgs-4 RFIC package size is 600x600 $\mu\text{m}$ . Alien Technology distributes sample RFICs on straps (Figure 5-1), which include two copper traces (3.5x2.5mm) connected to the RFIC inputs. The RFIC and traces are affixed to a dielectric substrate, called an inlay. Straps increase surface area and facilitate RFIC connection to tag antennas.

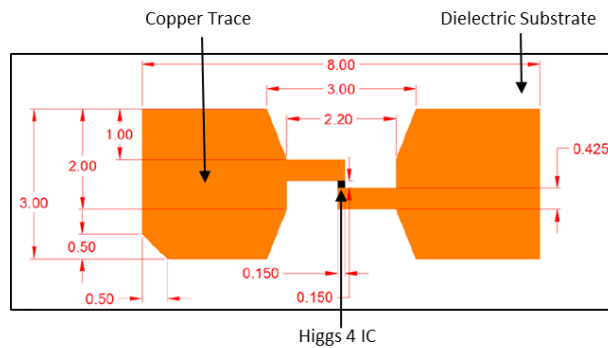


Figure 5-1: Higgs-4 Strap Dimensions (Millimeters) [9]

Manufacturers typically define RFIC input impedance as a parallel shunt resistor and capacitor (Figure 5-2). The Higgs-4 strap datasheet specifies shunt resistance  $R_L=1800\Omega$  and shunt capacitance  $C_L=0.95\text{pF}$  at minimum operating power, -18.5dBm (860MHz – 960MHz). The Higgs-4 series impedance response (parallel  $C_L$  and  $R_L$ ) is shown in Figure 5-3 and Figure 5-4. The RFIC's 915MHz center frequency input impedance  $Z_{IC}$  is  $18.43-j181.2\Omega$ . Measuring RFIC input impedance has been the focus of several studies including [28] [29] and is not a trivial task. The RFIC has several circuit blocks at the input (discussed in Chapter 1), and input impedance is power dependent. Furthermore, some RFIC designs include a wake-up sequence. The manufacturer specifies only a “best” compensation point at a specific power to simplify design procedures. A direct VNA measurement of RFIC input impedance yields incorrect results ( $Z_{IC}=9-140j\Omega$ ). For this thesis the manufacturer's stated input impedance ( $Z_{IC}=18.43-j181.2\Omega$ ) is used.

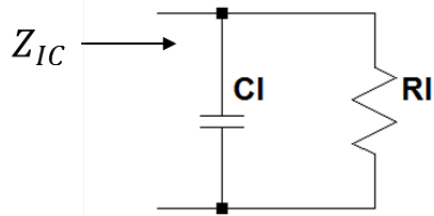


Figure 5-2: RFIC Input Shunt Capacitance and Resistance

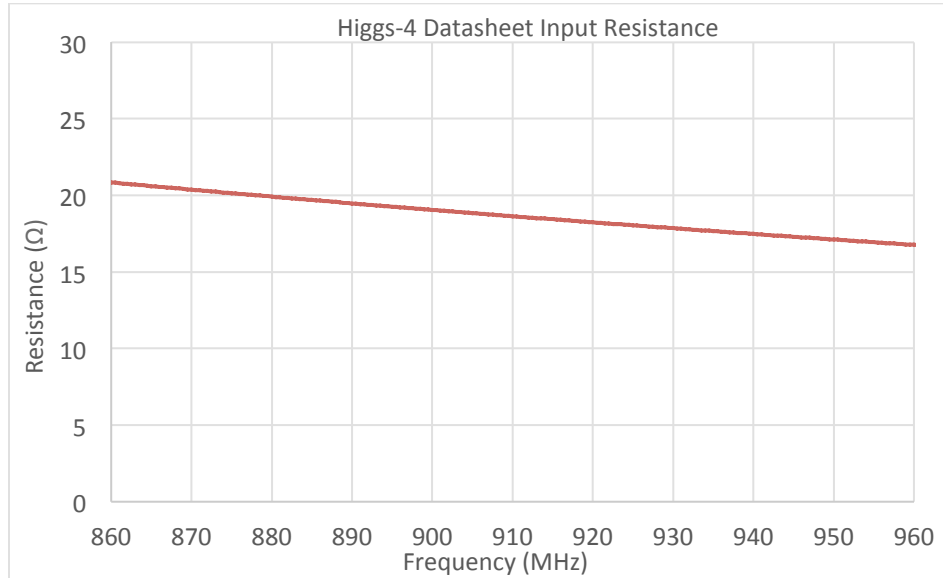


Figure 5-3: Higgs-4  $Z_{IC}$  Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

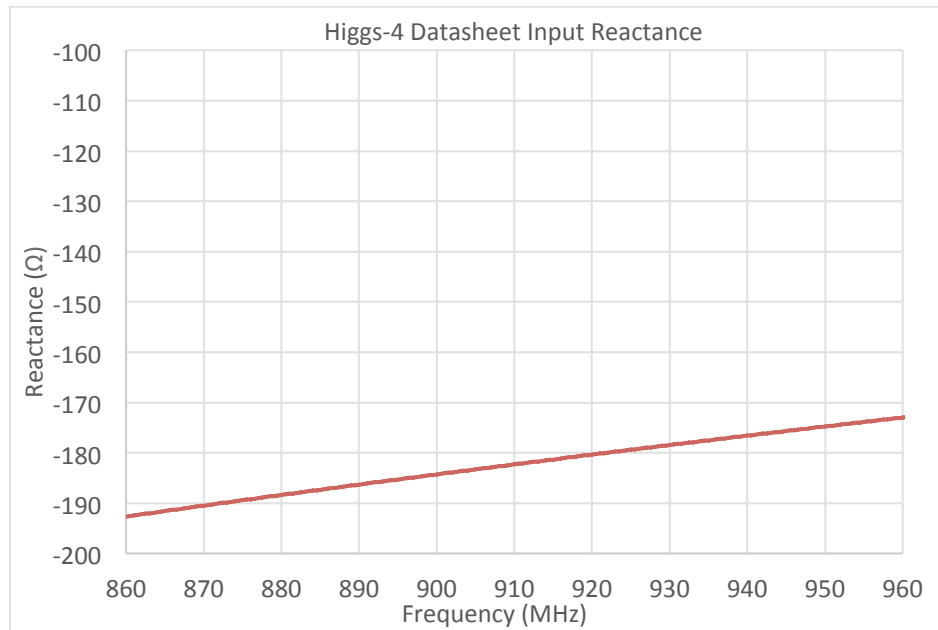


Figure 5-4: Higgs-4  $Z_{IC}$  Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

## 5.2: Narrowband 902-928MHz Design, Measurement and Range Testing

The Higgs-4 complex conjugate impedance at 915MHz is  $Z_{IN}^* = 18.43 + j181.2\Omega$ . Following Section 3.2's narrowband matching design, an  $s=11.3\text{cm}$ , 14 meander element antenna is defined in Advanced Design System (ADS) (Figure 5-5) to match the  $Z_{IN}^*$  target. Antenna dimensions are listed in Table 5-1. Gap  $g$  allows space for mounting the Higgs-4 strap between the two  $I_s$  series inductors. The simulation indicates the antenna's 915MHz input resistance (Figure 5-6) is  $5\Omega$  less than the Higgs-4 resistance. The antenna's 915MHz inductive reactance (Figure 5-7) is  $35\Omega$  less than the IC's input conjugate reactance.

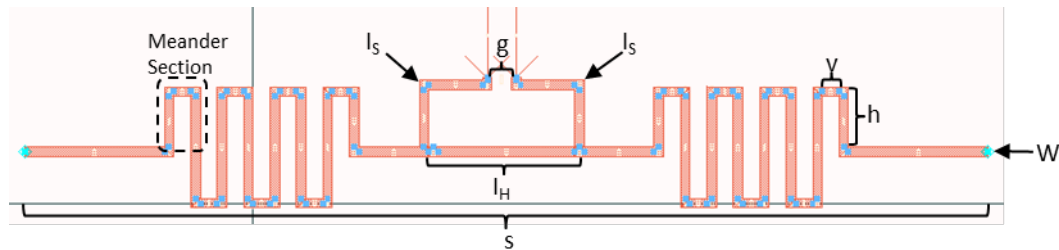


Figure 5-5: Narrowband Antenna Dimensions

Table 5-1: Narrowband Antenna Dimensions

Narrowband Antenna Dimensions	
Conductor Width (W)	1.1mm
Antenna Length (s)	113.0mm
Meander Sections (M)	14
Meander Spacing (v)	2.0mm
Meander Height (h)	6.0mm
Shunt Inductor ( $I_H$ )	17.0mm
Series Inductor ( $I_s$ )	17.0mm
Gap (g)	2.3mm

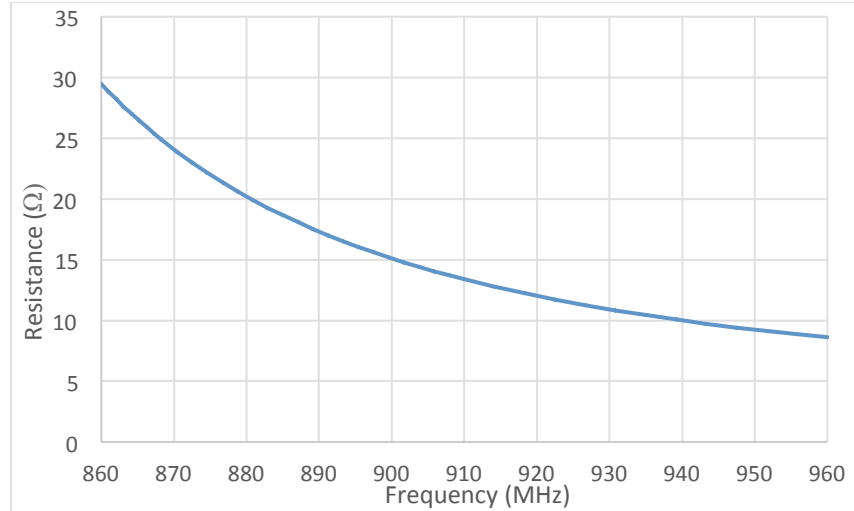


Figure 5-6: Simulated Narrowband Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

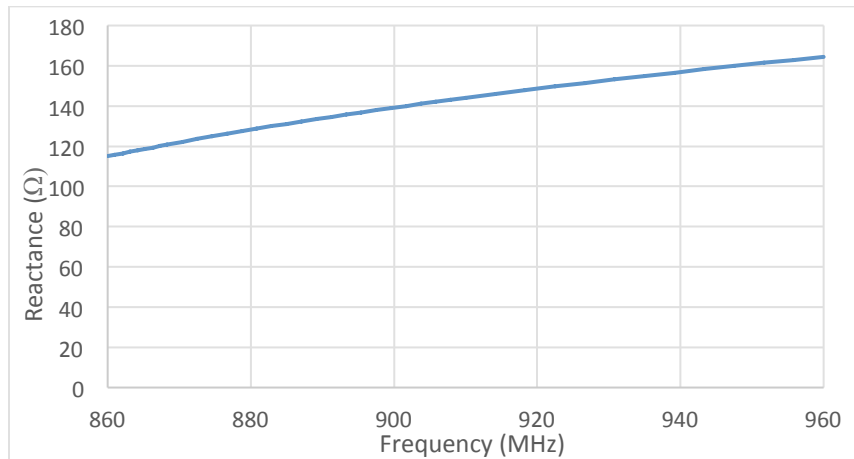


Figure 5-7: Simulated Narrowband Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

$I_H$  is lengthened by 1mm to increase resistance and  $I_S$  strips are lengthened in steps (18.0mm, 18.5mm and 19.5mm) and simulated to show series inductance effects (results in Figure 5-8 and 5-9). At center frequency the input resistance increases by  $2\Omega/\text{mm}$  and inductive reactance increases by  $12\Omega/\text{mm}$ . The three simulated antennas are fabricated and measured (Figure 5-10 and 5-11). Measured resistances are 8-9 $\Omega$  greater than simulated resistances and measured reactance is within 3-6 $\Omega$  simulated reactance. Sources of error are discussed in Section 5.3.

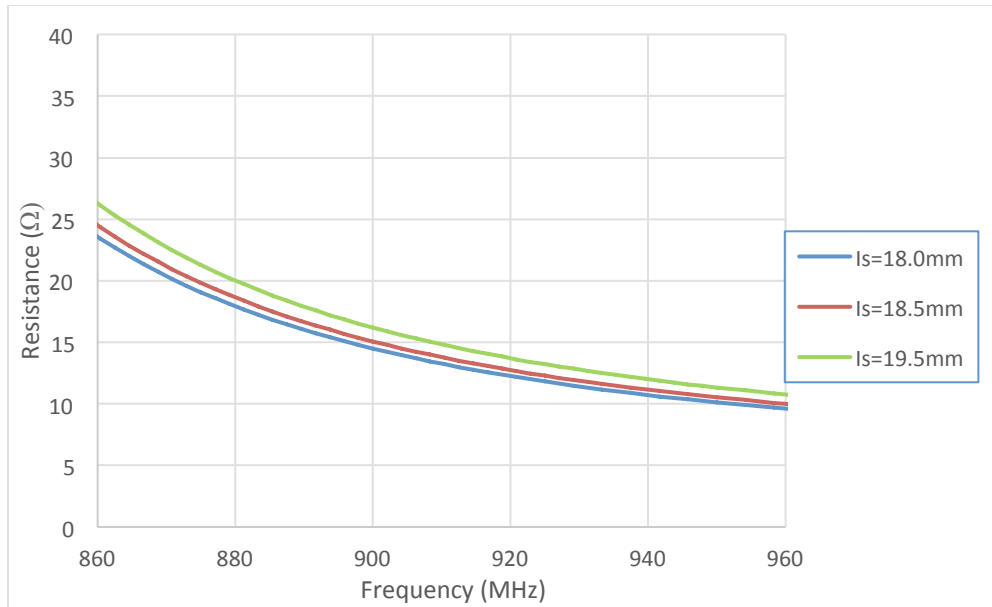


Figure 5-8: Simulated Narrowband Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

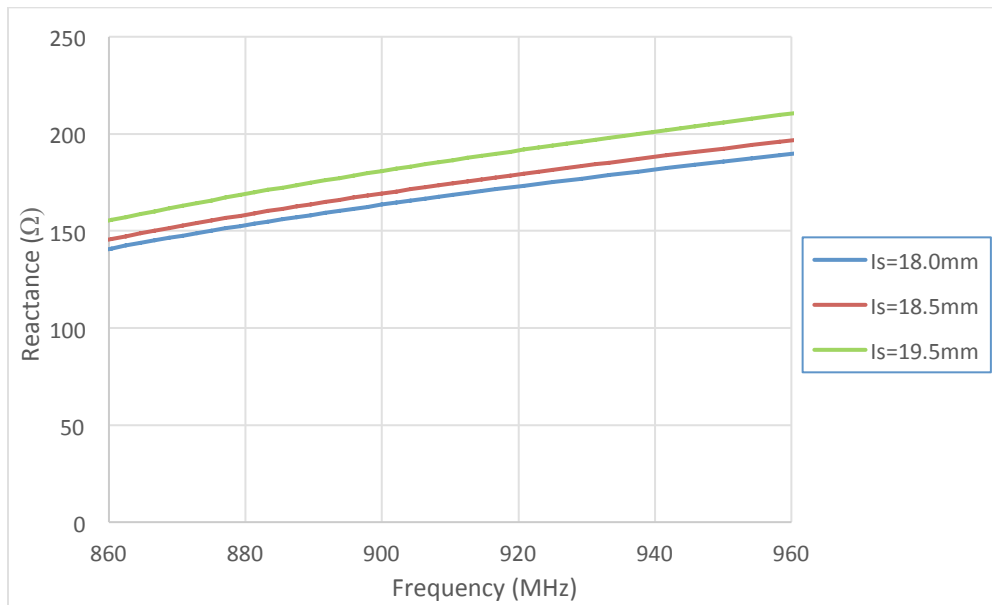


Figure 5-9: Simulated Narrowband Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

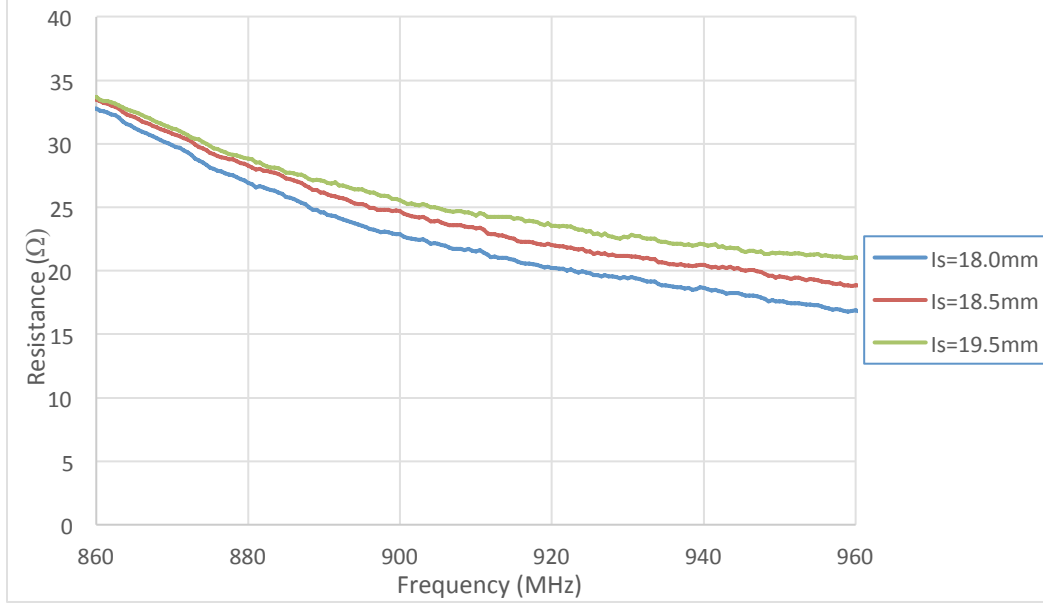


Figure 5-10: Measured Narrowband Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

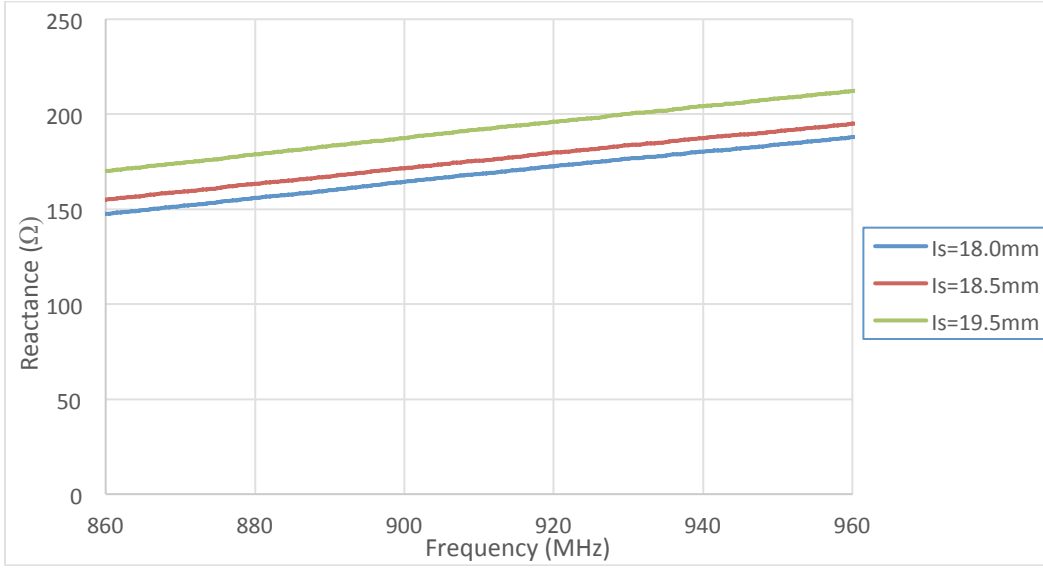


Figure 5-11: Measured Narrowband Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

Voltage reflection coefficient ' $s_r$ ' from RFIC to antenna is [1]:

$$s_r = \frac{Z_{IC} - Z_A^*}{Z_{IC} + Z_A} \quad (5-1)$$

where  $Z_A$  is antenna input impedance and  $Z_{IC}$  is RFIC input impedance. Reflection parameter  $|S_{11}|$  from RFIC to antenna is then [16]:

$$|S_{11}| = 20 * \log_{10}|s_r| \quad (5-2)$$

Antenna $|S_{11}|$  responses are calculated (Figure 5-12).

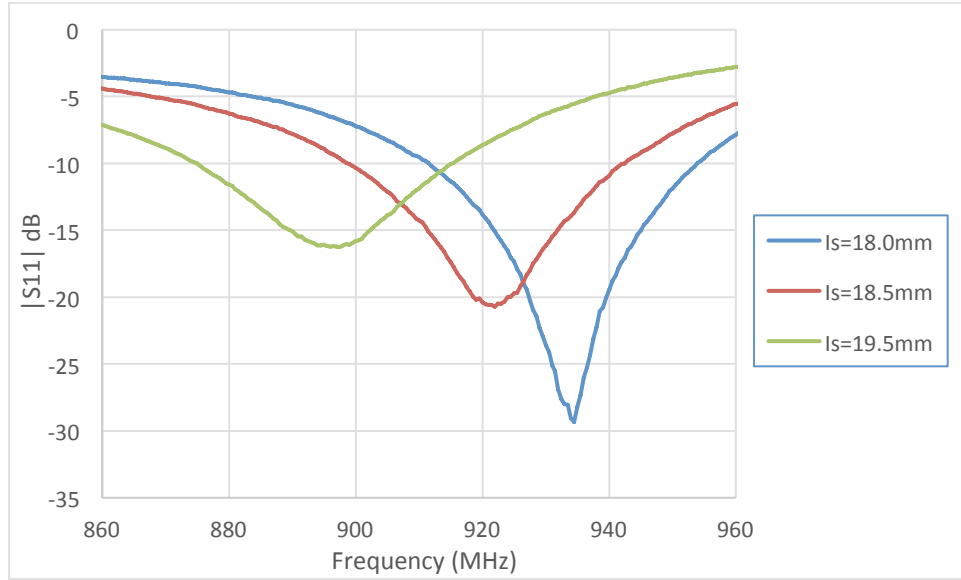


Figure 5-12: Simulated Narrowband  $|S_{11}|$  (dB) vs. Frequency (MHz)

The Friis Transmission Equation is [30]:

$$P_R = P_T G_T G_R L \left( \frac{\lambda}{4\pi R} \right)^2 (1 - |\Gamma_t|^2)(1 - |s_r|^2) \quad (5-3)$$

where  $P_T$  and  $P_R$  are transmit and receive power,  $G_T$  and  $G_R$  are transmit and receive antenna gain,  $L$  is transmit antenna cable attenuation,  $\lambda$  is wavelength,  $R$  is transmit to receive antenna distance, and  $\Gamma_T$  and  $s_r$  are the transmit antenna and receive antenna voltage reflection coefficients. Find  $R$  to determine maximum transmit range:

$$R = \frac{\lambda}{4\pi} \sqrt{\frac{P_T L G_T G_R (1 - |\Gamma_T|^2)(1 - |s_R|^2)}{P_R}} \quad (5-4)$$

Straps are attached to the printed narrowband antennas (Figure 5-13). The transmit antenna (reader patch antenna) and receive antenna (RFID tag) are separated by fixed distance  $R=4.0$ m (Figure 5-14). At 915MHz, transmit patch antenna gain  $G_T$  is 9.1dB (Chapter 4) and reflection coefficient  $\Gamma_T$  is -30dB. Cable (length = 1m) attenuation  $L$  is 1.1dB. Measuring receive antenna gain  $G_R$  on a Vector Network Analyzer (VNA) requires a matching network between the antenna feed and 50 $\Omega$  VNA port. Instead, ADS is used to simulate the field pattern (Figure 5-15) to derive receive antenna gain  $G_R$ . Simulated receive gain  $G_R$  is 1.6dB. Voltage reflection coefficient for conjugate

matching,  $s_r$ , is derived from antenna impedance measurements and the Higgs-4 input impedance datasheet value. The minimum receive power to activate the Higgs-4 IC  $P_R$  is -18.5dBm (from product datasheet [9]). Range testing conditions are shown in Table 5-2.

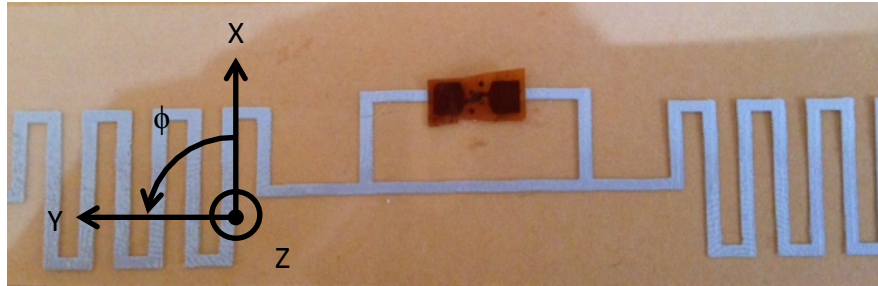


Figure 5-13: Higgs-4 RFIC Strap Attached to Narrow Band Antenna

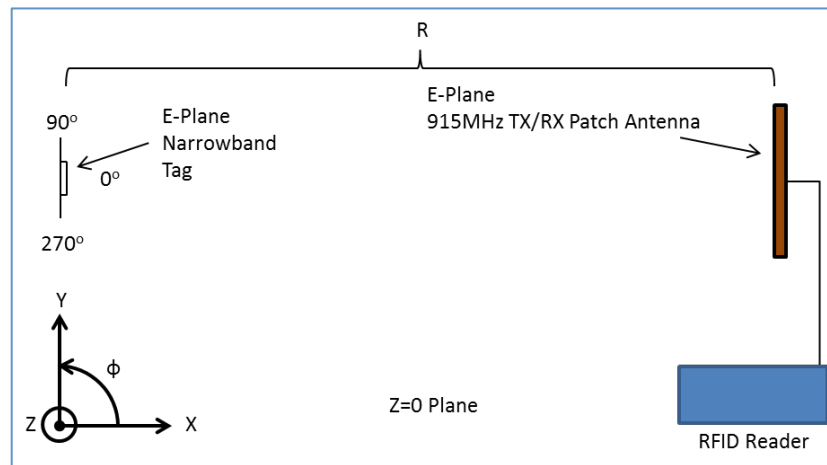


Figure 5-14: RFID Tag Range Testing Setup, E-Plane aligned with f

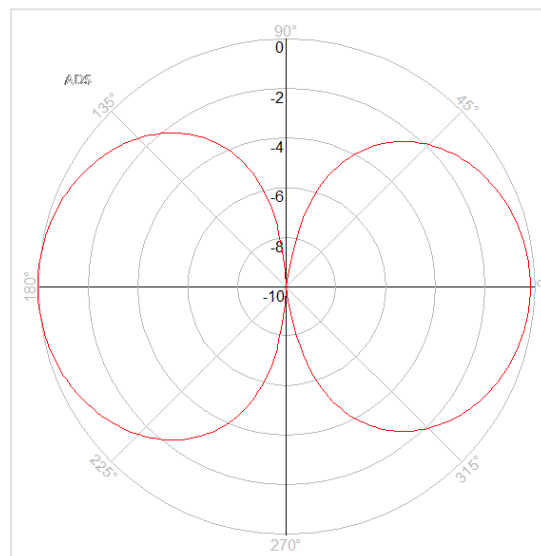


Figure 5-15: Simulated Narrowband 915MHz Antenna E-Field Pattern (f) in dB



Table 5-2: Narrowband Antenna Range Testing Conditions at 915MHz\*

Narrowband Antenna Range Testing Conditions at 915MHz		
R	4.0	m
$P_R$	-18.5	dBm
$G_T$	9.1	dB
L	1.1	dB
$G_R$	1.6	dB
$\Gamma_T$	-30	dB
$\lambda$	0.328	m

\* $s_r$  ( $|S_{11}|$ ) listed in Table 5-3

Tag transmit distance is evaluated by decreasing transmit power  $P_T$  until the reader no longer communicates with the tag. The reader frequency hops every 0.4ms within the 902MHz-928MHz band; the minimum power required for a read is not necessarily at the center frequency. The three length  $l_s$  antennas are range tested.  $R_{FRIIS}$  is the Friis Equation predicted antenna transmit distance with measured  $s_r$  and reader transmit power  $P_T$ . Range testing results are shown in Table 5-3. The  $l_s=18.0$ mm antenna correlates most closely to  $R_{FRIIS}$  and requires the least amount of power for a read.

Table 5-3: Narrowband Antenna/ Higgs-4 RFIC Range

Narrowband Antenna/Higgs-4 RFIC Read Range				
$l_s$ (mm)	$P_T$ (dBm)	$ S_{11} $ (dB)	$R_{FRIIS}$ (m)	$R_{FRIIS} - R$ (m)
18.0	16.6	-11.3	4.3	0.3
18.5	16.7	-17.4	4.5	0.5
19.5	18.0	-10.0	5.0	1.0

### 5.3: Broadband 865-955MHz Design, Measurement and Range Testing

The broadband match procedure, discussed in Section 3.4, overlays antenna input impedance on the RFIC's complex conjugate impedance for a best fit over the 865-955MHz band (shown for Impinj's Monza-4 IC load in Figure 5-16 and 5-17). The matching technique yields the broadband  $|S_{11}|$  simulated response in Figure 5-18.

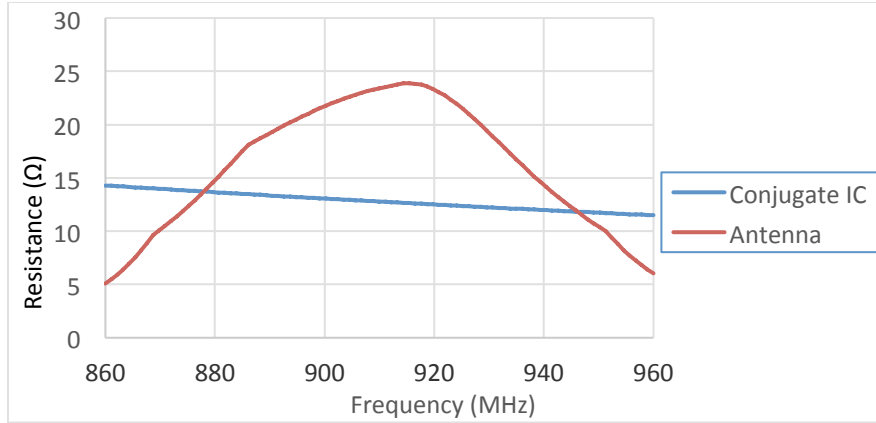


Figure 5-16: Broadband Simulated Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

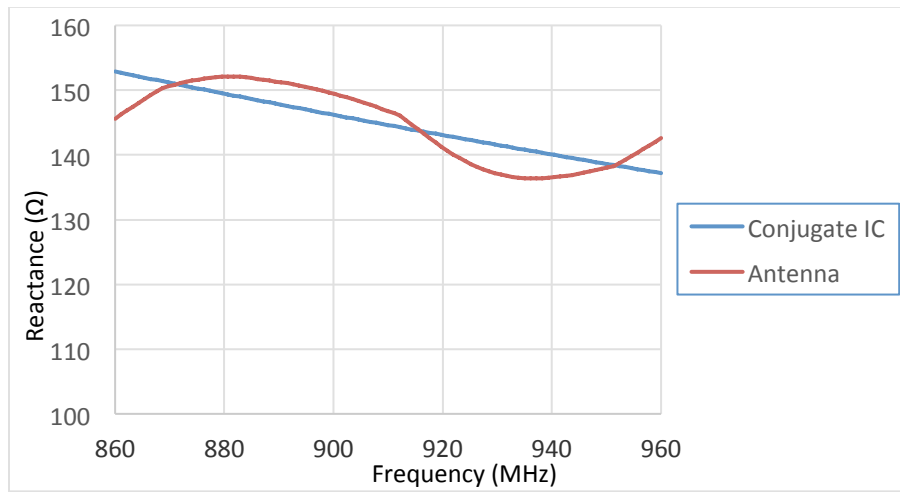


Figure 5-17: Broadband Simulated Reactance ( $\Omega$ ) vs. Frequency (MHz)

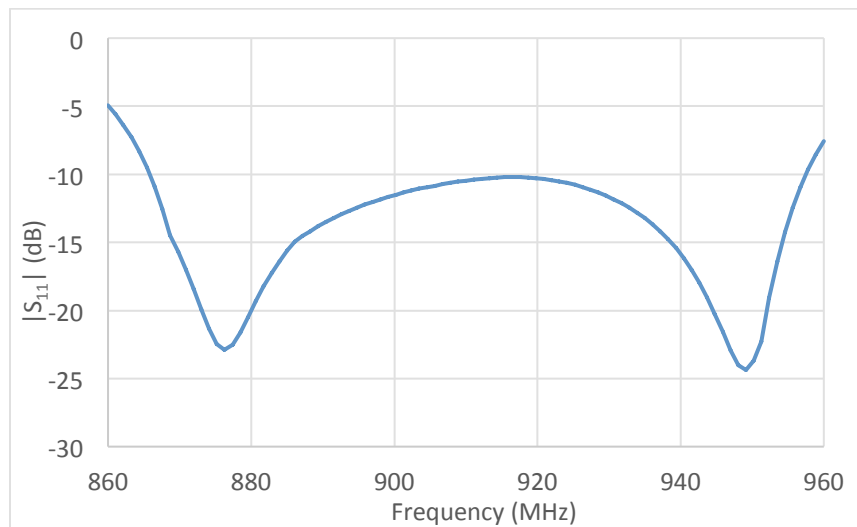


Figure 5-18: Broadband Simulated  $|S_{11}|$  (dB) vs. Frequency (MHz)

Following the Section 3.4 broadband matching procedure, a 14 element meander,  $s=9.0\text{cm}$  length antenna layout is designed (Figure 5-19) to match across the 865MHz–

955MHz band. The band's center frequency is 910MHz. Antenna geometry defined in Table 5-4. Simulated antenna impedance is shown in Figure 5-20 and 5-21.

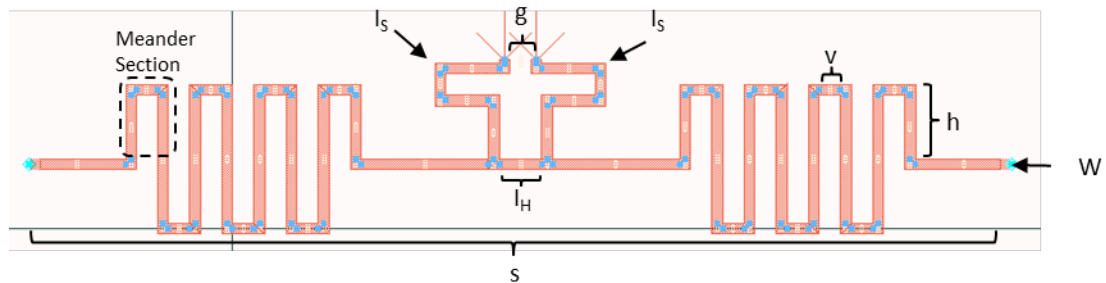


Figure 5-19: Broadband MLA Antenna Design

Table 5-4: Broadband Antenna Dimensions

Broadband Antenna Dimensions	
Conductor Width (W)	1.1mm
Antenna Length (s)	90.0mm
Meander Sections (M)	14
Meander Spacing (v)	2.0mm
Meander Height (h)	6.0mm
Strap Gap (g)	2.3mm
Shunt Inductor (I <sub>H</sub> )	5.0mm
Series Inductors (I <sub>S</sub> )	17.0mm

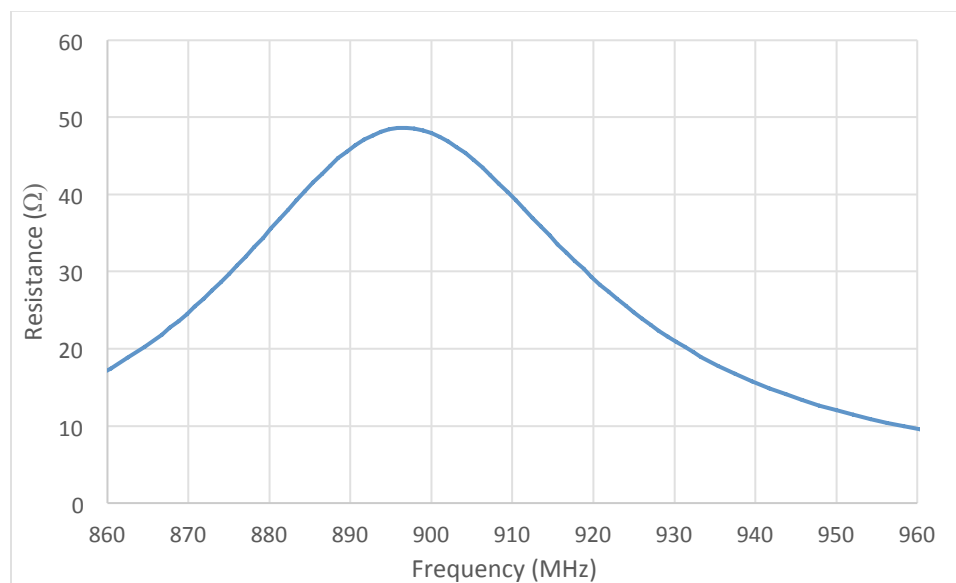


Figure 5-20: Calculated Broadband Input Resistance (Ω) vs. Frequency (MHz)

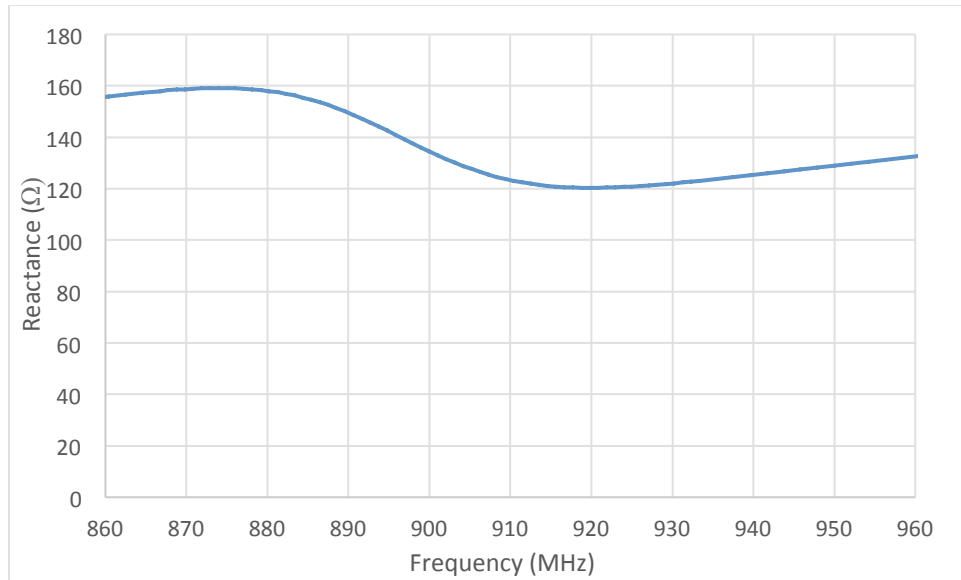


Figure 5-21: Calculated Broadband Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

The simulated resistance maxima occurs at 898MHz, which is 12MHz less than the center frequency. Simulated reactance is also 40 $\Omega$  lower across the band than the conjugate Higgs-4 reactance. The antenna is shortened to  $s=8.6\text{cm}$  to center the resistance maxima at 910MHz. The shunt inductor is shortened to 4mm, reducing peak resistance (discussed in Section 5.4). Series inductors ( $I_s$ ) are lengthened in 1mm steps to 22-24mm, to increase series inductance. Simulated impedance results are shown in Figure 5-22 and 5-23. The series inductance does not affect peak resistance but increases reactance 9 $\Omega/\text{mm}$ .

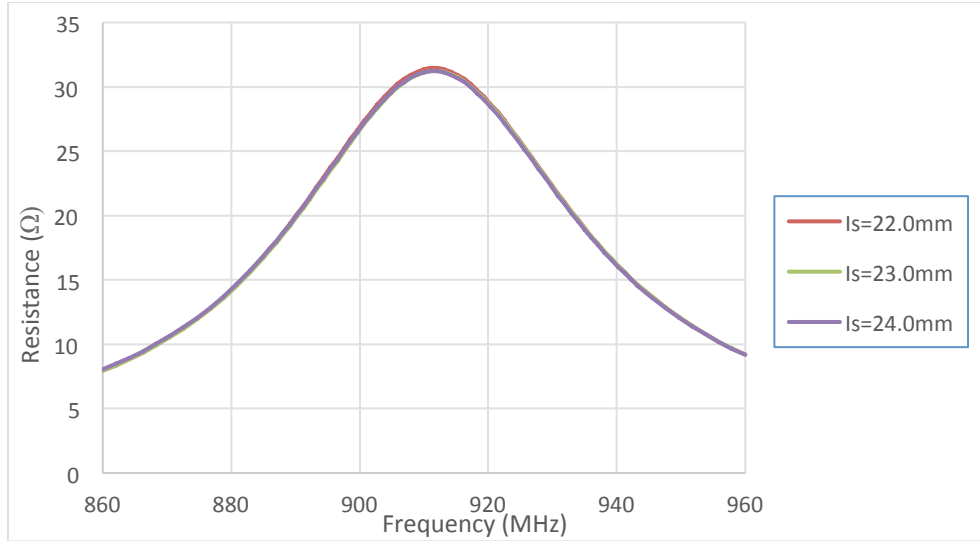


Figure 5-22: Simulated Broadband Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

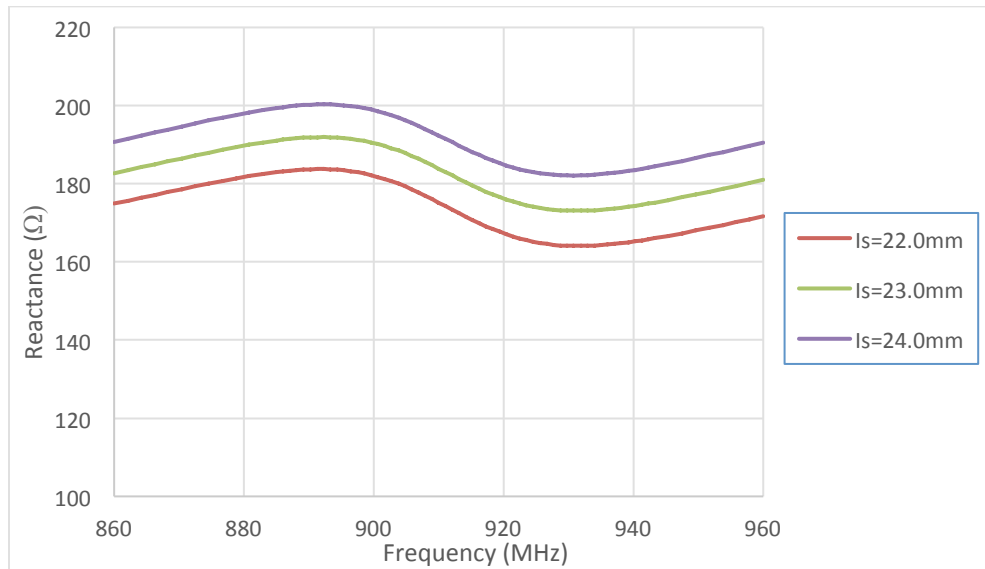


Figure 5-23: Simulated Broadband Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

Input impedances for three fixed antennas with increased  $I_s$  lengths are shown in Figure 5-24 and 5-25. The peak resistance for simulated and measured antennas both occur at 910MHz. Simulated resistance is 5-7 $\Omega$  greater than measured resistance and simulated reactance is 4 $\Omega$  greater than measured reactance at 910MHz. Simulated reactance variance over the band is 18 $\Omega$  but only 7 $\Omega$  across the measured band.

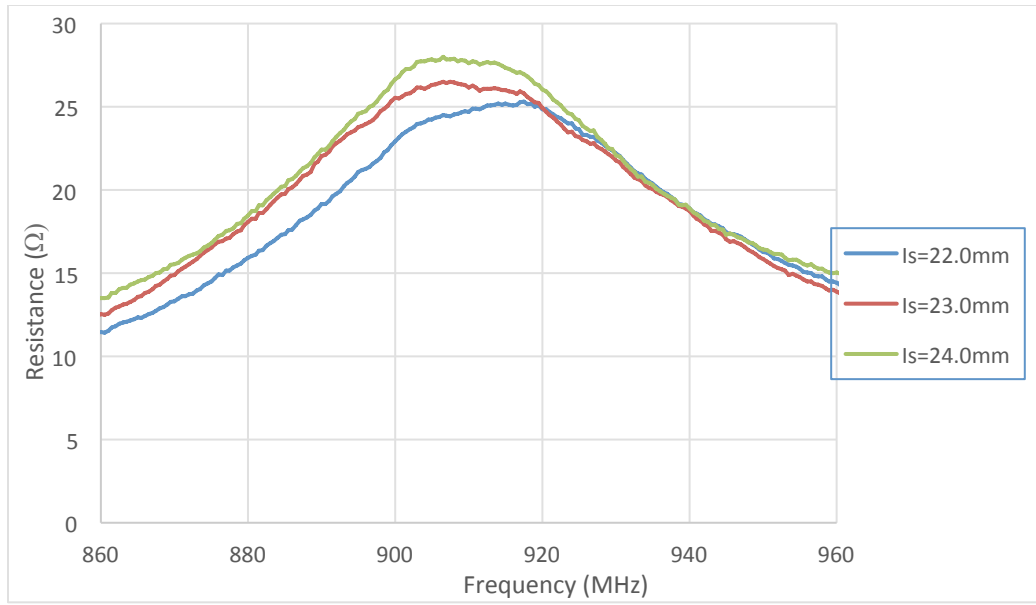


Figure 5-24: Measured Broadband Feed Input Resistance ( $\Omega$ ) vs. Frequency (MHz)

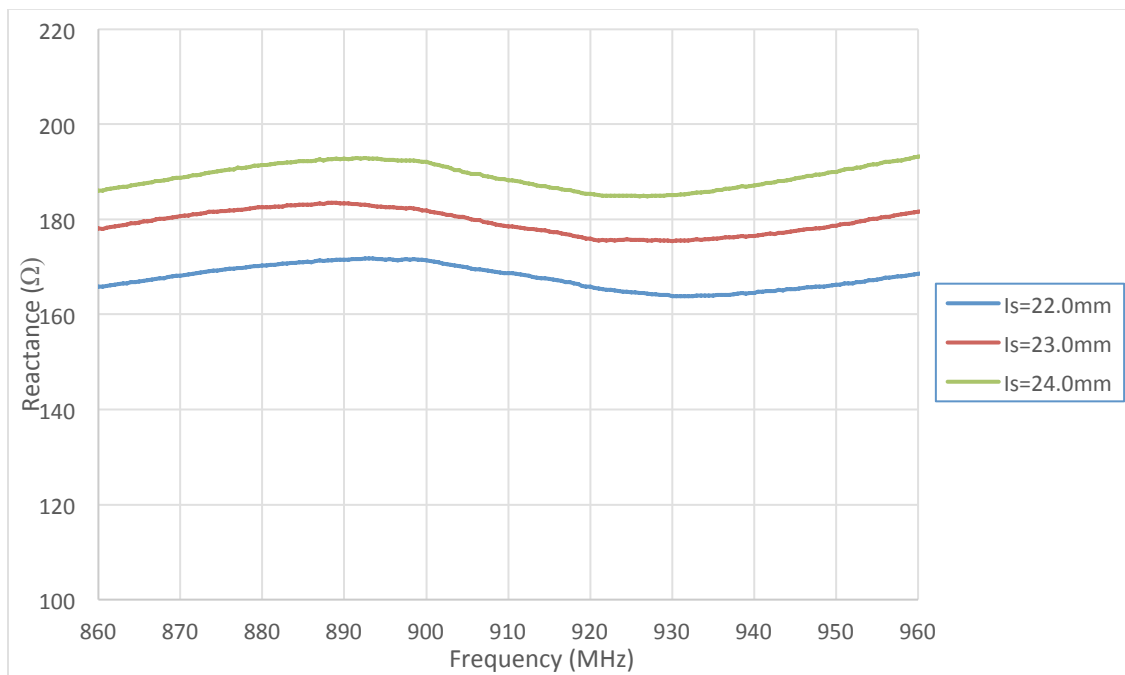


Figure 5-25: Measured Broadband Feed Input Reactance ( $\Omega$ ) vs. Frequency (MHz)

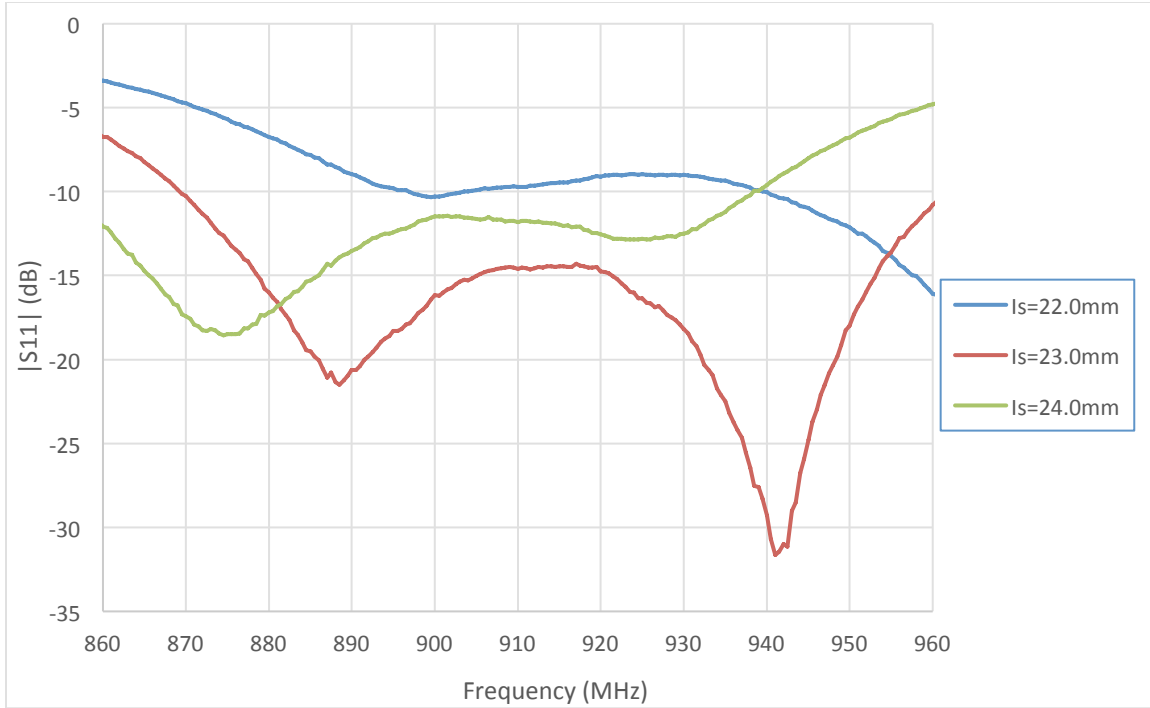


Figure 5-26: Measured Broadband Feed  $|S_{11}|$  (dB) vs. Frequency (MHz)

Conjugate match reflection parameters are shown in Figure 5-26. The broadband antennas are shorter than the narrowband antennas; hence, have decreased radiation resistance and gain. ADS simulation predicts a 1.0dB broadband antenna receive gain  $G_R$ . Range testing parameters are listed in Table 5-5. Range testing results are shown Table 5-6. The  $I_S=22\text{mm}$  antenna performed the best and is closest to the predictive value.

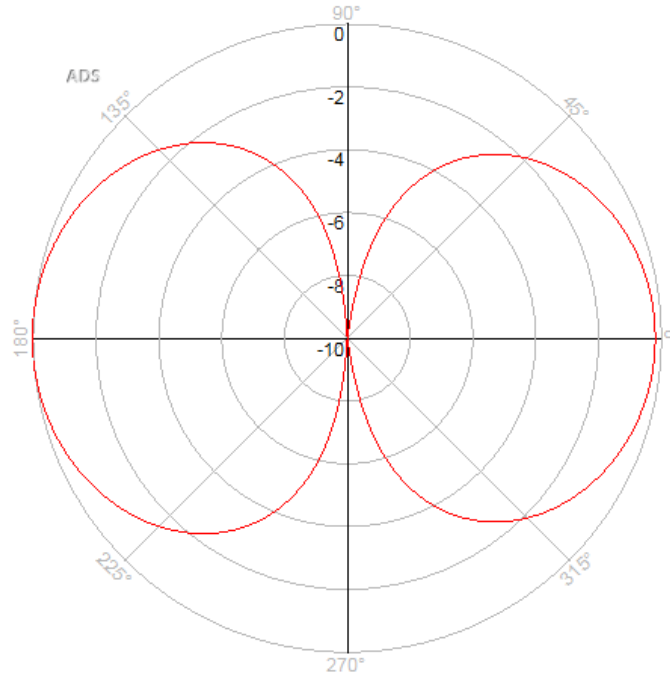


Figure 5-27: 915MHz Simulated Broadband Antenna E-Field Pattern ( $\phi$ ) in dB

Table 5-5: Broadband Antenna Range Test Conditions at 910MHz

<b>Broadband Antenna Range Testing Conditions at 910MHz*</b>		
R	4.0	m
$P_R$	-18.5	dBm
$G_T$	9.1	dB
L	1.1	dB
$G_R$	1.0	dB
$\Gamma_T$	-30	dB
$\lambda$	0.328	m

\* $s_r$  ( $|S_{11}|$ ) values listed in Table 5-6

Table 5-6: Broadband Antenna/RFIC Range Read Range

<b>Broadband Antenna/Higgs-4 IC Range</b>				
$l_s$ (mm)	$P_T$ (dBm)	$ S_{11} $ (dB)	$R_{FRIS}$ (m)	$R_{FRIS} - R$ (m)
22.0	16.8	-9.5	4.0	0.0
23.0	17.0	-14.5	4.3	0.3
24.0	18.1	-12.0	5.0	0.8

For simulations, the silkscreened silver ink vertical thickness theoretical approximation is 1mil based on silk-screen mesh and emulsion used. Sheet resistivity is 12-



15mΩ/sq/mil; exact values are affected by antenna curing temperature. Also, the conductor's surface roughness is not specified in the product datasheet. Adding a surface roughness parameter to the simulation reduces both the simulation's resistance peaking and reactance variance over the band.

#### 5.4: Narrowband vs. Broadband Frequency Response

Narrow and broadband antennas have similar structures but distinct shunt inductor length  $l_H$  and antenna length  $s$ . Antenna frequency responses with varied shunt inductor lengths  $l_H$  are plotted (Figure 5-28 and 5-29) to illustrate input impedance effects. Series inductor length  $l_S$  remains constant. Lengthening  $l_H$  increases antenna reactance and maximum resistance across the band. It also increases reactance variance vs. frequency. The narrowband antenna uses a large shunt inductor ( $l_H=18.0\text{mm}$ ) to adjust the resistance peak's falling edge to match the RFIC input impedance at the center frequency. The broadband design uses a short shunt inductor ( $l_H=4\text{mm}$ ) to compress the resistance peak and minimize reactance frequency response ripple. The broadband design dipole length must be decreased to center the resistance peak at the center frequency. Decreasing antenna length reduces gain; hence, bandwidth is exchanged for increased transmit range. This is an acceptable trade-off for RFIC applications; reducing gain yields a more omni-directional antenna. Also, commercially viable antennas are often restricted to small ( $s < 10\text{cm}$ ) package sizes.

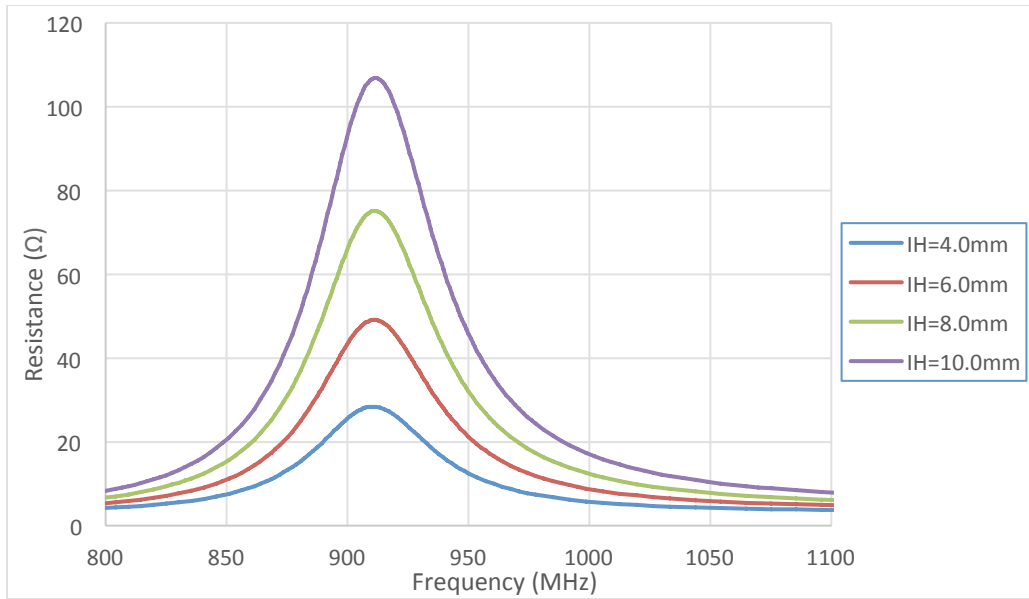


Figure 5-28: Simulated Antenna Resistance ( $\Omega$ ) vs. Frequency (MHz) for Varying Shunt Inductor  $I_H$  Lengths

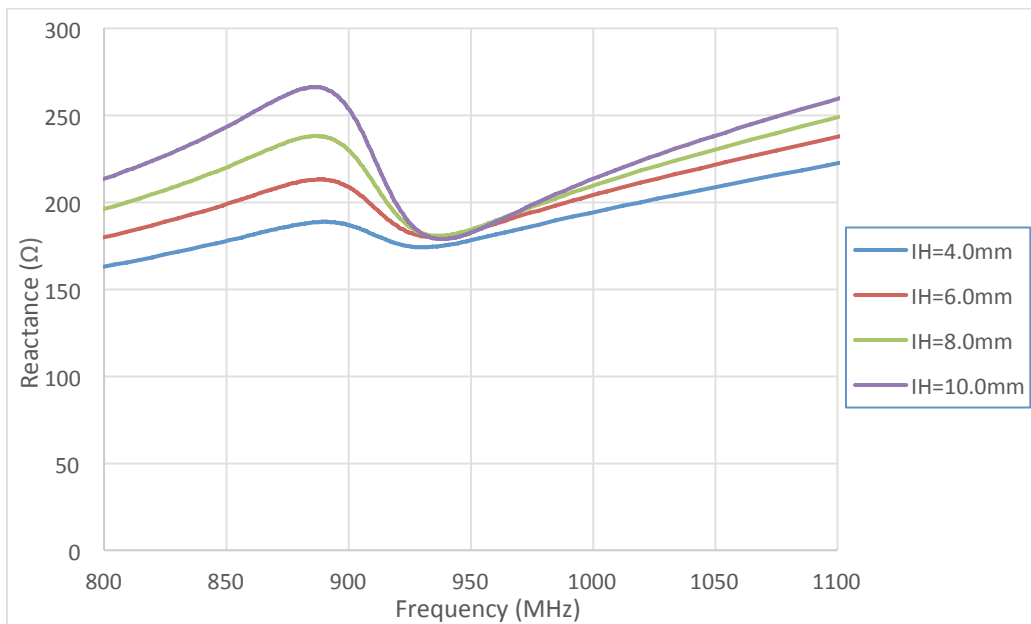


Figure 5-29: Simulated Antenna Reactance ( $\Omega$ ) vs. Frequency (MHz) for Varying Shunt Inductor  $I_H$  Lengths

## 5.5: Conclusion

This thesis demonstrates a start-to-finish design process for narrow and broadband planar RFID antennas and illustrates element sizing effects on antenna input impedance. The narrow and broadband antenna's input impedances are conjugate matched to a passive RFIC load (Alien Technology's Higgs-4) to maximize RFID tag transmit range. Antenna designs are simulated and refined in Keysight's Advanced Design System (ADS) Momentum electro-magnetic simulator, and then silk-screened at Cal Poly's Graphic Communication Department. Input impedance is measured differentially with a two-port method on a Vector Network Analyzer (VNA) to maximize bandwidth over a 1-port narrowband balun measurement. Antenna impedances are manually tuned by adjusting element dimensions. Impedance simulations, fabricated antenna impedance measurements and range testing results are compared for accuracy.

Antenna element sizes are parametrically swept to demonstrate effects on input impedance. Lengthening series inductor  $L_S$  increases input reactance and has minimal effects on input resistance. Lengthening shunt inductor  $L_H$  increases the input resistance peak and increases input reactance ripple over the band. Lengthening dipole length increases input resistance and reactance. The broadband dipole antenna has a short shunt inductor ( $L_H < 5\text{mm}$ ) to set the antenna's input resistance peak at the RFIC input resistance level and to limit the antenna's input reactance ripple. Inductor  $L_S$  length is then adjusted to conjugate match the RFIC input reactance. The broadband dipole length is set to approximately a quarter wave-length to center the resistance peak at center frequency. Dipole gain is largest at a half wave-length, and decreases when shortened. A broadband antenna can match over the GS1 EPC™ Global Gen2 860MHz-960MHz band; however, gain and range are reduced. The narrowband antenna can match only over the ISM 902MHz-928MHz band but also exhibits increased length, gain, and read range.

## BIBLIOGRAPHY

- [1] G. Gonzalez, *Microwave Transistor Amplifiers: Analysis and Design*, Upper Saddle River, NJ.: Prentice Hall, 1997.
- [2] S. Uda and Y. Mushiake, *Yagi-Uda Antenna*, Sendai, Japan: Research Institute of Electrical Communication, Tohoku University, 1954.
- [3] D. Deavours, "Analysis and Design of Wideband Passive UHF RFID Tags Using a Circuit Model," *IEEE International Conference on RFID*, pp. 283-290, 2009.
- [4] J. O. Sophocles, *Electromagnetic Waves and Antennas*, Piscataway, NJ: Rutgers University, 2008.
- [5] P. Cole, Z. Hu and Y. Wang, "Operating Range Evaluation of RFID Systems," in *Advanced Radio Frequency Identification Design and Applications*, Intech, 2011.
- [6] P. Penfield, "Noise in negative resistance amplifiers," *IEEE Trans. Circuit Theory*, Vols. CT-7, no. 2, pp. 166-170, 1960.
- [7] "Monza 4 Datasheet," Impinj, Seattle, WA, 2014.
- [8] "Higgs 3 IC Datasheet," Alien Technology, Morgan Hill, CA, 2014.
- [9] "Higgs 4 IC Datasheet," Alien Technology, Morgan Hill, CA, 2012.
- [10] "The Global Language of Business," 4 July 2016. [Online]. Available: [http://www.gs1.org/docs/epc/UHF\\_Regulations.pdf](http://www.gs1.org/docs/epc/UHF_Regulations.pdf).
- [11] R. Hansen, "Folded and T-Match Dipole Transformation Ratio," *IEEE Trans. Antenna Propagat.*, Vols. AP-30, no. 1, pp. 161-162, 1982.
- [12] W. Smythe, *Static and Dynamic Electricity*, New York: McGraw-Hill Book Company, Inc., 1950.
- [13] R. W. Lampe, "Design Formulas for an Asymmetrical, Coplanar Strip, Folded Dipole," *IEEE Trans. Antenna Propagat.*, Vols. Ap-33, no. 9, pp. 1028-1031, 1985.
- [14] K. Demarest, "Limitations of the Uda Model for T-Match Antennas," *Progress In Electromagnetics Research*, vol. 113, pp. 1-15, 2011.

- [15] M. T. Reich and C. Bauer-Reich, "UHF RFID Impedance Matching: When is a T-Match Not a T-Match?," in *IEEE International Conference on RFID*, Orlando, 2014.
- [16] D. Deavours, "Analysis and Design of Wideband Passive UHF RFID Tags Using a Circuit Model," in *IEEE International Conference on RFID*, 2009.
- [17] D. M. Dobkins, *The RF in RFID: UHF RFID in Practice*, Burlington, MA: Newnes, 2007.
- [18] M. Hamid and R. Hamid, "Equivalent Circuit of Dipole Antenna of Arbitrary Length," *IEEE Trans. Antennas Propagat.*, vol. 45, no. 11, pp. 1695-1696, 1997.
- [19] D. Miron, *Small Antenna Design*, Burlington, MA: Elsevier, Inc., 2006.
- [20] C. A. Balanis, *Antenna Theory: Analysis and Design*, 3rd Edition, Hoboken, NJ: Wiley-Interscience, 2005.
- [21] D. Deavours and D. Dobkin, "UHF Passive RFID Tag Antennas," in *Microstrip and Printed Antennas: New Trends, Techniques and Applications*, John Wiley & Sons, Ltd., 2010, pp. 263-303.
- [22] A. Yaghjian and S. Best, "Impedance, Bandwidth, and Q of Antennas," *IEEE Trans. on Antennas and Propagat.*, vol. 53, no. 4, pp. 1298-1324, 2005.
- [23] T. Endo, Y. Sunahara, S. Satoh and T. Katagi, "Resonant Frequency and Radiation Efficiency of Meander Line Antennas," *Electronics and Communications in Japan, Part 2*, vol. 83, no. 1, pp. 52-58, 2000.
- [24] X. Hu, P. Cole and L. Zhang, "A Method for Calculating the Resonant Frequency of Meander-line Dipole Antenna," in *IEEE Conference on Industrial Electronics and Applications*, Xi'an, China, 2009.
- [25] W. L. Stutzman and G. Thiele, *Antenna Theory and Design*, Third Edition, Hoboken, NJ: Wiley, 2013.
- [26] G. Breed, "The Fundamentals of Patch Antenna Design and Performance," *High Frequency Electronics*, pp. 48-51, March 2009.
- [27] R. Meys and F. Janssens, "Measuring the Impedance of Balanced Antennas by an S-Parameter Method," *IEEE Antennas Propagat. Magazine*, vol. 40, no. 6, pp. 62-65, 1998.

- [28] P. Nikitin, S. Rao, R. Martinez and S. Lam, "Sensitivity and Impedance Measurements," *IEEE Trans. Microwave Theory and Techniques*, vol. 57, no. 5, pp. 1297-1302, 2009.
- [29] T. Björninen, M. Lauri, L. Ukkonen, R. Ritala, A. Elsherbeni and L. Sydänheimo, "Wireless Measurement of RFID IC Impedance," *IEEE Trans. Instrumentation and Measurement*, vol. 60, no. 9, pp. 9194-3206, 2011.
- [30] N. L. Rao, "Antenna Design for UHF RFID Tags: A Review and a Practical Application," *IEEE Trans. Antennas Propagat.*, vol. 53, no. 12, pp. 3870-3876, December 2005.
- [31] B. C. Wadell, *Transmission Line Design Handbook*, Boston, Massachusetts: Artech House, 1991.
- [32] M. Iskander, *Electromagnetic Fields and Waves*, Long Grove, Illinois: Waveland Press, Inc., 2013.

## APPENDICES

### APPENDIX A: Characteristic Impedance $Z_{char}$

Characteristic impedance for two coplanar strips on a dielectric slab of finite thickness (Figure A1) [31] is required to evaluate the T-match odd mode impedance.

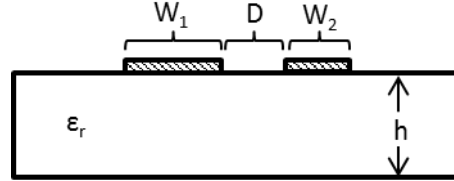


Figure A1: Asymmetric Coplanar Strip Cross-Section on Dielectric Slab of Finite Height and no Ground Plane

Characteristic impedance for asymmetric coplanar strips on a dielectric slab of finite height is [31]:

$$Z_{char} = \frac{60\pi}{\sqrt{\epsilon_{eff}}} \frac{K(k)}{K(k')}, \quad (\text{A.1})$$

$K(k)$  and  $K(k')$  are complete elliptic integrals of the first kind.  $K(k)/K(k')$  is approximated to an accuracy of  $10^{-11}$  as [31]:

$$\frac{K(k)}{K(k')} \cong \begin{cases} 2\pi \ln \left( 2 \frac{\sqrt{1+k} + \sqrt[4]{4k}}{\sqrt{1+k} - \sqrt[4]{4k}} \right)^{-1}, & 0 \leq k \leq \frac{1}{\sqrt{2}} \\ \frac{1}{2\pi} \ln \left( 2 \frac{\sqrt{1+k'} + \sqrt[4]{4k'}}{\sqrt{1+k'} - \sqrt[4]{4k'}} \right), & \frac{1}{\sqrt{2}} \leq k \leq 1 \end{cases} \quad (\text{A.2})$$

$$\epsilon_{eff} = 1 - \frac{(\epsilon_r - 1)}{2} \frac{K(k)}{K(k')} \frac{K(k_1)}{K(k'_1)}, \quad (\text{A.3})$$

$$k = \sqrt{\frac{D}{b} \left( 1 + \frac{b}{d} - \frac{D}{d} \right)}, \quad (\text{A.4})$$

$$k_1 = \sqrt{\frac{(t_1 - t_2)(t_3 - t_2)}{(t_1 + t_2)(t_3 + t_2)}}, \quad (\text{A.5})$$

$$t_n = \frac{e^{\lambda_n-1}}{e^{\lambda_n+1}} \text{ where } n = 1,2,3, \quad (\text{A.6})$$

$$\lambda_1 = \frac{\pi}{2} \left( \frac{2W_2}{h} + \frac{D}{h} \right), \quad (\text{A.7})$$

$$\lambda_2 = \frac{\pi D}{2h}, \quad (\text{A.8})$$

$$\lambda_3 = \frac{\pi}{2} \left( \frac{2W_1}{h} + \frac{D}{h} \right), \quad (\text{A.9})$$

$$k' = \sqrt{1 - k^2}, \quad (\text{A.10})$$

$$k'_1 = \sqrt{1 - k_1^2}, \quad (\text{A.11})$$

$$b = W_2 + D, \quad (\text{A.12})$$

$$d = W_1 + D. \quad (\text{A.13})$$

The characteristic impedance of coplanar strips is used to derive the T-match odd mode impedance.



## APPENDIX B: Capacitance Per Unit Length for Two Cylindrical Conductors

A line of point charges  $-q$  is parallel to the  $z$ -axis at  $x=y=0$  (Figure B1), and a line of positive point charges  $q$  is parallel to the  $z$ -axis at location  $\vec{R}$ . Potential  $\phi(\vec{x})$  per unit length at location  $\vec{x}$  is found by using Coulomb's Law [32] and adding potentials from the two charges:

$$\phi(\vec{x}) = \frac{-q}{2\pi\epsilon_0} [\ln|\vec{x} - \vec{R}| - \ln|\vec{x}|] = \frac{q}{2\pi\epsilon_0} \ln \frac{|\vec{x}|}{|\vec{x} - \vec{R}|} \quad (\text{B.1})$$

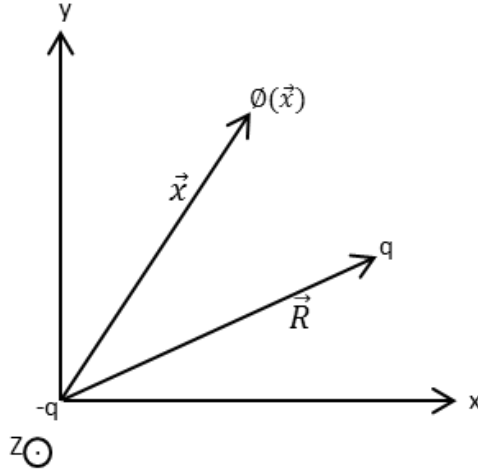


Figure B1: Z-axis Cross-section Positive and Negative Line Charges

Define equipotential surface  $\phi = V$ . Rearranging (B.1):

$$e^{(2\pi\epsilon_0 V/q)} = \frac{|\vec{x}|}{|\vec{x} - \vec{R}|} \quad (\text{B.2})$$

Substituting  $\zeta = e^{(2\pi\epsilon_0 V/q)}$  in (B.2) yields:

$$|\vec{x} - \vec{R}| = \zeta |\vec{x}| \quad (\text{B.3})$$

Squaring both sides of (B.3) and rearranging terms:

$$(1 - \zeta^2)x^2 - 2\vec{x} \cdot \vec{R} + R^2 = 0 \quad (\text{B.4})$$

Completing the square, the circle equation is:

$$\left| \vec{x} - \frac{\vec{R}}{1 - \zeta^2} \right|^2 = \frac{\zeta^2 R^2}{(1 - \zeta^2)^2} \quad (\text{B.5})$$

A circle is represented by  $|\vec{x} - \vec{x}_o|^2 = \rho^2$ . The center of the circle,  $\vec{x}_o$  is:

$$\vec{x}_o = \frac{\vec{R}}{1 - \zeta^2} = \frac{\vec{R}}{1 - e^{(-4\pi\epsilon_0 V/q)}} \quad (\text{B.6})$$

And the radius  $\rho$  is:

$$\rho = \frac{R\zeta}{|1 - \zeta^2|} = \frac{R}{2|\sinh(2\pi\epsilon_0 V/q)|} \quad (\text{B.7})$$

Hence, the equipotential surface of a line charge is a circle. For two distinct line charges, the two equipotential circles (Figure B2) are represented as the surfaces of two conductors where the separation distance  $d$  is greater than the sum of the two radii  $a$  and  $b$ .

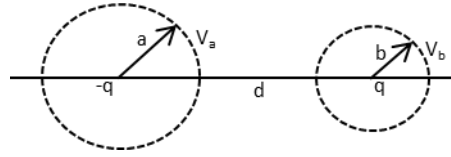


Figure B2: Two Equipotential Surfaces for Opposite Polarity Line Charges

Capacitance is the ratio of charge to voltage. The voltage between the two conductors is  $V_b - V_a$ . capacitance per unit length is:

$$C = \frac{q}{V_b - V_a} \quad (\text{B.8})$$

To express the capacitance in terms of conductor geometry, distance  $d$  is defined as the difference between the origins of the two charges:

$$\begin{aligned} d = |\vec{x}_{bo} - \vec{x}_{ao}| &= \frac{R}{1 - e^{-4\pi\epsilon_0 V_b/q}} - \frac{R}{1 - e^{-4\pi\epsilon_0 V_a/q}} \\ &= R \frac{e^{-4\pi\epsilon_0 V_b/q} - e^{-4\pi\epsilon_0 V_a/q}}{[1 - e^{-4\pi\epsilon_0 V_b/q}][1 - e^{-4\pi\epsilon_0 V_a/q}]} = \frac{R}{2} \frac{\sinh\left(\frac{2\pi\epsilon_0(V_a - V_b)}{q}\right)}{\sinh\left(\frac{2\pi\epsilon_0 V_b}{q}\right) \sinh\left(\frac{2\pi\epsilon_0 V_a}{q}\right)}. \end{aligned} \quad (\text{B.9})$$

Rewritten:

$$\frac{2d}{R} \sinh\left(\frac{2\pi\epsilon_o V_b}{q}\right) \sinh\left(\frac{2\pi\epsilon_o V_a}{q}\right) = \sinh\left(\frac{2\pi\epsilon_o (V_a - V_b)}{q}\right) \quad (\text{B.10})$$

From (B.7) when radius  $\rho=a$ :

$$\sinh\left(\frac{4\pi\epsilon_o V_a}{q}\right) = \frac{R}{2a} \quad (\text{B.11})$$

From (B.7) when radius  $\rho=b$ :

$$\sinh\left(\frac{4\pi\epsilon_o V_b}{q}\right) = -\frac{R}{2b} \quad (\text{B.12})$$

The following trigonometric identity is used:

$$\cosh(\xi_a - \xi_b) = \cosh(\xi_a) \cosh(\xi_b) - \sinh(\xi_a) \sinh(\xi_b) \quad (\text{B.13})$$

Rearranging (B.13):

$$\cosh(\xi_a - \xi_b) + \sinh(\xi_a) \sinh(\xi_b) = \cosh(\xi_a) \cosh(\xi_b) \quad (\text{B.14})$$

Squaring both sides of (B.14):

$$\begin{aligned} \cosh^2(\xi_a - \xi_b) + 2 \cosh(\xi_a - \xi_b) \sinh(\xi_a) \sinh(\xi_b) + \sinh^2(\xi_a) \sinh^2(\xi_b) \\ = \cosh^2(\xi_a) \cosh^2(\xi_b) \end{aligned} \quad (\text{B.15})$$

Using the hyperbolic trigonometric identity  $\cosh^2(\xi) = 1 + \sinh^2(\xi)$  on both right-hand terms of (B.15):

$$\begin{aligned} \cosh^2(\xi_a - \xi_b) + 2 \cosh(\xi_a - \xi_b) \sinh(\xi_a) \sinh(\xi_b) + \sinh^2(\xi_a) \sinh^2(\xi_b) \\ = [1 + \sinh^2(\xi_a)][1 + \sinh^2(\xi_b)] \\ = 1 + \sinh^2(\xi_a) + \sinh^2(\xi_b) + \sinh^2(\xi_a) \sinh^2(\xi_b) \end{aligned} \quad (\text{B.16})$$

Rearranging terms and applying  $\cosh^2(\xi_a - \xi_b) = 1 + \sinh^2(\xi_a - \xi_b)$  to (B.16):

$$2 \cosh(\xi_a - \xi_b) \sinh(\xi_a) \sinh(\xi_b) = \sinh^2(\xi_a) + \sinh^2(\xi_b) - \sinh^2(\xi_a - \xi_b) \quad (\text{B.17})$$

The final form is:

$$\cosh(\xi_a - \xi_b) = \frac{1}{2} \left[ \frac{\sinh(\xi_a)}{\sinh(\xi_b)} + \frac{\sinh(\xi_b)}{\sinh(\xi_a)} - \frac{\sinh^2(\xi_a - \xi_b)}{\sinh(\xi_a) \sinh(\xi_b)} \right] \quad (\text{B.18})$$

Letting  $\xi = 4\pi\epsilon_o V/q$  and substituting (B.10), (B.11) and (B.12) into (B.18) yields:

$$\cosh\left(\frac{2\pi\epsilon_o(V_a - V_b)}{q}\right) = \frac{1}{2} \left[ -\frac{a}{b} - \frac{b}{a} + \frac{d^2}{ab} \right] = \frac{d^2 - a^2 - b^2}{2ab} \quad (\text{B.19})$$

The capacitance per unit length is (B.8) is solved:

$$C = \frac{q}{V_b - V_a} = 2\pi\epsilon_o \left[ \cosh^{-1} \left( \frac{d^2 - a^2 - b^2}{2ab} \right) \right]^{-1} \quad (\text{B.20})$$

If the two radii are equal, the capacitance reduces to:

$$C = 2\pi\epsilon_o \left[ \cosh^{-1} \left( \frac{d^2 - a^2 - b^2}{2ab} \right) \right]^{-1} = 2\pi\epsilon_o \left[ \cosh^{-1} \left( \frac{d^2 - 2a^2}{2a^2} \right) \right]^{-1} \quad (\text{B.21})$$

If  $d \gg a$  then:

$$C = 2\pi\epsilon_o \left[ \cosh^{-1} \left( \frac{d^2 - 2a^2}{2a^2} \right) \right]^{-1} \approx 2\pi\epsilon_o \left[ \cosh^{-1} \left( \frac{d^2}{2a^2} \right) \right]^{-1} \quad (\text{B.22})$$

Applying  $\cosh^{-1}(x) = \ln[x + \sqrt{x^2 - 1}]$  yields:

$$\begin{aligned} C &\approx 2\pi\epsilon_o \left[ \cosh^{-1} \left( \frac{d^2}{2a^2} \right) \right]^{-1} \approx 2\pi\epsilon_o \ln \left[ d^2/2a^2 + \sqrt{(d^2/2a^2)^2 - 1} \right] \\ &\approx \pi\epsilon_o \ln(d/a) \end{aligned} \quad (\text{B.23})$$

APPENDIX C:  $\cosh^{-1}(x) = \ln(x + (x^2 - 1)^{1/2})$

This conversion is useful for simplifying capacitance per unit length of two cylindrical conductors.

$$2x^2 + 2x\sqrt{x^2 - 1} + 1 = 2x^2 + 2x\sqrt{x^2 - 1} + 1 \quad (\text{C.1})$$

$$2x^2 + 2x\sqrt{x^2 - 1} = \left[ x^2 + 2x\sqrt{x^2 - 1} + (x^2 - 1) \right] + 1 \quad (\text{C.2})$$

$$2x(x + \sqrt{x^2 - 1}) = (x + \sqrt{x^2 - 1})(x + \sqrt{x^2 - 1}) + 1 \quad (\text{C.3})$$

$$2x = (x + \sqrt{x^2 - 1}) + \frac{1}{(x + \sqrt{x^2 - 1})} \quad (\text{C.4})$$

$$2x = e^{\ln(x + \sqrt{x^2 - 1})} + e^{-\ln(x + \sqrt{x^2 - 1})} \quad (\text{C.5})$$

$$x = \frac{e^{\ln(x + \sqrt{x^2 - 1})} + e^{-\ln(x + \sqrt{x^2 - 1})}}{2} \quad (\text{C.6})$$

$$x = \cosh \left[ \ln(x + \sqrt{x^2 - 1}) \right] \quad (\text{C.7})$$

Therefore,

$$\cosh^{-1}(x) = \ln(x + \sqrt{x^2 - 1}) \quad (\text{C.8})$$

## APPENDIX D: Splitting Factor in Odd Mode, Two-Port Network Analysis of T-Match Structure

The T-match is analyzed as a two-port, even and odd mode network (Figure D1). A reciprocal network has equal voltages at ports 1 and 2 when equal currents are applied to both ports. In a symmetrical network, input impedance equals output impedance. The T-Match network is reciprocal, but not symmetric. Hence,  $Z_{12}$  equals  $Z_{21}$ , but  $Z_{11}$  does not equal  $Z_{22}$ .  $V_1$  represents source even and odd voltages,  $V_{e1}$  and  $V_{o1}$ , and  $V_2$  represents voltages  $V_{e2}$  and  $V_{o2}$ . The generalized two-port network equation is:

$$\mathbf{V} = \mathbf{Z}\mathbf{I} \quad (\text{D.1})$$

$$\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix} \quad (\text{D.2})$$

Applying reciprocal network conditions,  $Z_{12} = Z_{21}$ :

$$\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{12} & Z_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix} \quad (\text{D.3})$$

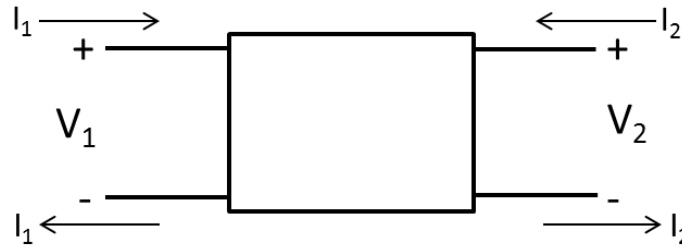


Figure D1: Two Port Network

In common mode operation,  $V_1 = V_2$  and  $I_2 = \alpha I_1$ ; (D.3) becomes:

$$\begin{bmatrix} V_1 \\ V_1 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{12} & Z_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ \alpha I_1 \end{bmatrix} \quad (\text{D.4})$$

Equating upper and lower rows:

$$V_1 = Z_{11}I_1 + Z_{12}\alpha I_1 = Z_{12}I_1 + Z_{22}\alpha I_1 \quad (D.5)$$

The splitting factor in terms of Z parameters is:

$$\alpha = \frac{Z_{11} - Z_{12}}{Z_{22} - Z_{12}} \quad (D.6)$$

From Section 2.1 (2-2), the even mode impedance is:

$$Z_e = \frac{V_e}{(1 + \alpha)I_e} \quad (D.7)$$

Since  $V_e = V_1$  and  $I_e = I_1$ , substituting  $\alpha$  for the two-port network yields:

$$Z_e = \frac{Z_{11}I_1 + Z_{12}\alpha I_1}{I_1 + \alpha I_1} = \frac{Z_{11} + \alpha Z_{12}}{1 + \alpha} = \frac{Z_{11}Z_{22} - Z_{12}^2}{Z_{11} + Z_{22} - 2Z_{12}} \quad (D.8)$$

For the differential mode,  $I_1 = -I_2$ ; equation (D.3) becomes:

$$\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} \\ Z_{12} & Z_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ -I_1 \end{bmatrix} \quad (D.9)$$

Defining  $V_1 = V_{o1}$  and  $V_2 = V_{o2}$ :

$$V_{o1} = Z_{11}I_1 - Z_{12}I_1 \quad (D.10)$$

$$V_{o2} = Z_{12}I_1 - Z_{22}I_1$$

Forming the ratio  $V_{o1}/V_{o2}$ :

$$\frac{V_{o1}}{V_{o2}} = \frac{Z_{11} - Z_{12}}{Z_{12} - Z_{22}} \quad (D.11)$$

$$V_{o1} = \frac{Z_{11} - Z_{12}}{Z_{12} - Z_{22}} V_{o2} = -\frac{Z_{11} - Z_{12}}{Z_{22} - Z_{12}} V_{o2} \quad (D.12)$$

Using (D.6):

$$V_{o1} = -\alpha V_{o2} \quad (\text{D.13})$$

Equation (D.13) is used to solve the T-Match input impedance in Section 2.1.



## APPENDIX E: Inductive Loop Circuit Transformation

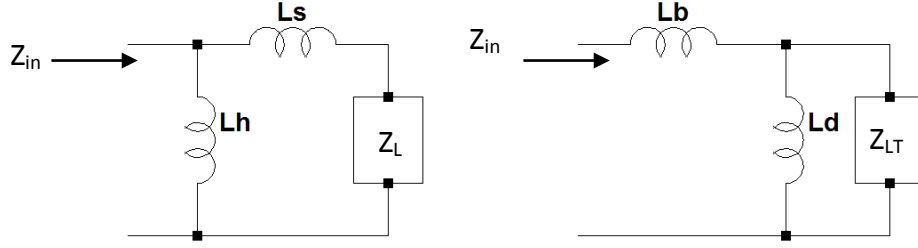


Figure E.1: (a) Original Circuit, (b) Transformed Circuit

The input impedance  $Z_{in}$  from Figure E.1a is:

$$Z_{in} = \frac{(Z_L + Z_s)Z_h}{Z_L + Z_s + Z_h} = \frac{Z_h Z_L + Z_h Z_s}{Z_L + Z_s + Z_h} \quad (E.1)$$

which is re-written in the form:

$$Z_{in} = \left[ \frac{Z_h}{Z_s + Z_h} \right]^2 \frac{Z_L(Z_s + Z_h)}{(Z_L + Z_s + Z_h)} + \frac{Z_h Z_s}{Z_s + Z_h} \quad (E.2)$$

The input impedance of Figure E.1b is:

$$Z_{in} = \frac{Z_{LT}X_d}{Z_{LT} + X_d} + X_b \quad (E.3)$$

Let:

$$\beta = \frac{X_h}{X_h + X_s} = \frac{L_h}{L_h + L_s} \quad (E.4)$$

Equate (E.2) to (E.3) and compare similar terms to find  $X_b$ ,  $L_b$ ,  $Z_{LT}$  and  $X_d$ :

$$X_b = \frac{X_h X_s}{X_s + X_h} \quad (E.5)$$

$$L_b = \frac{L_h L_s}{L_h + L_s} \quad (E.6)$$

$$Z_{LT} = \beta^2 Z_L \quad (E.7)$$

$$X_d = \beta^2 (X_h + X_s) \quad (E.8)$$

(E.8) reduces to:

$$L_d = \beta^2(L_h + L_s) \quad (\text{E.9})$$