ADAPTIVE COMPONENT USAGE FOR THE THERMAL MANAGEMENT OF PICOSATELLITES

A Thesis
presented to
the Faculty of California Polytechnic State University,
San Luis Obispo

In Partial Fulfillment
of the Requirements for the Degree
Master of Science in Aerospace Engineering
by
William Dudley Whalen
June 2011
COMMITTEE MEMBERSHIP

TITLE: Adaptive Component Usage for the Thermal Management of Picosatellites

AUTHOR: William Dudley Whalen

DATE SUBMITTED: June 2011

COMMITTEE CHAIR: Dr. Jordi Puig-Suari, PhD

COMMITTEE MEMBER: Dr. Kira Abercromby, PhD

COMMITTEE MEMBER: Dr. Kristina Jameson, PhD

COMMITTEE MEMBER: Dr. Robert Crockett, PhD
The CubeSat standard originated in 1999. It was a joint development led by Dr. Jordi Puig-Suari of California State Polytechnic University San Luis Obispo and Professor Robert Twiggs of Stanford University. The engineering challenges that have come from this picosatellite class have created incredible educational opportunities for engineering students throughout the world. Since the challenges of engineering a CubeSat abound the designers are always looking at novel and even revolutionary solutions to each one. One of those opportunities is in thermal subsystem design, implementation and characterization. A potential solution for CubeSats is adaptive component usage.

This thesis is the written catalogue of my study of adaptive component utilization to solve the thermal management problem inherent in picosatellites. Inside the limited design space of a picosatellite’s electrical, mechanical and software subsystems active spacecraft thermal control often is a necessary forfeiture. This does not preclude CubeSat teams from addressing the thermal aspect of spacecraft design. To the contrary it forces them down a different route to ensuring the spacecraft is verified to meet appropriate environmental constraints. Most CubeSat teams, Cal Poly included, use punishing qualification testing, robust system design and a restricted spacecraft operational lifespan ensure their system will operate through all of the environments it will encounter during launch, separation, spacecraft activation and on until the end of operations.

The testing, engineering and modeling I performed were to answer the hypothesis, can a standard* 1-U CubeSat utilize existing hardware and software to improve its thermal condition and operational lifetime?

This hypothesis assumes thermal control or situational improvement would have to be gained without the addition of thermal control surfaces, active heaters, heat pipes or louvers and no additional flight software. Ground control software and operation alterations were explicitly not included in these assumptions.

The thesis began with defining the many unknowns that existed in the material properties. This required: research into the methods required, specialized measurement hardware to be obtained and set-up, controlled measurements to be taken and thorough testing procedures to be developed. Once the unknowns were better defined the thesis required a detailed satellite thermal analysis by
multiple methods along with both thermal vacuum chamber simulation trials and finally on-orbit testing.

Based on the research, modeling and testing performed and results obtained through this study, yes, a standard* 1-U CubeSat utilizing existing hardware and software can improve its thermal condition and operational lifetime. As is shown in Section 3.0 and discussed in detail in Section 4.0, utilizing only the onboard electronics and existing flight software the orbital temperature delta that components are experiencing can be reduced by up to 35.8%. Further analyses in section 4.0 use the temperature data to show that by lowering the temperature deltas the satellite does in fact have the capability to both improve its lifetime and certain key subsystem performance parameters.

Keywords: Thermal, CubeSat, Picosatellite, Heat, Radiation.
ACKNOWLEDGMENTS

To my parents, your love and constant support have made me who I am today. Thanks for always supporting me no matter what.

For my family, Emily, thanks for putting up with my timeline, changing diapers while I typed and taking long walks with O to the park and around town to help me get this done, I never could have finished this were it not for your sacrifice and effort. Owen, thanks for understanding when dad wasn’t much fun and had to sit at the computer for days on end. I know being two years old and having to be quiet in the house so Dad could study was probably harder than writing this thesis but I appreciate it and love you more than the world itself.

To Dr. P, thank you for the inspiration in the lab and keeping the whole CubeSat and PolySat team on the cutting edge of CubeSats. Thank you for believing in me as my advisor and for believing in the value of rigorous testing (and for buying me enough dewars of liquid nitrogen to put this hypothesis to the test)! Lastly, thanks for your vision and work with Prof. Twiggs, without it I never would have had the incredible opportunity to test and command satellites while still in school.

For Roland, you are a mentor, a friend and a constant source of optimism. CubeSat wouldn’t be what it is without you and I never would have learned how to operate the T-VAC were it not for your time and patience.

Dr. A, I know you didn’t need any extra work during the weeks before finals but you were willing to sign on anyway. Thank you for making the time to be a part of my committee and helping me make this thesis a good piece of engineering!

To all of my friends in the lab - Justin, Hobbes, Bryan, Parin, Austin, Keith, Lori, Jon, David and Ryan. You have all helped me in one way or another and I thank you for it. I cherish all the memories we made in the ATL over those four years. Anyone who said working on satellites is not fun never worked with our team and obviously did not work on small enough satellites.

Dr. J, thank you for being willing to take time from your own research to help me. Your expertise brought a lot to my committee and without your willingness to jump in I wouldn’t have been able to get this finished!

The “Foundation” – Kyle, Spencer, Chris, Jacob, Noe, Derek, Kalia, Armen, Simon and Roland. Thank you for going through all the challenges that came before, for never giving up, for the time and hard work you spent. Thank you for listening to my questions, for helping me with data, for always being there. Nanos gigantium humeris insidentes my friends.

Dr. Crockett, your systems engineering classes were amazing and your passion for the subject helped me become the engineer I am today. Thank you again for agreeing to sign up to my committee.
# TABLE OF CONTENTS

LIST OF FIGURES .................................................................................................................. X

LIST OF TABLES ..................................................................................................................... XV

LIST OF COMMON ACRONYMS & ABBREVIATIONS .......................................................... XVII

1.0 INTRODUCTION ............................................................................................................. 1

    1.1 The History and Background of CubeSats ................................................................. 1

    1.2 CubeSat Engineering Challenges and Design Evolutions; Divergence from
        Traditional Satellite Analysis and Design ................................................................. 7

2.0 THESIS MOTIVATION, DRIVERS, GENERAL REASONING AND HYPOTHESIS

    DEFINITION ..................................................................................................................... 12

        2.1 Thesis Motivation ................................................................................................. 12

        2.2 Thesis Drivers ..................................................................................................... 13

        2.3 General Reasoning, Hypothesis Definition, Assumptions and Verification Criteria .... 15

            2.3.1 General Reasoning ......................................................................................... 15

            2.3.2 Hypothesis Definition ...................................................................................... 16

            2.3.3 Hypothesis Assumptions ............................................................................... 17

            2.3.4 Hypothesis Verification ................................................................................... 17

3.0 THESIS BODY; ANALYSES, TESTING & MODELING REVIEW .................................... 20

    3.1 CubeSat Thermal Environment Simulations ............................................................... 20

    3.2 Defining the Unknowns, Material Properties and Variations ....................................... 24

        3.2.1 6-Layer Flame Retardant #4 Printed Circuit Boards (PCBs) Radiative
            Properties ............................................................................................................... 24

        3.2.2 TAIYO PSR-4000BN Liquid Photoimageable Solder Mask Radiative
            Properties .................................................................................................................. 36

            3.2.2.1 A Thermal Vacuum Test Method for Obtaining Emissivity Values ............ 37
<table>
<thead>
<tr>
<th>Section</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.2.2.2 An Improved Emissivity Test Method Via the RAYTEK MI40</td>
<td>45</td>
</tr>
<tr>
<td>3.2.3 Conformal Coat (IPC A-610 Compliant) Radiative Properties</td>
<td>53</td>
</tr>
<tr>
<td>3.2.4. Improved Triple Junction (ITJ) and Ultra Triple Junction (UTJ) Solar Cells</td>
<td>55</td>
</tr>
<tr>
<td>3.3 Thermal Analyses and Models</td>
<td>57</td>
</tr>
<tr>
<td>3.3.1 P’s Office</td>
<td>57</td>
</tr>
<tr>
<td>3.3.2 Satellite Thermal Analysis Overview</td>
<td>58</td>
</tr>
<tr>
<td>3.3.3 General Thermal Equilibrium Calculation (Analytical Baseline)</td>
<td>70</td>
</tr>
<tr>
<td>3.3.4 Heat Generation by Mode</td>
<td>77</td>
</tr>
<tr>
<td>3.3.5 Heat Flux Path Mapping via Resistor Model</td>
<td>87</td>
</tr>
<tr>
<td>3.3.6 Finite Element Model Generation and Solutions</td>
<td>92</td>
</tr>
<tr>
<td>3.3.7 Finite Difference Models TRASYS/TSS-SINDA</td>
<td>104</td>
</tr>
<tr>
<td>3.4 Active Thermal Vacuum Testing</td>
<td>106</td>
</tr>
<tr>
<td>3.5 On-Orbit Commanding of Modes 1, 2a and 5</td>
<td>129</td>
</tr>
<tr>
<td>4.0 THESIS RESULTS AND CONCLUSIONS</td>
<td>134</td>
</tr>
<tr>
<td>4.1 Review of Results Against Verification Criteria</td>
<td>134</td>
</tr>
<tr>
<td>4.2 Future Studies</td>
<td>143</td>
</tr>
<tr>
<td>4.2.1 Commanding</td>
<td>143</td>
</tr>
<tr>
<td>4.2.2 Passive Thermal Control Improvements</td>
<td>145</td>
</tr>
<tr>
<td>4.2.3 Improved Capabilities with Increased Power Budgets</td>
<td>145</td>
</tr>
<tr>
<td>REFERENCES</td>
<td>148</td>
</tr>
<tr>
<td>APPENDIX A. CP3 POWER STORAGE THERMAL &amp; LIFE PROPERTIES</td>
<td>151</td>
</tr>
<tr>
<td>APPENDIX B. ISOLA FR406 PCB MATERIAL PROPERTIES SHEET</td>
<td>152</td>
</tr>
<tr>
<td>APPENDIX C CP3 PAYLOAD (KAC-9638/48) THERMAL PROPERTIES</td>
<td>154</td>
</tr>
<tr>
<td>APPENDIX D – POPULATED PCB CALCULATIONS &amp; ADJUSTMENTS</td>
<td>156</td>
</tr>
</tbody>
</table>
APPENDIX E. SIERRA PROTO EXPRESS SOLDER MASK PROPERTIES ........................................160
APPENDIX F. INITIAL CALCULATIONS (HISTORICAL REFERENCE) ......................................161
APPENDIX G. PROJECTED AREA CALCULATIONS ..............................................................162
APPENDIX H. THERMAL EQUILIBRIUM WORST-CASE HOT/COLD ........................................164
APPENDIX I. THERMAL RESISTANCE CALCULATIONS .......................................................167
APPENDIX J. TSS AND SINDA MODELING ........................................................................168
APPENDIX K. SMC THERMAL GRADIENT FAILURE RATES ..................................................176
APPENDIX L. RSSI – TEST PROCEDURE & RESULTS ..........................................................179
APPENDIX M. COMPONENT TEMPERATURE SENSITIVITIES .................................................190
APPENDIX N. NASA THERMAL CYCLE GUIDELINES PT-TE-140230 ....................................196
APPENDIX O. TEMPERATURE MEASUREMENT ON THE CP BUS37 ......................................198
APPENDIX P. GENESAT-1 TELEMETRY AND TEMPERATURE RANGES .................................202
LIST OF FIGURES

Figure 1. CubeSat Specification Revision 12 .................................................................2
Figure 2. The Poly-Picosatellite Orbital Deployer (P-POD) .........................................3
Figure 3. QuakeSat – Eurockot 2003 Launch ................................................................4
Figure 4. Colony Triple Cube Spacecraft – Falcon 9 Launch ...........................................4
Figure 5. CP6 – Minotaur 2009 Launch ........................................................................5
Figure 6. NanoSail-D CubeSat Deployed with Team1 ....................................................6
Figure 7. Weighted Effectiveness of Major Spacecraft Tests7 .........................................23
Figure 8. PC104 Standard Board Layout and Mechanical Requirements8 ......................25
Figure 9. PC104 Horizontal Layout Example8 ...............................................................26
Figure 10. An NPS CubeSat PC104 Stack9 ..................................................................26
Figure 11. CP2 “Bus” System Stack ..............................................................................27
Figure 12. CP3 Bus and Payload System Stack ...............................................................28
Figure 13. Populated CP2 Power (EP&H) Board ...........................................................29
Figure 14. Surface Mount Resistor Size Values10 ..........................................................30
Figure 15. Surface Mount Capacitor Size Values11 ........................................................31
Figure 16. Maxim 1239MEEE Package E16-4 Outline12 ..............................................32
Figure 17. AD627AR Package Outline and Dimensions13 ............................................33
Figure 18. Emissivity/Absorptivity TVAC Test with Thermistor Locations .........................38
Figure 19. Teflon Thermal Standoff Illustration Pre-Shroud & with Shroud .........................41
Figure 20. Heat Flux (BTU/hr) vs Test Article TC vs Pressure14 .....................................42
Figure 21. Emissivity Reference Panel from MI40 Operator’s Guide .................................47
Figure 22. Raytek Thermalert MI40 w/ Breakouts Wires Installed .................................47
Figure 23. Raytek Thermalert MI40 Post Emissivity Adjustment .......................................48
Figure 24. IR Sensor Protective Cover and Cover Removed for Testing .............................49
Figure 25. Payload Face Board used for Testing (pre-3M Taping) ......................................50
Figure 26. Payload Face Board used for Testing (post-3M Taping) ....................................50
Figure 27. Sensor “Spot Diameter” versus Distance from Raytek Operators Guide. .................51
Figure 28. Solder Mask Emissivity Test Comparison ..................................................................53
Figure 29. Spectrolab ITJ and UTJ Cell Radiative Values16 ..................................................55
Figure 30. Incident Radiation on a Surface2 ............................................................................59
Figure 31. Corrected Satellite Surface Property Averages (Satellite Whole)...............................62
Figure 32. Solar Albedo Calculation Chart2 ............................................................................65
Figure 33. Earthshine IR Calculation Chart2 ...........................................................................67
Figure 34. Earthshine IR for a LEO Spacecraft (Sample)2 ......................................................68
Figure 35. Combined On-Orbit Heat Loads2 ..........................................................................68
Figure 36. Worst Case Cold Condition ....................................................................................69
Figure 37. Worst Case Hot Condition ....................................................................................69
Figure 38. Satellite Area Projection Calculator – Surface Area vs. Attitude..............................71
Figure 39. External On-Orbit Temperature Maximums (Externals) ........................................72
Figure 40. External On-Orbit Temperature Maximums (Externals) ........................................74
Figure 41. Beta Angle Plot (v. Time) .......................................................................................76
Figure 42. CP3 Lighting Conditions (12 Hour Plot) ................................................................77
Figure 43. Baseline Three Hour Post-Launch Run (Detail View) ............................................82
Figure 44. CP3 with 1.062W “Standard Operations” Internal Heat Dissipation .......................83
Figure 45. Mode 2b. Increased 60s Beacon Rate with 1.78W Internal Heat Dissipation ...........84
Figure 46. Mode 5 – Increased Data Drop Simulation with 3.38 W Internal Heat Dissipation ..84
Figure 47. Mode 4a. ADC System “ON” with 5.09 W Internal Heat Dissipation ......................85
Figure 48. Mode 4c. ADC System “ON” and 60s Beacon Rate with 5.74 W Internal Heat Dissipation ...........................................................................................................85
Figure 49. Heating Case with 9 Watt Internal Heat Dissipation ................................................86
Figure 50. Surface Mount Component (PIC) Material Layers21 ............................................88
Figure 51. Surface Mount Component Resistance Network & Heat Flow (Example)21 ................88
Figure 52. Resistance Model for CubeSat Side-Panel with Example Layout .............................89
Figure 53. Resistance Model Comparison .................................................................91
Figure 54. Battery Panel to Structure Heat Paths .........................................................91
Figure 55. CP3 Internals Exposed – SolidWorks Model w/o C&DH Board and Side-panels ....95
Figure 56. Conductivity Across and Through an FR4 multilayer PCB ..........................96
Figure 57. 6-Layer Printed Circuit Board Design & Ground Plane Illustration ..............97
Figure 58. Magnetorquer traces in the CP side panels ..................................................98
Figure 59. NPS CubeSat Payload PCB Board Showing Layers and Traces ....................99
Figure 60. Layout for CP2 C&DH Subsystem PCB .....................................................100
Figure 61. Cold Soak Temperatures for Normal Ops .....................................................101
Figure 62. Cold Soak Temperatures Operating in Mode 2a .........................................102
Figure 63. Solar Panel Mesh Pre-Analysis .................................................................103
Figure 64. De-Integrated Satellite Prior to Thermistor Installation ..............................107
Figure 65. Thermistor Bundle Integrated Prior to Testing ..........................................108
Figure 66. T-VAC PC with Labview, Test Plan & Radio ..............................................109
Figure 67. Antennae Taping Prior to T-VAC Testing ..................................................109
Figure 68. Preparing the Satellite for Testing; Thermistor Bundle & Antennae Release ....110
Figure 69. Marconi and Hertz, PolySat Groundstation Computers, Antennae Controllers and Radios .................................................................111
Figure 70. Cold-Bias Testing Profile ...........................................................................113
Figure 71. Expected Environment (Thermal Shroud) Temperature with Inertia Effects ....115
Figure 72. Standard Operations “Mode 1” Cycle Temperatures ....................................117
Figure 73. Omega Controller with National Instruments DAQ ....................................118
Figure 74 Solenoid Valve & Liquid Nitrogen Dewar during Testing ..............................118
Figure 75. Post Test Inspection & Antennae Tape-Down ............................................120
Figure 76. Mode 2a Increased Beacon Rate 90s .......................................................121
Figure 77. Mode 2b Increased Beacon Rate 60s .......................................................122
Figure 78. Mode 3 Payload Enabled Test ....................................................................123
Figure 106. SINDA Output w/ Payload Enabled (Schema 3) ................................................................. 173
Figure 107. SINDA Output w/ ADC On (Schema 4a) ................................................................. 173
Figure 108. SINDA Output w/ ADC On & 90s (Schema 4b) ......................................................... 174
Figure 109. SINDA Output w/ ADC On & 60s (Schema 4c) ......................................................... 174
Figure 110. SINDA Output w/ Increased Contact/ Data Drop (Schema 5) ..................................... 175
Figure 111. PCB Article for Surface Mount Component Failure Rate Test .................................... 176
Figure 112. Solder Joint Cracking due to Thermal Cycling & Alteration in Resistance ............... 177
Figure 113. 1000 Cycle Thermal Test on 3216 Surface Mount Resistor ...................................... 177
Figure 114. Failure by Resistor Type and Cycle Number for Different Ramp Gradients ............. 178
Figure 115. RSSI Receive Sensitivity vs. Power Inputs (Use for Comparison) ............................. 187
Figure 116. C&DH Temperature During Cycle #1 ................................................................. 188
Figure 117. MAG Data Readings During Cycle #1 ........................................................................ 188
Figure 118. RSSI Values For Cycle #1 ......................................................................................... 189
Figure 119. Citizen Crystal Oscillator Variances ........................................................................ 190
Figure 120. CC1000 and C&DH Reference Oscillator – Citizen HCM49 ........................................ 191
Figure 121 XO (Uncompensated) Crystal Oscillator Temperature Shift .................................... 192
Figure 122. Supply Current vs. Temperature for the DS3231 ...................................................... 192
Figure 123. Frequency Deviation vs. Temperature and Aging Value for the DS3231 .................... 193
Figure 124. Delta Time and Frequency vs. Temperature for the DS3231 ....................................... 194
Figure 125. Shutdown Supply Current vs. Temperature for MAXIM 1239 ADCs ..................... 194
Figure 126. Lithium Ion Electrolyte Conductivity Over Temperature .......................................... 194
Figure 127. Specific Energy versus Temperature by Battery Type ............................................. 195
Figure 128. Lithium Ion Anode Degradation ................................................................................. 195
Figure 129. Resistance to Temperature for a NHQM Series Thermistor by Thermometrics ......... 196
Figure 130. ADC Code vs. Thermistor Temperature ................................................................. 199
Figure 131. GeneSat-1 Temperature History (External) ............................................................... 202
Figure 132. GeneSat-1 Beacon Data Including Thermal Ranges ................................................... 203
LIST OF TABLES

Table 1. Size Differences (By Mass); CubeSats vs Industry Satellites ........................................... 7
Table 2. Component Temperature Limits for Industrial Satellites ......................................................... 9
Table 3. Component Temp. Limits from CubeSat Parts vs. Industry ..................................................... 10
Table 4. Populated PCB Area and E/Alpha Adjustments – Side Panel Interior .................................... 34
Table 5. Populated PCB Area and E/Alpha Adjustments – Side Panel Exterior (Solar Panels) .......... 34
Table 6. Populated PCB Area and E/Alpha Adjustments – C&DH Board (Satellite Interior) .......... 35
Table 7. Emissivity Obtained Through Thermal Vacuum Testing Method ......................................... 43
Table 8. Solder Mask Emissivity Test Results ......................................................................................... 52
Table 9. Emissivity Values for Conformal Coated Parts ...................................................................... 54
Table 10. Standard Spacecraft Emissivity, Absorptivity and Alpha/Epsilon Values ............................ 61
Table 11. Hot Case Equilibrium Calculation Results ............................................................................ 72
Table 12. Cold Case Equilibrium Case Results ..................................................................................... 73
Table 13. CP3 Calculated Orbital Properties ........................................................................................ 77
Table 14. Heat Generation by Component/System (CP3) .................................................................. 78
Table 15. Heat Generation by Mode .................................................................................................... 79
Table 16. Cold Case by Mode ............................................................................................................. 80
Table 17. Criteria 1.1 Verification Evidence; Percent Reduction Maximum to Minimum .............. 81
Table 18. SINDA/TSS Modes 1, 2a, 2b, 3, 4a, 4b, 4c, 5 .......................................................... 105
Table 19. Thermal Vacuum Chamber Test Results ............................................................................. 128
Table 20. Verification Criteria Compilation ......................................................................................... 134
Table 21. Criteria 1.1 Verification Evidence; Percent Reduction Max to Min .................................. 135
Table 22. SINDA Output Results Summary by Mode ....................................................................... 135
Table 23. Criteria 1.1 Thermal Vacuum Chamber Test Results ......................................................... 136
Table 24. Surface Area by Representative Component & Property Adjustment ............................... 156
Table 25. Side Panel Interior Emissivity Calculation (Correction) ...................................................... 157
Table 26. Side Panel Exterior Absorptivity and Emissivity Calculation (Correction) ..................... 157
Table 27. C&DH Board Emissivity Calculation (Correction) .................................................................157
Table 28. Standard Side Panel Corrected Emissivity and Absorptivity Values .........................158
Table 29. +Z & -Z Side Panel Corrected Emissivity and Absorptivity Values .........................158
Table 30. Antenna Side Panel Corrected Emissivity and Absorptivity Values .........................159
Table 31. Corrected Satellite Surface Averages (Satellite Whole) ................................................159
Table 32. Tumbling Satellite Panel Area Calculations .................................................................162
Table 33. Tumbling Satellite Panel Area Calculations (cont’d) ..................................................163
Table 34. Inputs for Thermal Equilibrium Calculations .............................................................165
Table 35. Outputs for Thermal Equilibrium Calculations ...........................................................165
Table 36. Thermal Equilibrium Calculation Results .................................................................166
Table 37. Adjusted “Worst-Case” Hot w/ Adjusted Internal Heat Flux .......................................166
<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>C&amp;DH</td>
<td>Command and Data Handling</td>
</tr>
<tr>
<td>CONOPS</td>
<td>Concept of Operations</td>
</tr>
<tr>
<td>COTS</td>
<td>Commercial-Off-the-Shelf</td>
</tr>
<tr>
<td>CAL POLY</td>
<td>California Polytechnic State University</td>
</tr>
<tr>
<td>CP</td>
<td>Cal Poly</td>
</tr>
<tr>
<td>EP&amp;H</td>
<td>Electrical Power and Handling</td>
</tr>
<tr>
<td>GEVS</td>
<td>General Environmental Verification Specification</td>
</tr>
<tr>
<td>LEO</td>
<td>Low Earth Orbit</td>
</tr>
<tr>
<td>NASA</td>
<td>National Aeronautics and Space Administration</td>
</tr>
<tr>
<td>PCB</td>
<td>Printed Circuit Board</td>
</tr>
<tr>
<td>P-POD</td>
<td>Poly-Picosatellite Orbital Deployer</td>
</tr>
<tr>
<td>TVAC</td>
<td>Thermal Vacuum Chamber</td>
</tr>
</tbody>
</table>
1.0 Introduction

1.1 The History and Background of CubeSats

When Dr. Jordi Puig-Suari and Dr. Robert Twiggs came up with the CubeSat standard both agreed that a small, common satellite standard could be an enabler. It could provide students and small companies worldwide the ability to design, test and launch a flight quality satellite. This experience was previously inaccessible for the vast majority of university students and the capability was originally beyond most small businesses. The true embodiment of the “learn by doing” mantra, the CubeSat project helped to fill this crucial niche. Below are some of the benefits it provided.

- Giving students the real-world satellite design, integration and launch experience.
- Providing companies the ability to test new components on-orbit with low cost and risk.
- Giving the satellite industry a development path to new concepts and innovative designs.

The CubeSat Specification Revision 12 front page (figure 1) allows for a cube 100mm on each side and with a mass of 1330g or less. This standard is the newest revision currently published by the California Polytechnic State University CubeSat project.
Alternative CubeSat designs, per the standard, can also utilize a 200mm or 300mm length and a 100x100mm cross-section with an approximate 2 kilogram or 4 kilogram mass limitation, respectively. Along with this satellite standard, the P-Pod or Poly-Picosatellite Orbital Deployer, shown in figure 2, has enabled CubeSats to be simply and reliably integrated onto nearly all existing space launch vehicles (LVs) at very low cost and with no impact on the primary LV payload.
Launch providers from Russia and India to the United States have begun offering ride-sharing opportunities that utilize the P-Pod. Thanks to standardization in the satellite form factor and reliability in the integration mechanism increasing numbers of CubeSat class payloads are being launched. Launches began in 2003 with the Eurockot launch of six CubeSat class payloads including the first ever triple cube shown in figure 3, QuakeSat.
CubeSat launches continue today with the recent SpaceX Falcon 9 launch that lofted several triple cubes on December 8th 2010. In figure 4 is a sample of the triple cube Colony spacecraft launched on the SpaceX Falcon 9.

Thanks to inexpensive launch costs, community outreach and regular conferences over the past ten years the CubeSat development community itself has matured from only a handful of schools in Europe and the United States to
several hundred developers worldwide. As an example of the evolution more recent CubeSats launched, CP6, is shown below in figure 5. This satellite was originally built as the back-up flight model for CP3 launched on the second DNEPR mission. Since it was never used, the CP3 back-up was modified, a new payload was added along with a low noise amplifier (LNA), and it was launched with two other 1-U CubeSats out of Wallops, VA.

![CP6 – Minotaur 2009 Launch](image)

The mission and requirements driving the design of a CubeSat can vary widely. This is another challenge faced by the picosatellite thermal engineer. The specification really is flexible enough that almost any payload that can fit in the satellite can be flown. Payloads that have flown include everything from photomultiplier tubes to micro-vacuum thrusters and even the first fully deployed solar sail, NanoSail-D. This wide range of payloads between figure 5 CP6 and figure 6 NanoSail-D can create lots of challenges!
CubeSats, like all satellites, perform better and last longer as a system when the components inside them remain within the temperature limits they are designed for. Satellite thermal design and control begins with establishing the temperature specifications within which the satellite will operate during its lifetime. The design of the thermal subsystem must be done such that the established specifications are not exceeded during the satellites orbit. However, to ensure robustness, the satellite must also be designed to have the ability to perform satisfactorily even if subjected to short-term out of specification temperatures. The keys to this challenge are the same as in any engineering challenge, analysis, similarity, tests and innovative design. The next section reviews some of the differences in these keys when it comes to picosatellites.
1.2 CubeSat Engineering Challenges and Design Evolutions; Divergence from Traditional Satellite Analysis and Design

With picosatellites the typical models do not always apply. To illustrate the vast technical and analytical differences that exist between the design of picosatellites and their larger cousins consider the following points. The traditional and statistical median satellite is anywhere from several hundred to several thousand kilograms, with the mean being much closer to the latter than the former. This ‘average’ satellite has an energy consumption of several kilowatts of electrical power along with the production, distribution and storage systems capable of generating this power while on orbit. Accompanying the several metric tons is a surface area that measures in tens of meters squared and a volume that is also exponentially greater than that of a picosatellite. Table 1 was constructed to illustrate the difference that exists between CubeSats and industrial satellites just in the system mass quantity alone.

Table 1. Size Differences (By Mass); CubeSats vs Industry Satellites

| SBIRS GEO | 4500 | Lockheed Martin |
| DSP | 2400 | Northrop Grumman |
| GEO-EYE 1 | 1200 | General Dynamics |
| IKONOS | 800 | Lockheed Martin |
| IRIIDIUM | 680 | Motorola |
| Pioneer 1 | 38 | TRW |
| XSS-10 | 28 | Boeing |
| CP2/3 | 1 | PolySat - Cal Poly |
Larger satellites have advantages in the realm of thermal design and analysis. In analysis they have very high accuracy with regards to what a surface is emitting or absorbing due to their budget and the economy of scale principal. Their solar panels are tightly characterized and fitted GaInP2/GaAs/Ge cells and low absorptivity Kapton tape. Their supporting walls are bounded on the exterior by either specialty multi-layer insulation (MLI) and second surface or first surface mirrors. On the interior their electrical housings are coated in 3M “Velvet Black” paint to provide the maximum radiative heat dispersion. In addition they have heat pipes built into the supporting walls below along with heater elements and thermostats mounted inside or around them. Their dishes/antennae are covered in military grade thermal coatings, all of which have been exhaustively tested and whose composition and thermal properties are well vetted for their degradation due to ultraviolet radiation and charged particles from the beginning to the end of life (BOL & EOL).

CubeSats can not make the same generalizations, use expensive military standard paint, employ an MLI design and sewing team or ensure their commercial off the shelf (COTS) subcontractors will provide every last material property detail to them. CubeSats must rely on testing, modeling and extensive categorization in order to fully know their thermal properties, especially their irradiative properties.

In the realm of thermal design larger satellites have another advantage to picosatellites. Components on a picosatellite cannot be thermally isolated from
one another without extensive work and significant mass losses. Refer to the two tables below (tables 2, 3) for a comparison of the temperature ranges on MIL-STD parts and subsystems used in large commercial or government satellites against the temperature limits that the COTS components inside a CubeSat should be held to (excerpted from manufacturer data available in Appendix A, B, C).

Table 2. Component Temperature Limits for Industrial Satellites²

<table>
<thead>
<tr>
<th>Component</th>
<th>Typical Temperature Ranges (°C)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Operational</td>
</tr>
<tr>
<td>Batteries</td>
<td>0 to 15</td>
</tr>
<tr>
<td>Power Box Baseplates</td>
<td>–10 to 50</td>
</tr>
<tr>
<td>Reaction Wheels</td>
<td>–10 to 40</td>
</tr>
<tr>
<td>Gyros/IMUs</td>
<td>0 to 40</td>
</tr>
<tr>
<td>Star Trackers</td>
<td>0 to 30</td>
</tr>
<tr>
<td>C&amp;DH Box Baseplates</td>
<td>–20 to 60</td>
</tr>
<tr>
<td>Hydrazine Tanks and Lines</td>
<td>15 to 40</td>
</tr>
<tr>
<td>Antenna Gimbals</td>
<td>–40 to 80</td>
</tr>
<tr>
<td>Antennas</td>
<td>–100 to 100</td>
</tr>
<tr>
<td>Solar Panels</td>
<td>–150 to 110</td>
</tr>
</tbody>
</table>
As can be seen from the comparison above, the temperature limits are similar. The key takeaway here is that on large satellite system the solar panels aren’t thermally coupled with or re-radiating to the batteries and they are thermally isolated from the star trackers. On a CubeSat the external surfaces are both conducting and re-radiating to the interior parts. Larger satellites will often have an entire active thermal subsystem composed of heaters, radiators and heat pipes that can use special materials, designs and electrical energy to moderate or manage the thermal situation and significantly reduce any temperature instability or variance. So components like polymer-based integrated circuits (ICs), for example, that are designed to operated with full confidence in a 0°C to 40°C environment can be maintained in that environment, while other components like Gallium Arsenide solar cells which operate best anywhere from -100°C to 20°C can be isolated from the ICs and passively

<table>
<thead>
<tr>
<th>Actual CubeSat</th>
<th>Comparable Industry</th>
<th>CubeSat Component</th>
<th>Industry Component</th>
</tr>
</thead>
<tbody>
<tr>
<td>Batteries (CGA)</td>
<td>Batteries (Charging)</td>
<td>-10 to 60 °C</td>
<td>-10 to 25 °C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>0 to 45 °C</td>
<td></td>
</tr>
<tr>
<td>FR4 Solar Panels</td>
<td>Solar Panels</td>
<td>-20 to 130 °C</td>
<td>-200 to 130 °C</td>
</tr>
<tr>
<td>Payload Cameras</td>
<td>Star Trackers</td>
<td>10 to 50 °C</td>
<td>-10 to 25 °C</td>
</tr>
</tbody>
</table>
allowed to fluctuate to the lower temperatures with only thermal coatings and MLI to protect against overheating or other thermal issues. This isolation and thermal specificity is achieved through the methods previously discussed along with conduction isolators. Using materials with low conductivity such as ceramics is one way of isolating two components or subsystems with differing thermal ranges. Another method of thermal isolation is to utilize designs that limit the total heat flux by limiting the cross-sectional area available to aforementioned flux.

Picosatellites are obviously limited by mass and volume, so physically limiting the cross-sectional area or utilizing advanced materials are not always available to the thermal designer. Again, with a CubeSat class satellite the majority of your systems have set thermal couplings that cannot be adjusted without incurring serious cost or schedule impacts.
2.0 Thesis Motivation, Drivers, General Reasoning and Hypothesis
Definition

2.1 Thesis Motivation

The thermal challenges facing picosatellites as discussed in Section 1.2 are numerous. Before beginning this thesis work a qualitative review of the major factors and their impact on the thesis was in order. The list below was developed to attempt to quantify these major factors motivating the thesis.

- Extreme on orbit temperature gradients
  - Due to low thermal inertia, lack of control and LEO orbits with eclipse conditions CubeSats undergo constant thermal cycling causing thermal shock and temperature gradients across subsystems

- Unknown thermal environments
  - Secondary spacecraft do not choose the orbit nor control the ascent heating or payload fairing jettison conditions. (CubeSat teams can choose a general LEO orbital regime but it limits their opportunities).

- Spacecraft Attitude or Orientation
  - CubeSats often lack the ability to accurately control their attitude and more advanced CubeSats that do have attitude control typically use it solely for payload operations, not for thermal orientation or for providing a “thermal roll.”
2.2 Thesis Drivers

The factors listed in section 2.2 were great motivators to begin research in thermal control for picosatellite systems. Given these motivating factors the following list of key drivers, or items which the testing for the thesis itself were derived. These were done to attempt to focus the assumptions and begin the hypothesis definition process.

- Increasing orbital debris regulations
  - The Federal Communication Commission (FCC), International Telecommunication Union (ITU), National Aeronautics and Space Administration (NASA), European Space Agency (ESA), International Astronautical Congress (IAC) and Committee on Space Research (COSPAR) have all increased the regulations and restrictions on orbital debris, impacting the CubeSat community immensely. The majority of the new standards read similar to the NASA standard: *Limiting orbit lifetime after mission completion to 25 years or maneuvering to a disposal orbit*. This drives CubeSats to extend their useful mission or system lifetime so they can ensure they meet these regulations. Using Commercial Off-The Shelf (COTS) products and trying to extend mission life is an engineering dichotomy.
- Stringent picosatellite budgets
  
  o The budget for volume, mass, power and schedule are all limited for picosatellites. These tight constraints drive a very narrow trade space and check the potential solutions available to CubeSat engineers. This driver pushed the hypothesis to look for an atypical solution. Atypical solutions however are the norm when it comes to CubeSat. Since the beginning of the standard Prof. Twiggs and Dr. Puig-Suari call for CubeSat engineering students to literally “think outside the box”\(^4\) which has driven everything from kilometer long hoy-tethers to picosatellite micro-propulsion systems and Stanley™ tape-measure body mounted antennas.

  With the motivation developed and the drivers established the next step was to find a reasonable solution that could be tested and evaluated to meet the need. In evaluating the motivators and the drivers, reviewing them with team members and the thesis advisor the hypothesis began taking shape.
2.3 General Reasoning, Hypothesis Definition, Assumptions and Verification Criteria

2.3.1 General Reasoning

The general solutions proposed to meet the motivation and drivers in Section 2.2, from a thermal perspective, are shown in the list below.

- Modifications to flight hardware and software
- Strict enforcement of testing and qualification programs
- Adaptive component usage and command schema alterations

Since the second generation of Cal Poly satellites built were already integrated into Poly Picosatellite Orbital Deployers (P-PODs) their design and flight hardware were fixed and could not be modified for thermal concerns. Aside from already being integrated the increased cost, time, mass, power and effort required to implement this option is out of scope for most CubeSat teams (Cal Poly included).

The second option was that which the Cal Poly series of satellite actually pursued prior to launch and operations. In lieu of adding heater components, controllers, multi-layer insulation (MLI), applying thermal treatments or installing thermal mass the Cal Poly team decided to take an alternate path of aggressive testing and qualification. This testing is reviewed and discussed in greater detail later in this thesis (section 3.0).
The last option considered was a new concept for picosatellites and as such it had the required originality suited to a masters level thesis. There was opportunity to define, model and test this option without making flight system alterations. It also would require critical and independent thinking with a substantial undertaking to provide the evidence, combine evidence types and analyze them for conclusions and verification of the hypothesis. For the aforementioned reasons, the third solution was chosen as the basis for the thesis.

2.3.2 Hypothesis Definition

Using the third solution discussed in section 2.3 the following hypothesis was made:

*Can a standard 1-U CubeSat utilize existing hardware and software to improve its thermal condition and operational lifetime?*

My stance and thesis are that based on first order calculations, material properties testing, fundamental engineering analyses, Finite Element Analyses (FEAs), Finite Difference models (FDs), thermal vacuum environment simulations and finally the on-orbit testing discussed in section 3 and section 4, yes, a 1-U CubeSat through adaptive component usage, is able to improve its thermal condition and operational lifetime with its existing hardware and software. However before that thesis could be posed and backed with data, the hypothesis
needed simplifying assumptions, modeling constraints and clear verification criteria to justify this stance and thesis to be truth.

2.3.3 Hypothesis Assumptions

To appropriately bound the study, ensure accurate results and have precision between verification methods (test, direct analysis, FEA/FD) the following assumptions were made.

- No additional mass is available to provide for either thermal control or alter the systems’ thermal inertia.
- No changes are to be made to the system volume, geometry or surface areas.
- No flight software modifications can be made.
- Existing steady-state on orbit temperatures are already within the satellite’s component survival levels (CSLs).

2.3.4 Hypothesis Verification

To verify that the hypothesis had been met and the thesis proved, clear and concise success criteria were established prior to testing and modeling. The criteria, their implementation or measure and any additional detailed examples are displayed in the format below.

1. Criteria

   1.1. Implementation or Measure

      1.1.1. Detailed example (if required)
The list below covers criteria generated to provide clear success or failure during the verification process of the thesis.

1. The thermal condition of the satellite shall be more stable* with the implementation of the thesis than via passive control and standard on-orbit operations. Stability* is defined as achieving one of the following.
   1.1. *A 15% reduction in spacecraft subsystem component temperature variability across maximum and minimum orbital heat flux conditions.
   1.2. *A 10% reduction of in-plane thermal gradients on the subsystem printed circuit boards (PCBs) and an associated (equivalent) reduction in heat-flux across the subsystem PCBs.

2. The satellite shall have improved operations or lifetime via one or more of the following conditions through the implementation of the thesis.
   2.1. Increased on-orbit component lifespans (potential, via analysis).
   2.2. Improved RF or Electrical Power and Handling (EP&H) subsystem performance (stability) and/or reliability.
      2.2.1. Via measurably higher Received Signal Strength Indicator (RSSI) for the RF performance parameter.
      2.2.2. Via higher percentage of acknowledge (ACK) responses during operations for the RF reliability parameter.
   2.3. Improved Command and Data Handling (C&DH) or Inter-Integrated Circuit I²C reliability.
      2.3.1. Via lower CDH Reset Value.
2.3.2. Via improved CC1000 or equivalent crystal oscillator operations reliability and lowered drift (Analysis).

2.4. Enhanced payload performance (Realized or Potential).

2.4.1. Via testing performed to support K. Glassey on payload sensitivity and thermal performance.

2.4.2. Via analysis of quantifiable improvements to payload subsystems.

3. Primary or bus system operations must not be negatively impacted by the implementation of the command schema.

3.1. EP&H subsystem values (BATTA and BATTB) must not have overcurrent or undercurrent faults.

3.2. EP&H subsystem Depth of Discharge (DoD) must not exceed recommended manufacturer limits.
3.0 Thesis Body; Analyses, Testing & Modeling Review

3.1 CubeSat Thermal Environment Simulations

Before delving into the analysis, testing & modeling done for this thesis it is critical to discuss what existing CubeSat teams have done in the past to solve the thermal environment challenge. Currently the in order to qualify designs for thermal environments on orbit the CP PolySat and CubeSat team put their satellites through a punishing series of thermal cycle, soak and shock testing that, in leui of having detailed thermal designs, subsystems or entirely MIL-STD qualified and accordingly priced parts certifies that the satellite will indeed operate through the worst scenarios on orbit. (Note some CubeSats do indeed use MIL-STD certified parts.)

By simulating conditions above and beyond the NASA General Environmental Verification Specification (GEVS) the Cal Poly team and other CubeSat developers ensure that their systems are robust enough to withstand the on-orbit environments. It must be noted that this testing is a critical part of the design and testing of any spacecraft. This testing ensures that issues and anomalies can be uncovered while the design is still capable of being altered or modified. Using a thermal vacuum chamber or “T-VAC” the teams simulate the radiative scenarios in space by first creating a vacuum of $10^{-5}$ Torr and then using cooled, typically via liquid nitrogen, or heated, typically via resistive heaters or infrared lamps, walls. This closely imitates the on-orbit environment that the satellite being tested will see.
The NASA GEVS standard requires that:

“…thermal-vacuum testing at component, subsystem, and system levels of assembly. Eight (8) thermal cycles are required on all hardware prior to assembly on the spacecraft. Normally four (4) cycles are performed at the component level and four (4) at the subsystem or instrument level. Four (4) cycles are also performed on the spacecraft making the total number of thermal cycles twelve (12). The major changes for thermal testing, other then clarifications, are to recommend a 5°C margin for acceptance testing and to increase the required margins and durations if tests are approved by Goddard to be performed at ambient pressure. Many Goddard projects already impose the 5°C margin for acceptance testing in vacuum. Recommendations are being made to increase test margins by an additional 15°C and to increase the number of cycles and dwell times by 50% if testing is performed at ambient pressure.6

The thermal vacuum testing and qualification of the CP satellites ensured that GEVS requirements are met both in cycles on the spacecraft along with performing the necessary thermal dwells or “soaks” at both hot and cold extremes. Since testing on the CP satellite was performed in a thermal vacuum at very low pressure ($10^{-5}$ Torr) the last portion calling for a 15°C margin increase does not apply.

In addition to testing the operation of the spacecraft, the hot dwell or soak periods during these thermal cycles provided the necessary assurance to launch
providers that the total mass loss (TML) and collected volatile condensable materials (CVCM) values are within spec prior to being integrated onto the launch vehicle and placed next to the primary mission. NASA’s Goddard Space Flight Center lists a TML of 1% or less and a CVCM of .1% or less is acceptable and most launch contractors levy this requirement upon their secondary payload providers (it is also a requirement per the CubeSat Design Standard, Revision 12)\(^6\). This standard for outgassing and removing materials is set to follow the ASTM standard which specifies that the vacuum and temperature for performing TML and CVCM testing should be less than \(5 \times 10^{-5}\) Torr and 125°C, respectively.

It is through this rigorous system level thermal testing that CP currently verifies their satellites will operate nominally on-orbit. To demonstrate just how critical this testing is, figure 7 shows the weighted effectiveness for environmental screens (tests) established by the Johns Hopkins APL Space Department, using data compiled from major United States satellite manufacturers.
Thermal cycling, of the same type applied to all CP CubeSats, is the most effective satellite test method for revealing system issues by a wide margin (52%) over random vibration. The additional point that the Johns Hopkins team noted from this chart is that of the entire set of environmental conditions a satellite will see, thermal cycling is the most effective at detecting issues. Also of note was that the hot soak of the satellite ranked third and the thermal shock test ranked fourth, both are also tests that the CP team performs on their operational satellites.
3.2 Defining the Unknowns, Material Properties and Variations

Thermal analyses, even simple ones, are typically very reliable for giving confidence that your satellite design will operate in its expected environment. The unique thing about CubeSats in terms of thermal analyses is that the majority of satellite thermal analysis is based on making engineering assumptions and problem simplifications. Those simplifications and assumptions limit the accuracy of the analysis results. For this thesis to be conclusive there needed to be greater definition and the least number of simplifications made. The starting point was recognizing the unknowns that would have the greatest affect on the analyses in question and putting time and effort into reducing them. Spacecraft thermal analysis hinges upon radiation as the sole method of heat flux into and out of the system, as such the surface properties of the satellite are critical to be able to accurately predict the systems thermal equilibrium values.

3.2.1 6-Layer Flame Retardant #4 Printed Circuit Boards (PCBs) Radiative Properties

CubeSats made almost exclusively of populated FR4 boards meeting the Personal Computer 104 (PC/104 or PC104) mechanical and electrical standards. PC104 is a natural fit for CubeSats. Literally the PC104 standard calls for a 96mm x 90mm (3.55inx3.75in) board with no backplane and it fits the CubeSat 10cm x 10cm standard perfectly. In lieu of a backplane like your typical Advanced Technology Extended (ATX) or micro ATX (mATX) PC standard, 104 allows individual boards to be stacked together using their 104 pin standard
interface point and also using standard corner fastener areas to create a “block” PC. This standard was initially designed for specialized embedded computing and customized rugged PC systems for military or industrial applications.

Being designed for embedded computing made PC104 commercial off the shelf (COTS) parts easily available to find and adapt for CubeSat use. Another key driver behind using PC/104 was the relaxed bus drive current of 6 mA. This effectively lowers power consumption to under 1-2 Watts per module and minimizes component count. The height interface between boards is also set in the standard to be 11 mm but is not always adhered to, especially for boards that may be using the mechanical standard but not the electrical pin standard.

Figure 8. PC104 Standard Board Layout and Mechanical Requirements

Another view of the PC104 standard is shown in figure 9. Here you can see that the stacking utility designed into PC104 is akin to the necessary stacking
of electronics to efficiently meet the CubeSat Design Standard (CDS).

Figure 9. PC104 Horizontal Layout Example

As an example the NPS CubeSat utilized the 104 standard fully, integrating the 104 pin design into their stack shown in figure 10.

Figure 10. An NPS CubeSat PC104 Stack
Comparing the PC104 Horizontal Layout Example in figure 10 to the CP2 “Bus” System Stack in figure 11, along with a clamshell open picture of the CP3 stack in Figure 12, the reason for utilizing the standard is clear. However the CP bus, unlike the NPS-Pumpkin design, doesn’t follow the electrical portion of the standard, instead opting for ribbon cabling and alternate board connectors since the amount of necessary board interplay both physical and electrical was found to be more efficient in this manner (for the CP bus specifically).

Figure 11. CP2 “Bus” System Stack
For reference, FR4 is electrical-grade, fire rated dielectric fiberglass laminate epoxy resin with a glass fabric substrate. The abbreviation FR4 comes from F for flame and R for retardancies with the 4 denoting a #4 epoxy. FR4 offers excellent chemical resistance and electrical properties under varied conditions. The standard for FR4 is MIL-I-24768/27.

The PC104 boards used in CP2 and CP3 with no traces, vias or populated components are fairly easy to model for thermal properties and analysis, simply utilize the solder mask surface properties and the well-established FR4 material conduction properties. However that is not at all representative of how the PCBs are actually implemented on the spacecraft. On the spacecraft they are not blank boards, they are densely populated with components of varying size and surface
properties, none of which is identical to that of the PCB itself. All of this leads to the first major unknown property that had to be defined – the radiative properties of populated PCBs.

![Populated CP2 Power (EP&H) Board](image)

**Figure 13. Populated CP2 Power (EP&H) Board**

By reviewing the available area, measuring the laid components and gathering the surface properties of the laid components the average values can be obtained through area calculations. Initially this review began as attempting to count, measure and surface average every component. After attempting to do this for the CP CD&H board, shown in figure 13, this was quickly realized to be impractical. In lieu of measuring and cataloging every unique component on the populated board individually the following generalizations were made. These generalizations allowed for a more expedient process while still providing appreciably enhanced surface property knowledge.
Surface mount resistors are assumed to be uniformly of the metric 1005 standard classification thus establishing a standard area for resistor body and leads of 1.0mm x 0.5mm x .35mm (L x W x H) as shown below in figure 14. The exposed leads are assumed to take up .25mm x .5mm (L x W) on both ends of the resistor but in the surface area calculations were given no height value since they “drop” when soldered and to a varying degree become flush with the board.

![Surface Mount Resistor Size Values](image)

Figure 14. Surface Mount Resistor Size Values

Surface mounted capacitors and similarly sized component are assumed to be uniformly of the metric 1005 standard classification thus establishing a standard area for capacitor body and leads of 1.0mm x 0.5mm x 0.5mm (L x W x H) as shown in Figure 15. The exposed leads on the capacitor are assumed to take up .25mm x .5mm (L x W) on both ends but in the surface area calculations were given no height value since they too “drop” when soldered and to a varying degree become flush with the board.
Figure 15. Surface Mount Capacitor Size Values

Surface mounted Maxim 1239 Analog to Digital Converters (ADCs) and similarly sized components were assumed to conform to the nominal (averaged between max-min) values provided for the E16-4 package outline shown below. The averaged value was calculated to be 3.9mm x 4.89mm x 1.55mm (W x L x H). The exposed leads were calculated to each be 1.06mm x .25mm with 8 leads per side (total surface area of 4.24mm² per chip).
Surface mounted AD627 instrumentation amplifiers were assumed to conform to the nominal (averaged between max-min) value given in their specification below. The averaged value was calculated to be 3.9mm x 4.90mm x 1.55mm (W x L x H). The exposed leads were calculated to each be 1.05mm x 0.41mm with 4 leads per side (total surface area of 3.44mm² per chip).
With these simplifications the majority of board components could be counted from their layout drawings and the appropriate reductions in solder mask surface area and increases in component surface areas could be accounted for (See Appendix D). With these values calculated a more accurate surface radiative value and area value was obtained. As an example of the discrepancy in both area and radiative properties between a PCB with assumed values and a PCB with adjustments for component population is shown below.
Table 4. Populated PCB Area and E/Alpha Adjustments – Side Panel Interior

<table>
<thead>
<tr>
<th>Solar Panels Interior</th>
<th>&quot;Resistors&quot;</th>
<th>&quot;Capacitors&quot;</th>
<th>&quot;Maxims&quot;</th>
<th>&quot;ADCs&quot;</th>
</tr>
</thead>
<tbody>
<tr>
<td>Extra Surface Area</td>
<td>48.00</td>
<td>35.00</td>
<td>4.00</td>
<td>3.00</td>
</tr>
<tr>
<td></td>
<td>50.40</td>
<td>52.50</td>
<td>109.00</td>
<td>81.84</td>
</tr>
<tr>
<td>Replaced Surface Area</td>
<td>36.00</td>
<td>26.25</td>
<td>110.20</td>
<td>82.65</td>
</tr>
<tr>
<td>Avg. Emissivity (Extra)</td>
<td>0.55</td>
<td>0.61</td>
<td>0.90</td>
<td>0.90</td>
</tr>
<tr>
<td>Avg. Emissivity (Replaced)</td>
<td>0.61</td>
<td>0.61</td>
<td>0.63</td>
<td>0.67</td>
</tr>
<tr>
<td>Weighted E By Area (Extra)</td>
<td>27.55</td>
<td>31.85</td>
<td>98.10</td>
<td>73.66</td>
</tr>
<tr>
<td>Weighted E By Area (Repl)</td>
<td>21.84</td>
<td>15.93</td>
<td>69.33</td>
<td>55.13</td>
</tr>
<tr>
<td>TOTAL (mm^2)</td>
<td>293.74</td>
<td>0.00029</td>
<td>4%</td>
<td>7%</td>
</tr>
<tr>
<td>TOTAL (m^2)</td>
<td>255.11</td>
<td>0.00026</td>
<td>3%</td>
<td></td>
</tr>
<tr>
<td>E AVG (SA)</td>
<td>0.79</td>
<td>0.72</td>
<td>0.81</td>
<td>0.90</td>
</tr>
<tr>
<td>E AVG (EX + RE)</td>
<td>0.64</td>
<td></td>
<td></td>
<td>10%</td>
</tr>
</tbody>
</table>

For the side panel interiors which re-radiate heat to the interior of the spacecraft the difference between the previous emissivity value and the corrected populated PCB value was 10% and changing from .90 to .81. The table summary for exterior side panels is below.

Table 5. Populated PCB Area and E/Alpha Adjustments – Side Panel Exterior (Solar Panels)

<table>
<thead>
<tr>
<th>Solar Panels Exterior</th>
<th>Cells</th>
<th>Leads</th>
<th>Averaged</th>
<th>% Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>Surface Area</td>
<td>54.6</td>
<td>2.32</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Avg. Emissivity</td>
<td>0.85</td>
<td>0.02</td>
<td>0.82</td>
<td>4.0%</td>
</tr>
<tr>
<td>Avg. Absorptivity</td>
<td>0.92</td>
<td>0.3</td>
<td>0.89</td>
<td>2.7%</td>
</tr>
<tr>
<td>Averaged E For Panel</td>
<td></td>
<td></td>
<td>Remainder of Panel is Soldermask @ .9</td>
<td>0.84</td>
</tr>
</tbody>
</table>

For the solar panels the change was not as large but was still numerically significant (as shown in section 3.2.4 results) going from an emissivity of .85 to
.82 and an absorptivity of .92 to .89 showing a discrepancy of 4% and 2.7% respectively.

On the C&DH PCB the change was even greater than on the side panel interior. In Table 6 below the difference was found to be a 16% total reduction in the emissivity value for the surface. Since this board is by far the most populated, with component surfaces making up 12% of the actual panel surface area, it was not surprising to have that large of a change, but it illustrated again that assuming the base material emissivity (.90) for the entire panel is not a conservative nor an accurate assumption that CubeSat thermal designers should make. The actual emissivity value of .76 is significantly lower than the .90 and will result in less energy exchanged to and from the side-panel interiors (which also had a lower true emissivity value).

Table 6. Populated PCB Area and E/Alpha Adjustments –C&DH Board (Satellite Interior)

<table>
<thead>
<tr>
<th>C&amp;DH Board</th>
<th>&quot;Resistors&quot;</th>
<th>&quot;Capacitors&quot;</th>
<th>&quot;Maxims&quot;</th>
<th>&quot;ADCs&quot;</th>
</tr>
</thead>
<tbody>
<tr>
<td>Extra Surface Area</td>
<td>23.00</td>
<td>79.00</td>
<td>11.00</td>
<td>2.00</td>
</tr>
<tr>
<td>Replaced Surface Area</td>
<td>24.15</td>
<td>118.50</td>
<td>299.74</td>
<td>54.56</td>
</tr>
<tr>
<td>Avg. Emissivity (Extra)</td>
<td>0.55</td>
<td>0.61</td>
<td>0.90</td>
<td>0.90</td>
</tr>
<tr>
<td>Avg. Emissivity (Replaced)</td>
<td>0.61</td>
<td>0.61</td>
<td>0.63</td>
<td>0.67</td>
</tr>
<tr>
<td>Weighted E By Area (Extra)</td>
<td>13.20</td>
<td>71.89</td>
<td>269.77</td>
<td>49.10</td>
</tr>
<tr>
<td>Weighted E By Area (Repl)</td>
<td>10.47</td>
<td>35.95</td>
<td>190.67</td>
<td>36.76</td>
</tr>
<tr>
<td>TOTAL (mm^2)</td>
<td>496.95</td>
<td>0.00050</td>
<td>6%</td>
<td>12%</td>
</tr>
<tr>
<td>TOTAL (mm^2) % of Panel</td>
<td>434.66</td>
<td>0.00043</td>
<td>5%</td>
<td></td>
</tr>
<tr>
<td>E AVG (SA)</td>
<td>0.81</td>
<td>0.73</td>
<td>0.76</td>
<td>0.90</td>
</tr>
<tr>
<td>E AVG (EX +</td>
<td>New E</td>
<td>Previous E</td>
<td>0.63</td>
<td>16%</td>
</tr>
</tbody>
</table>
3.2.2 TAIYO PSR-4000BN Liquid Photoimageable Solder Mask Radiative Properties

The next set of unknowns that required testing or analysis to define was the IPC-SM-840 compliant TAIYO Solder Mask utilized to coat the PCB surfaces. Solder mask is a thin layer of deposited epoxy liquid that is silkscreened onto the finished PCB pattern. It protects the otherwise exposed top layer copper traces (CP3 uses a 6-layer PCB) from damage while also preventing solder from spanning conductors and causing a short. Other than the exterior of the solar panels this is the dominant surface material in the satellite.

As this material plays such a significant role in the heating and cooling of the satellite it was critical to have accurate knowledge of its properties. The available information was limited, the PCB supplier was unable to find or provide anything beyond the TAIYO specification sheet that showed compliance with IPC-SM-840 but provided little else in terms of applicable surface properties (Appendix E). TAIYO was contacted several times via phone and email but were not willing to provide anything beyond their specification sheet which had already been provided via Sierra Proto Express.

After research on several manufacturers websites it was discovered that while certain manufacturers quoted surface properties they varied by spectrum and also varied by manufacturer. Only properties for green solder mask were provided as it was the most typical application. CP boards used a mix of green,
red and black solder masks with black solder mask being the most prevalent and the kind used in the later revisions of the system (such as CP3 and CP6).

### 3.2.2.1 A Thermal Vacuum Test Method for Obtaining Emissivity Values

Two tests were designed and performed specifically to categorize the solder masks emissivity and absorptivity with accuracy. The first test followed the method prescribed by Dr. Ross Judd from the University of Michigan in his article “Determination of the Emissivity of a Heated Copper Surface.”

Dr. Ross’ method uses the principle that the rate at which heat is transferred from the surface of a heated object will become equivalent to the rate at which heat is generated within the object when steady state conditions have been attained. If the associated conduction and convection heat transfer effects can be eliminated then radiation heat transfer becomes the only method available to the heat flux.

The proportion of radiant thermal energy that is transferred to and from an object by radiation can be objectively quantified using the object’s radiant properties. These properties can be estimated using tables, calculated using material properties and surface roughness or obtained through experimental processes. The most accurate, though work-intensive, method is typically experimentation. In order to properly quantify the emissivity value of the solder mask on the PCB panels a thermal vacuum test was designed following Dr. Ross’ method.
By utilizing a thermal vacuum chamber one can nearly eliminate heating by conduction or by convection. While the thermal vacuum chamber is at or near $6 \times 10^{-5}$ Torr there is so little gas available that convection or even molecular conduction through it is effectively negated. The layout of the test is shown in Figure 18 below for reference where the board is placed upon four Teflon standoffs to isolate it from the solid copper baseplate and then thermistors were placed at three locations along the board in a diagonal (1,2,3). Also the standard thermistors used by the Omega thermal controller are shown relative to the thermal shroud as (4,5).

![Diagram of Emissivity/Absorptivity TVAC Test with Thermistor Locations](image)

**Figure 18. Emissivity/Absorptivity TVAC Test with Thermistor Locations**

Figure 18 illustrates the location of the panel being tested. The panel has been isolated from the thermal enclosure using circular solid Teflon (Polytetra
Flouroethylene) standoffs. The Teflon standoffs are extremely isolative and have very low conduction. The total heat transfer through conduction in the Teflon was calculated to ensure that the amount of heat being transferred was truly negligible.

\[ q^k = -k \frac{\partial T}{\partial n} \]

**Equation 1. Fourier’s Law of Heat Conduction**

The difference in thermal conductivity between the two temperature extremes that the Teflon was exposed to was found to be minimal and so the thermal conductivity was considered constant for the duration of the experiment.

An adjusted form of the Fourier Law of Heat Conduction with the associated material conductivity is shown in Equation 2 below where \( Q \) is the total heat conducted per time:

\[ \frac{Q}{t} = \frac{kA(T_h - T_c)}{d} \]

**Equation 2. Fourier Law of Heat Conduction With Conductivity Values**

Using the values for the Teflon TFE standoffs where \( k = 0.002 \text{W/m}^2\text{C} \), then assuming a worst-case temperature dispersion between the shroud (hot) and the panel (cold) and lastly filling in the required area and length values generates the equation below.
\[ Q = \frac{.002W \times .0113m^2(86^\circ C - 20^\circ C)}{m^2C(.0032m)} \]

**Equation 3. Solved Conduction Equation for Standoffs**

From the results of the calculation the assumption of negligible conductive heat transfer in comparison to the amount of heat flux generated by the enclosure’s resistive heaters holds true.

The thermal enclosure is made of 8-gauge electrical-grade copper with a one-inch thick copper baseplate. There are three 400-Watt resistive heaters grounded to each wall of the shroud (top, left side, right side) and attached to an Omega 300A thermal controller.

The DAQ (Data Acquisition) interface is located at the lower left corner, exterior to the thermal enclosure but interior to the thermal vacuum chamber. Here the thermistors are all brought through a sealed plate to the exterior of the chamber where they are fed to the Omega controller and reported via a USB link, tracked and saved via a LabView interface to a computer exterior to the cleanroom staging area.

Three interior thermistors were (#1,2,3) placed on different locations of the panel. Two thermistors are permanently located on the shroud itself which are utilized as controller temperatures for the Omega temperature controller system. The shroud also contains four machined Teflon offsets that isolate the thermal enclosure from conduction to the stainless steel interior mass of the thermal
vacuum chamber itself. These are seen on the diagram above at the lower right and left corners of the thermal enclosure. One of the corner standoffs can also be seen in figure 19 below. Note the image to the left is taken from prior to the installation of the thermal shroud into the thermal vacuum chamber while the image to the right has the shroud installed.

![Figure 19. Teflon Thermal Standoff Illustration Pre-Shroud & with Shroud](image)

The experiment utilizes the fact that both convection and conduction, as shown later, have been essentially removed from the system and the only remaining method of heat transfer is radiation. The equation governing the radiative heat exchange of the surrounding heating elements and copper shroud, total \( q_e \), is shown below (SP denotes side-panel – CS denotes copper shroud).

\[
q_e = 2A_{SP}\varepsilon_{SP}\sigma \left[T_{CS}^4 - T_{SP}^4\right] + q_c
\]

Equation 4. Radiative Heat Exchange for Testing
In this equation \( q_c \) designates any additional heat transfer occurring due to minor conduction from the standoffs or from the rarified atmosphere between the side panel and the surrounding enclosure. The theory of Kyte, Madden and Piret\(^{15}\) conveyed in the graph below enables the prediction of the value of \( q_c \) for a generic copper sphere in different levels of a vacuum. It is plotted as a function of the copper sphere and the pressure of the air surrounding it.

![Graph](image)

**Figure 20. Heat Flux (BTU/hr) vs Test Article \( T_c \) vs Pressure\(^{14}\)**

Since we have proven that the atmosphere is “high-vacuum” and the standoffs provide only negligible conduction, using Kyte, Madden and Piret’s theory and data in Figure 20 above we can remove \( q_c \) leaving the following.
\[ q_e = 2A_{SP} \varepsilon_{SP} \sigma \left[ T_{CS}^4 - T_{SP}^4 \right] \]

Equation 5. Reduction of Radiative Heat Transfer using Kyte, Madden and Piret Theory

The \( \varepsilon_{SP} \) designates the emissivity of the side panels (in question) and \( \sigma = 0.1718 \times 10^{-8} \text{ BTU/hr-ft}^2\text{-R}^4 \) is the Stefan Boltzman constant. Using our measured shroud and baseplate temperature along with our known heater input we can find the only unknown, the emissivity value.

\[
\frac{q_e}{2A_{SP} \sigma \left[ T_{CS}^4 - T_{SP}^4 \right]} = \varepsilon_{SP}
\]


Table 7 shows the experimental values obtained and the emissivity derived for each steady state level that was used.

<table>
<thead>
<tr>
<th>Pressure</th>
<th>( T_{CS} ) (°K)</th>
<th>( T_{SP} ) (°K)</th>
<th>( \varepsilon_{SP} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( P &lt; 10^{-5} \text{ Torr} )</td>
<td>312.95</td>
<td>310.75</td>
<td>0.80</td>
</tr>
<tr>
<td>( P &lt; 10^{-5} \text{ Torr} )</td>
<td>318.25</td>
<td>316.35</td>
<td>0.88</td>
</tr>
<tr>
<td>( P &lt; 10^{-5} \text{ Torr} )</td>
<td>322.75</td>
<td>320.85</td>
<td>0.84</td>
</tr>
<tr>
<td>( P &lt; 10^{-5} \text{ Torr} )</td>
<td>327.95</td>
<td>326.05</td>
<td>0.81</td>
</tr>
<tr>
<td>( P &lt; 10^{-5} \text{ Torr} )</td>
<td>342.95</td>
<td>341.55</td>
<td>0.95</td>
</tr>
</tbody>
</table>

*Emissivity values were rounded when calculated in accordance with significant figures.

Temperatures for the copper shroud use thermistor #5 and for the side panel used #2. These
This method of obtaining the emissivity values for the side panel was found post-testing to be highly inaccurate and from a perspective of increasing the accuracy of the side panel emissivity values it was an extremely time consuming failure. Simplifications that Dr. Ross was able to perform in keeping the flux values constant, the surface areas simple and the view factor at 1 made his experiment more accurate, varying only 9% between his maximum and minimum results.

The results for the authors experiment varied 16% from the maximum to the minimum values with the average emissivity being .86 with a standard deviation of .06. The results were most likely affected by different plateau or step values not being held for long enough to be considered steady state. The last step used was 69.8 degrees Celsius and was held for the longest duration (~40 minutes) and as such is probably the most accurate value amongst the data set. If a similar test were to be performed in the future increasing the requirements for steady state to be \( \Delta T_{\text{Panel}} < 1^\circ \text{C} \) per 10 minutes instead of the \( \Delta T_{\text{Panel}} < 1^\circ \text{C} \) per 5 minutes (as was used), would likely decrease the variability in the values obtained. However, increasing the steady state requirements would in turn drastically increase the length of the test beyond the eleven hours already required to complete the thermal vacuum test portion and force the test to be broken into two days, introducing repeatability challenges.

Despite the time invested in trying to experimentally solve for the solder mask emissivity in this manner, the author was forced to find a different way to
more accurately solve for the emissivity. The next method used was not only faster but also far more accurate, though it required hardware that was not previously available when Dr. Ross’ method was attempted.

After performing thermal-vacuum testing and calculating the results for solder mask emissivity, the author recognized that a more accurate testing method would be required. Several experts were contacted including the Raytek office in Santa Cruz, CA. Raytek is a Fluke company that specializes in non-contact infrared temperature measurements. They were able to suggest several products that could support accurately defining the emissivity values of the panels along with a general test-method for finding those values.

3.2.2.2 An Improved Emissivity Test Method Via the RAYTEK MI40

Unfortunately the cost to either purchase an non-contact imager with iterative emissivity support like a Fluke 68 (cost: $575.00) or rent a non-contact thermal imager like the Fluke ti25 (cost: $320.00/day + deposit) were very prohibitive to a student budget and timeline. Fortunately an alternative was found. The release of the Raytek Thermalert MI45, made the MI40 sensor less desirable. With the help of a Raytek sales engineer, a company that had recently purchased the MI45 models to phase out their existing MI40 Thermalert models was found. The author was able to purchase an un-used spare for about one fifth of the OEM price ($470.00) from the company. As a lesson learned to the CubeSat community, where budgets are always tight, asking companies for older or outdated but roughly equivalent versions is a one way to lower testing
equipment costs. Some companies, like Northrop Grumman (previously TRW), will actually donate expensive but used equipment like Thermal Vacuum chambers free of charge provided it will be for educational use.

The MI40 sensor acquired is a ruggedized passive IR sensor designed to measure factory process temperatures with a high degree of accuracy (+/- 1% per Raytek Specification) when a contact sensor is not reasonable. The key to its usefulness for this thesis is that it was the lowest cost available sensor that still had high accuracy and adjustable emissivity. Since the temperature being read via infrared radiation is highly surface dependent, the sensor allows the user to manually adjust the emissivity value to get accurate and repeatable process results. In essence the test necessary to accurately predict the solder mask emissivity was just the reverse of the test procedures that the industrial users require to generate accurate temperature data.

The MI40 has a simple power interface and adjustable output options varying from J or K thermocouples to a simpler 0-5 Volt or 4 – 20 mA output. For the purposes of this test the 0-5 Volt option was used. The unit was connected to a power supply on the test bench in the Advanced Technology Labs and then breakout wires (shown in Figure 21. below) were connected to a lab multimeter. The following settings were adjusted on the MI40 sensor box prior to powering on the unit and beginning the test. The 0-5 V jumper and 4-20 mA jumper were installed leaving the J and K thermocouple jumpers open thus setting the unit to 0-5 V output mode. The emissivity was set to .95 as a starting point for our test-
using figure 3 in the MI40 Operator’s Guide, (figure 21) shown below as a reference. The actual setting jumpers can be seen in figure 22.

Figure 21. Emissivity Reference Panel from MI40 Operator’s Guide

Figure 22. Raytek Thermalert MI40 w/ Breakouts Wires Installed

At this time the test article (CP3 Payload Face board – spare) was laid on the bench and a thermistor was taped with Kapton to the back of the board. The thermocouple (a type J) was connected to the multimeter first and the temperature of the sample was verified to be 21.6°C. Next the thermocouple was
taped to between the sample (board) and the black 3M electrical tape covering one edge of the board and the temperature was again verified to be 21.6°C. Next the thermocouple was removed from the multimeter and leads with plunger hook clips were inserted for connection to the output on the sensor.

![Raytek Thermalert MI40 Post Emissivity Adjustment](image)

*Figure 23. Raytek Thermalert MI40 Post Emissivity Adjustment*

At this point the lens cap on the MI40 was removed, while wearing latex nitrile gloves to ensure no molecular deposition occurred, and the sensor head was carefully pointed at the 3M 49656 Black electrical tape portion of the side panel.
This tape specifically is used because it has a very well defined, tested and documented emissivity value of .95. Having an accurate known emissivity reference provides an easy way to compare the two materials and accurately find the unknown solder mask emissivity value. In figure 25 below even the light balance has very low reflectance off the taped portion of the board, an easy sign that it is a good emitter. As a side note the adhesive on electrical tape should never be placed on a flight satellite panel as it left significant molecular residue requiring extensive IPA and Kimwipes to clean. This test was done using a non-flight board, but as a reference to other CubeSat teams, do not put this tape on your flight board to calculate its surface emissivity!
The voltage was read three times on the multimeter and recorded in a lab notebook. Next the sensor head was pointed at the un-taped portion of the solder masked payload face. Still wearing the latex nitrile kimtech gloves the emissivity gain jumpers were stepped down in .01 increments until the initial measured voltage was reached. This was performed six times, each time re-reading the known .95 emissivity tape and then aiming the sensor head at the solder mask, and adjusting the emissivity gain down until the voltage output was
matched as close as possible to the initial reading. After the third test the thermocouple was reattached to the multimeter and the samples were re-verified to ensure their temperatures were still uniform. This was done at a distance of less than 2 inches away from the material to ensure the sensor heads spot diameter didn’t exceed the width of the taped section.

![Diagram of spot diameter versus distance](image)

**Figure 27. Sensor “Spot Diameter” versus Distance from Raytek Operators Guide.**

Post-test the emissivity values were placed into table format and then combined with the initial method to illustrate the improvement in accuracy, the results of this comparison can be found in Table 25 (next page) with the details included in Appendix D. The average of the emissivity settings using this testing method (shown in Table 19 below) was .90 and so this was the value used going forward to represent the black solder mask emissivity.
While the testing performed to meet Dr. Ross’ prescribed method was an excellent albeit time consuming learning experience the final value that would be used for thermal analyses came exclusively from an average of the MI40 testing whereby the emissivity value was established to be 0.90. This value was within the range of what external sources both online and in print had proposed for solder mask emissive values. Specifically this final value was checked against Clyde Coombs “Printed Circuits Handbook” section 17.11 in which he states “typical solder mask has an emissivity ranging from .85 to .95... depending on the roughness....” Since our solder mask was a dark color with limited surface roughness this value was very reasonable.

<table>
<thead>
<tr>
<th>Run #</th>
<th>Sample (Reference) Temperature</th>
<th>Emissivity (Final Gain Setting)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>21.6</td>
<td>.91</td>
</tr>
<tr>
<td>2</td>
<td>21.6</td>
<td>.90</td>
</tr>
<tr>
<td>3</td>
<td>21.6</td>
<td>.90</td>
</tr>
<tr>
<td>4</td>
<td>21.8</td>
<td>.89</td>
</tr>
<tr>
<td>5</td>
<td>21.8</td>
<td>.90</td>
</tr>
<tr>
<td>6</td>
<td>21.8</td>
<td>.90</td>
</tr>
</tbody>
</table>
3.2.3 Conformal Coat (IPC A-610 Compliant) Radiative Properties

One item that is utilized extensively on spacecraft electronics is conformal coat. NASA-STD-8739.1 defines it as “a thin electrically nonconductive protective coating that conforms to the contours of the PWA.” It consists typically of an epoxy with harder durometer (than other options) a coefficient of thermal expansion (CTE) that is similar to the substrate PCB, high glass transition range and good dielectric properties. It’s used on PCB’s to both stake components, giving them structural support underneath and around their leads (see figure 16. Maxim 1239MEEE Package E16-4 Outline¹²) while also providing them with additional thermal conductivity to the PCB. For some unknown reason the same NASA-STD-8739.1 defines staking exclusively as “an electrically nonconductive adhesive material used for additional support after a part has been attached by mechanical or soldering process,” omitting the thermal conductivity additions that
storing provides. The conformal coat used on CP3 is a transparent polyurethane material called Arathane.

The same process that worked so efficiently for solder mask was performed again for conformal coat and generated the results in the table below. From the test results it appears that the conformal coat had little to no affect on the emissivity of the board. This was only done on locations where the coat was applied over solder mask. Note that for two of the measurements the emissivity did increase. It was postulated after the test that slightly angling the sensor head of the MI40 may have caused it to give an off reading. The emissivity of the conformal coat was shown to be identical to the material below.

<table>
<thead>
<tr>
<th>Table 9. Emissivity Values for Conformal Coated Parts</th>
</tr>
</thead>
<tbody>
<tr>
<td>Run #</td>
</tr>
<tr>
<td>-------</td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td>2</td>
</tr>
<tr>
<td>3</td>
</tr>
<tr>
<td>4</td>
</tr>
<tr>
<td>5</td>
</tr>
<tr>
<td>6</td>
</tr>
</tbody>
</table>

The results of this test could be misleading for other CubeSat teams however. Not all conformal coats are made alike. While the conformal coat used on CP3 is a transparent polyurethane material other conformal coat materials may be opaque or highly pigmented. Additionally, the conformal coat process is highly irregular and usually varies between both personnel and teams as to the end results thickness and variances across the board. Since there are so many
inconsistencies to this material other teams using either a pigmented or opaque coat should independently test their coating for its radiative properties. As a note, previously Arathane material was named Uralane, and NASA documentation still refers to Uralane vice Arathane in most places.

3.2.4. Improved Triple Junction (ITJ) and Ultra Triple Junction (UTJ) Solar Cells

Spectrolab, the solar cell manufacturer and supplier for CP satellites, have extensively quantified the radiative properties for theirs solar cells and data is readily available on them. The figure below is extracted from their available specification sheets. While the CP3 satellite system uses the ITJ model the solar absorptance and emittance is the same for both cell types so future work using UTJ cells will also be covered by these calculations. Note that the 29.5% NeXt Triple Junction (XTJ) Solar Cells do not conform to these values and have a lower absorptance value of .90.

![Thermal Properties Table]

**Figure 29. Spectrolab ITJ and UTJ Cell Radiative Values**

The ITJ solar cells have an per cell of 27.3 cm² and their tabs have an total effective area of 2.32 cm² per panel. Thus 4.2% of the total area taken by the solar panels does not share the absorptance and emittance values provided above. Typically large satellite manufacturers or solar cell providers have
advanced laying techniques that completely eliminate these extra tabs as an external viewable surface but CubeSat teams who purchase the cells at a reduced rate and populate the boards by hand currently do not modify the tabs so there is no loss of contact or lowered power throughput.

From Gilmore, the thin copper foil has an absorptance value of .30 and an emittance value of .02. When these values and the additional surface area are accounted for, the new average emittance value (calculated in Appendix D) is .81 and the absorptance value is .89, for a net change of 4% and 3% respectively.

These changes are not large, but the point in effect is that a CubeSat engineer must often consider design parameters that even large spacecraft designers can negate or assume without repercussion. To be fair, the alternate is also very true. CubeSat designers have the luxury of shorter mission life, more benign LEO orbits and increasingly risk tolerant customers and/or missions than large spacecraft designers do. A decrease in the absorbed thermal energy by 5% is enveloped by conservatism and wide allowable temperatures for an engineer whose solar panels are isolated (e.g. Loral), use a spinning “thermal roll” design (e.g. Hughes) or have large tolerances (e.g. JPL/Northrop), but to a picosatellite which has none of the above, this is valuable data and can be used to more accurately predict the on-orbit environment.
3.3 Thermal Analyses and Models

In this section several computations running the gamut of thermal analysis options are presented. First and foremost in the progression is the initial “back of the envelope” P’s office analysis which spawned this whole idea. As this thesis is written in the spirit of CubeSats, the next portion is a review of the basics of spacecraft thermal analysis and their application to CubeSats so as to support other teams basic analysis needs. The last three sections carry the detailed thermal equilibrium calculations, heat generation by mode estimates, heat dispersion conduits and electrical “resistor” theory application, and finally the computer aided design (CAD) finite element and finite difference model generations and solutions.

3.3.1 P’s Office

Because it could not be left out, the first calculation performed in 2006 which became the starting point for this thesis is attached, scanned in its original form in Appendix F. This calculation provided the idea that, considering an extremely simplified scenario, a picosatellite (specifically CP2 or CP3) could generate enough thermal energy to have a positive effect on their orbital temperature. The calculation utilized the equation 7 below in section 3.2.2, found in SMAD, to estimate a worst-case cold orbital equilibrium scenario with a one Watt averaged CP3 power use and heat generation. This resulted in a temperature of 254.4K. The second calculation utilized the same worst-case scenario but with an increased, albeit reasonable, heat generation of five Watts.
average for the satellite and resulted in a temperature of 273.3K. This difference in temperature (18.9K) was non-trivial and provided the first evidence that the CP bus, in an extremely simplified scenario, had the required heat generation to positively impact its thermal condition on orbit. As a note the equation we used neglects the background temperature of space (4K), which added conservatism to our cold-case scenario.

\[ Q_{\text{internal}} + Q_{\text{external}}A_{\text{satellite}} = \varepsilon_{\text{Satellite}}\sigma A_{\text{Satellite}}(T_{\text{Satellite}}^4) \]

Equation 7. SMAD Basic Satellite Thermal Balance Equation^{Ac}

3.3.2 Satellite Thermal Analysis Overview

This section provides a background and review of the key principles, properties and governing equations within satellite thermal analysis theory. It follows the general analysis review presented by Aerospace Corporation’s “Spacecraft Thermal Control Handbook” and performed by Phillips and Gilmore\(^2\). Since the current generation of CubeSats do not use advanced heat pipes or pumped fluid loops the only two methods of heat transfer used in analysis for this thesis were conduction and radiation. Fourier’s Law governing conduction is shown below in Equation 8. The heat flux between objects is \( q'' \), typically expressed in watts per meter squared (W/m\(^2\)).\(^{17}\) The temperature gradient across the surface is \( \frac{dT}{dx} \), typically expressed in terms of Kelvin per meter (K/m). Lastly \( k \) is the thermal conductivity of the material, typically expressed in terms of watts per meter degree Kelvin (W/m\(^o\)K).
\[ q'' = -k \frac{dT}{dx} \]

Equation 8. Fourier’s Law

Radiation is the sole method of energy addition and rejection for a satellite. The surface properties and geometry of a satellite determine the amount of energy a satellite takes in, reflects and emits. The amount of radiation absorbed by a surface is relative to a property called absorptivity, represented typically by the greek letter alpha, \( \alpha \). The amount of radiation emitted from a surface is relative to a property called emissivity, represented typically by the greek letter epsilon, \( \varepsilon \). The amount of radiation transmitted through a material (surface) is relative to a property called transmissivity, represented by the greek letter tau, \( \tau \). Lastly the amount of energy reflected by a surface is relative to a property called reflectivity, often represented by the greek letter lambda \( \lambda \).\(^{18}\)

Figure 30. Incident Radiation on a Surface\(^2\)

Figure 30 above clearly illustrates that of all the incident radiation (Q), some is reflected \( (Q_p) \), some is absorbed \( (Q_\alpha) \) and becomes heat, and lastly some is
transmitted through the body ($Q_t$). In addition to these three, when the surface itself is warmer than absolute zero it will emit IR radiation ($Q_\epsilon$). For spacecraft purposes the two properties used most are absorptivity and emissivity with each being specially quantified as solar and infrared (IR), respectively. By categorizing them in this manner it reminds anyone looking up or applying the property that the specified absorptivity value applies to the solar spectrum while the emissivity property governs the IR portion of the spectrum. Some typical values for these properties are shown in the table below. Another key property listed in the table below ($\alpha/\epsilon$) is very useful to spacecraft thermal engineers. This material property gives a rough idea of whether the material will be a good emitter (low $\alpha/\epsilon$) or a good absorber (high $\alpha/\epsilon$). Put simply if a thermal engineer is choosing their surface materials to keep their satellite cool they’ll choose a low $\alpha/\epsilon$ value material and if they need to keep their satellite warm they’ll choose a high $\alpha/\epsilon$ value material.
The table above specifically makes no mention of several of the materials that are exposed on a CubeSat’s external surfaces. This is because, as discussed in Section 2, typical spacecraft thermal designers don’t have to work inside the confines of a picosatellite’s volume, mass and budget. Their side panels are typically an aluminum honeycomb or composite layers with a carefully applied exterior surface finish. As CubeSats rely heavily on printed circuit boards and integrated circuits their external surfaces, outside of solar panel areas, are almost exclusively one of those two materials, neither of which is particularly well suited for additional thermal surface treatment (MLI excluded). This architecture of PCB panels with aluminum sub-structure is part of what enables satellites like CP2 and CP3 to have over 40% of their mass budget available to payload systems, altering it would mean potentially diminishing the payload mass and the
entire justification for the mission (i.e. the payload). Since this design is critical to the entire architecture, the thermal design cannot drive a change to it but must instead find ways to work with it. Adaptive component usage is one way to work with this architecture. Figure 31 shows, for comparison and calculation purposes, the average surface area properties of the CP3 satellite. Refer to Appendix D for the detailed calculations used to generate an accurate value for the entire surface of the spacecraft.

<table>
<thead>
<tr>
<th>SATELLITE SURFACE AVERAGED</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Emissivity</td>
<td>0.84</td>
</tr>
<tr>
<td>Absorptivity</td>
<td>0.87</td>
</tr>
<tr>
<td>E/Alpha</td>
<td>0.97</td>
</tr>
</tbody>
</table>

**Figure 31. Corrected Satellite Surface Property Averages (Satellite Whole)**

The corrected surface properties give CP3 an $\varepsilon/\alpha$ ratio of .97. Using these surface properties above you must develop an energy balance equation showing all the radiant energy entering and exiting the system. The conservation of energy assures us that the sum of the absorbed energy and the emitted energy will come to a steady state over time. This assumes we are not converting any stored chemical energy into heat.

$$Q_{Absorbed} - Q_{Radiated} = 0$$

**Equation 9. Energy Balance Equation**

Due to the satellites internal heat generation capabilities a $Q_{\text{internal}}$ should be added generating the equation below.
\[ Q_{Absorbed} - Q_{Radiated} + Q_{Internal} = 0 \]

**Equation 10. Energy Balance Equation with Internal Heat Generation**

The governing equation for radiative heat flux to space is shown below where the symbol sigma (\(\sigma\)) denotes the Stefan-Boltzman constant and is equal to 5.67x10\(^8\) W/m\(^2\)K\(^4\). The satellite temperature is denoted as \(T_{Satellite}\), the temperature of space is denoted by \(T_{Space}\) and the emissivity of the spacecraft is denoted with the symbol epsilon.

\[ Q_{Radiation} = \varepsilon_{Satellite} \sigma A (T_{Satellite}^4 - T_{Space}^4) \]


For satellites in Earth orbit the radiation flux can be broken into three components. The first component is solar flux and it is the largest potential energy source of the three. Even with the 11-year solar cycle the amount of energy radiating from the sun varies within a fraction of 1% at all times.\(^2\) Due to the eccentricity of its orbit, however, there is a slight alteration of the amount of incident solar energy reaching. The ‘average’ heating values used throughout this thesis are per Goddard Space Flight Centers Guideline GD-AP-2301 document. This is considered to be the NASA standard for Earth orbit environmental heating. At the summer solstice when Earth is farthest from the sun the intensity (shown below as \(Q_{Sun}\)) is at its minimum of 1318 W/m\(^2\) and during the winter solstice when the Earth is closest to the sun it is at its maximum of 1422 W/m\(^2\). the average intensity value for solar flux is 1367.5 W/m\(^2\) and its
measured when the Earth is at its standard distance of 1 AU.² To calculate the solar flux being absorbed by a satellite use a derivation of equation 12, shown below.

\[ Q_{\text{Solar}} = Q_{\text{Sun}} A_{\text{Satellite}} \alpha_{\text{Satellite}} \]

Equation 12. Solar Flux Calculation

For completeness the solar flux should also be adjusted for the angle of the spacecraft surface with respect to the sun. The amount of energy being absorbed by the surface area is a function of both its absorptivity and the angle psi (ψ) between its surface normal and the sun (modeled as a point source).

\[ Q_{\text{Solar}} = Q_{\text{Sun}} A_{\text{Satellite}} \alpha_{\text{Satellite}} \cos(\psi) \]

Equation 13. Solar Flux Adjusted for Spacecraft Angle

The next component of radiative flux on orbit is Albedo. This is made up of solar energy being reflected off of a planet or moon, in the case of CubeSats this is Earth albedo. Unlike solar flux albedo can fluctuate significantly and is highly dependent on the location of the satellite. D. Gilmore notes in his book “Spacecraft Thermal Control Handbook” that variations in albedo make selection of the best albedo value for a certain thermal analysis rather uncertain and variations throughout the aerospace industry are common.² Figure 32 below was produced by Lockheed Corporation to serve as an estimating tool for Earth albedo.
To determine the albedo value from the calculation chart one must first establish their orbital properties and then using their beta angle and the reference surface geometry an approximate value for albedo can be obtained. Another method for establishing the albedo value is shown in Equation X below. Here the user must input the albedo percentage, which from NASA guidelines varies from .25 to .35. This method is only suggested for developing maximum and minimum case values (using .30 and .23 respectively). $Q_{\text{Sun}}$ is the chosen solar heat flux, $a$ is the chosen albedo percentage, $r_e$ is the radius of the Earth.

Figure 32. Solar Albedo Calculation Chart\[^2\]
\[ Q_{\text{Albedo}} = Q_{\text{Sun}} \alpha K \left[ \frac{r_e^2}{(r_e + h)^2} \right] A_{\text{Satellite}} \]

**Equation 14. Albedo Heat Load**

K is defined as the reflected solar energy factor, which accounts for the reflection properties as the albedo goes through the atmosphere and is defined separately below.

\[ K = .664 + .521 - .203 \left( \frac{r_e}{r_e + h} \right)^2 \]

**Equation 15. Reflected Solar Energy Factor\(^\text{19}\)**

Earthshine is the long wave IR radiation that every location on the planet produces. It allows the entire planet to maintain its own thermal stability. For Low Earth Orbits (LEO) it can be a significant heating factor and when in eclipse it becomes the sole external heating factor available to the satellite. In Equation X below, \( Q_{\text{IR}} \) is the total flux of long wave IR from the Earth.

\[ Q_{\text{IR}} = Q_{\text{Earth IR}} \left( \frac{r_e^2}{(r_e + h)^2} \right) \varepsilon A_{\text{Satellite}} \]

**Equation 16. Earthshine IR Radiation**
Another method for establishing Earthshine that is utilized by the Lockheed Martin Company is a calculation chart shown in figure 33 (above). Data points can be interpolated and a maximum and minimum can be established in that manner as well. An interesting feature of Earthshine is its variability. According to data collected by NASA on the Earth Radiation Budget Experiment (ERBE) its value ranges from 108 W/m$^2$ to 300 W/m$^2$ depending on the orbital and ground conditions. See the figure 34 below illustrating the wide variability for a LEO satellite.
The combination of the three listed radiation sources vary throughout the orbit of a satellite and at some points all three generate a maximum heating condition and at other points only IR exists and the heating condition is minimized. The Figure 35 below illustrates the combination of the sources along with some nominal values.
The minimum and maximum conditions are shown in figures 36 and 37, illustrating how the CubeSat is eclipsed by the Earth, creating a minimum heating condition, and then comes out of eclipse and receives the maximum amount of radiative heat flux.

![Figure 36. Worst Case Cold Condition](image)

For CP2 and CP3 the Albedo loads are appropriate to model as a collimated source simply because the altitude in question is above the point where Albedo is
affected by orientation and shading. For most spacecraft a general assumption for albedo view factor is 0.3 with a cosine reduction (as discussed in the analytical baseline section 3.3.3) in the reflected energy from the subsolar point of the orbit to the terminator.

3.3.3 General Thermal Equilibrium Calculation (Analytical Baseline)

To formalize the back of the envelope analysis and provide the necessary information for other CubeSat teams to calculate their own accurate equilibrium hot and cold temperatures the steps leading to the radiative loading are detailed in this section. The hottest orientation, as shown in Figure 37 in section 3.3.2, results when all three radiative heat fluxes are being applied on the largest area of the satellite. Note that the Albedo load factor used is well above the Goddard guideline; this is because those limits are to be used only for average orbital equilibrium calculations and not for maximum loading conditions. With our orbit inclination being greater than 90 degrees, the following worst-case hot heat load assumptions can be made:

- Earth IR is 244 Watts/m²
- Solar Albedo is 57 percent
- Solar Flux is 1414 W/m²

The equilibrium state calculations rely upon the surface area maximum and minimum values for the viewable cross-section of the satellite, which changes, on orbit with respect to attitude. A calculation to determine the cross sectional area
was performed and is included in Appendix G. The resulting graphic below of maximum incident area (0.01732 m$^2$) was generated from this calculation.

![Satellite Area Projection Calculator (m^2)](image)

**Figure 38. Satellite Area Projection Calculator – Surface Area vs. Attitude**

Maximum area is not a reasonable assumption for picosatellites like CP2 and CP3 however because they do have some amount of roll, pitch and yaw while on orbit. Taking an average of the projected area is far more realistic and gives a result of 0.0149 m$^2$. This also lowers the affect of the reflectivity on the exposed surfaces to a point where it can be neglected for our maximum and minimum calculations.

Using the inputs derived for the hot case generated the values in the table below (supporting calculations and tables shown in Appendix H). Adjusting to account for the true surface emissivity and absorptivity of the CubeSat being evaluated (CP3) resulted in a 10 degrees change to the worst-case hot values of slightly over (12% difference). It was also interesting to note that Gilmore’s suggestion of limiting Earthshine to only one face generated a significantly lower worst-case hot situation.
Table 11. Hot Case Equilibrium Calculation Results

<table>
<thead>
<tr>
<th>CASE</th>
<th>Solved for T Kelvin</th>
<th>(Celsius)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CASE 4 - Hot w/o Adjustments</td>
<td>356</td>
<td>83</td>
</tr>
<tr>
<td>CASE 5 - Hot w/ Adjustments</td>
<td>346</td>
<td>73</td>
</tr>
<tr>
<td>CASE 6 - Hot w/ Single Face (Gilmore)</td>
<td>315</td>
<td>42</td>
</tr>
</tbody>
</table>

After reviewing these calculation results against on-orbit measurements, the Gilmore result (while not conservative) was found to be far more accurate a prediction (for hot case conditions). Actual on-orbit measurements established during the operation of CP3 peaked around 40 degrees Celsius (see figure 39).

![External Temperatures](image)

Figure 39. External On-Orbit Temperature Maximums (Externals)

The coldest orientation, as shown in figure 36 in section 3.3.2, occurs when the minimum Earth IR is being applied across the smallest cross sectional area of the satellite. With an orbit inclination of greater than 90 degrees the following worst-case cold heat load assumptions can be made:
• Earth IR is 218 Watts/m$^2$
• Solar Albedo is 0 percent (eclipse)
• Solar Flux is 0 W/m$^2$ (eclipse)

Using these assumptions and a worst-case area projection for the cold case was calculated and is shown in the following table (supporting calculations shown in Appendix H). The table also shows a best-case IR assumption that J. A. Wiebelt suggests in his book.$^{18}$ This assumes that Earth IR is a diffuse property due to the longer wavelength bending in the atmosphere and should be applied on all viewable surfaces. This obviously generates a more favorable cold case temperature of -32°C.

<table>
<thead>
<tr>
<th>CASE</th>
<th>Solved for T Kelvin</th>
<th>(Celsius)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2a - Cold w/o Adjustments</td>
<td>260</td>
<td>-13</td>
</tr>
<tr>
<td>2b - Cold w/ Adjustments</td>
<td>241</td>
<td>-32</td>
</tr>
<tr>
<td>2c - Cold w/ Single Face (Gilmore)</td>
<td>205</td>
<td>-68</td>
</tr>
</tbody>
</table>

The cold case condition, using Gilmore’s method, was found to be, as expected, far more conservative at -68°C than the other methods, which account for diffuse IR. The adjustments made based on the detailed measurement and calculations of the true surface emissivity and absorptivity of the CubeSat being evaluated (CP3) resulted in a significant change to the worst-case cold values of almost 20°C lower (146% lower). As you can see in figure 40 the actual on-orbit worst-case (external-thermistor) cold temperatures (-33°C) lined up quite well with the adjusted calculations. The actual mass of the spacecraft on orbit is not
at -33°C but the external thermistors and surfaces react quickly (shock) to the eclipse environment.

![External Temperatures Graph]

**Figure 40. External On-Orbit Temperature Maximums (Externals)**

This differential in cold case calculated temperature was one of the most substantial pieces of evidence that validated the importance of a CubeSat designer accurately evaluating, measuring and calculating their system’s surface properties.

To move beyond the equilibrium worst-case evaluations the basic orbital parameters for the satellite in question need to be established so that beta angle, eclipse period and other key thermal parameters change over time could be deduced. This began with calculating the period, or time required to make one orbit about the Earth, and used the following equation.
\[ P = 2\pi \left( \frac{a^3}{\mu} \right)^{\frac{5}{3}} \]

**Equation 17. Orbital Period**

In the orbital period equation \( P \) is the period in seconds, \( \mu \) is the product of the universal gravitational constant and the mass of the planet where for LEO satellites \( \mu \) is equal to \( 3.986 \times 10^{14} \text{m}^3/\text{s}^2 \) and \( a \) is the semimajor axis of the orbit, the average of the radius of perigee and apogee. For our CP3 orbit we had a semimajor axis of 7096 km leading to an (circular assumption) orbital period of 5949 seconds or 99 minutes. One of the most useful orbital parameters from a thermal standpoint is the beta angle \( \beta \). It helps in visualizing the orbital thermal environment for LEO satellites.\(^2\) It is defined to be the minimum angle from the orbital plane and the solar vector with a value between -90 and +90 degrees. The equation for beta angle is shown below.

\[
\beta = \sin^{-1}(\cos(\delta_s) \sin(RI) \sin(\Omega - \Omega_s) + \sin(\delta_s) \cos(RI))
\]

**Equation 18. Calculating Beta Angle Given Ascending Nodes**

In this equation \( \delta_s \) is the declination of the sun, \( RI \) is the orbit inclination, \( \Omega_s \) is the right ascension of the sun, and \( \Omega \) is the right ascension of the ascending node.\(^2\) Given that the DNEPR launch was at local 2200 the beta angle was found to be approximately 44 degrees which, using the equation below, generated an eclipse fraction of .33.
\[
    f_E = \frac{1}{180} \cos^{-1} \left[ \frac{(h^2 + 2Rh)^5}{(R + h) \cos(B)} \right]
\]

**Equation 19. Eclipse Fraction by Beta Angle and Altitude**

Using STK the beta angle was calculated and plotted in Figure 41 below (starting angle is denoted with the red marker). The variance in beta angle causes variances in the lighting conditions present on the satellite and the resulting eclipse lengths.

Figure 41. Beta Angle Plot (v. Time)

The eclipse and lighting conditions present on the satellite during its first twelve hours on orbit are shown below in Figure 42. The umbra (eclipse) and penumbra (partial eclipse) are clearly about 30 minutes with a precise value of 32.98 minutes. One of the key parts of this graph is to illustrate the speed at which CP3 moves from complete eclipse into full sun. Known as thermal shock this situation takes the satellite from its coldest condition seeing minimal Earthshine and the cold of deep space into full solar irradiance and full albedo reflection as well.
With an established eclipse fraction and orbital period a concise table was built to track the values for use in radiative loading iterations and analyses. The table below shows the general orbital properties and the sunlit orbit fraction.

Table 13. CP3 Calculated Orbital Properties

<table>
<thead>
<tr>
<th>Orbit Properties</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Semimajor Axis:</td>
<td>7105.20 km</td>
</tr>
<tr>
<td>Eccentricity:</td>
<td>0.010</td>
</tr>
<tr>
<td>Inclination:</td>
<td>98.00 deg.</td>
</tr>
<tr>
<td>Period:</td>
<td>99.330 minutes</td>
</tr>
<tr>
<td>Sunlit Orbit Fraction:</td>
<td>0.67</td>
</tr>
</tbody>
</table>

3.3.4 Heat Generation by Mode

Satellites generate heat while running their electronics and the only difference with CubeSats is there is obviously less power available to generate heat due to their size and solar panel limitations. One of the greatest power draws on the CP bus is a COTS amplifier (RFMD RF2117) designed for use with
RF signals between 400 and 500 MHz. The part, as implemented on the CP bus, operates at a supply voltage of 3V and sinks a maximum of 1,100 mA of current, thus the 3V operation consumes approximately 3.3 W.\(^{20}\)

One of the other heavy power draws on the system is the Attitude Determination and Control Subsystem (ADCS). It pulls approximately 1,000 mA off the battery directly using 3.7-4.0 W total. The ADCS also has a specific command feature allowing the power draw, duty factor and duty cycle to be set independent of one another. Beyond the communications system’s beacon settings there are no other subsystems that have a unique command scheme like this. These two (ADCS & C&DH) subsystems make up almost three quarters of the total energy being used during the (peak) operation of the satellite. Logically with such large power draws these were also the prime subsystems for attempting to provide an on-orbit heat supply. The total power draw for each subsystem is shown in Table 14 below.

<table>
<thead>
<tr>
<th>Component</th>
<th>Current: 6V</th>
<th>Current: Reg</th>
<th>Orbit Average Duty Factor</th>
<th>Peak Sub-system Power</th>
<th>Average Sub-system Power</th>
</tr>
</thead>
<tbody>
<tr>
<td>Command Data Handling:</td>
<td>0 amps</td>
<td>0.026 amps</td>
<td>1</td>
<td>0.078 watts</td>
<td>0.0858 watts</td>
</tr>
<tr>
<td>Communications:</td>
<td>0 amps</td>
<td>0.024 amps</td>
<td>1</td>
<td>0.072 watts</td>
<td>0.0792 watts</td>
</tr>
<tr>
<td>Transmitter:</td>
<td>1 amps</td>
<td>0 amps</td>
<td>0.1</td>
<td>3.7 watts</td>
<td>0.37 watts</td>
</tr>
<tr>
<td>Side Panels:</td>
<td>0 amps</td>
<td>0.035 amps</td>
<td>1</td>
<td>0.105 watts</td>
<td>0.1155 watts</td>
</tr>
<tr>
<td>Attitude Control System:</td>
<td>1 amps</td>
<td>0 amps</td>
<td>0.01</td>
<td>3.7 watts</td>
<td>0.037 watts</td>
</tr>
<tr>
<td>Payload:</td>
<td>0.25 amps</td>
<td>0 amps</td>
<td>0.3</td>
<td>0.925 watts</td>
<td>0.2775 watts</td>
</tr>
<tr>
<td>TOTAL</td>
<td>2.25 amps</td>
<td>0.085 amps</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Total Output Power/Energy Per Regulator:</td>
<td>9.45 watts</td>
<td>0.255 watts</td>
<td>8.58 watts</td>
<td>0.965 watts</td>
<td></td>
</tr>
<tr>
<td>Total Input Power/Energy Per Regulator:</td>
<td>10.0531915 watts</td>
<td>0.30141844 watts</td>
<td>10.35460993 watts</td>
<td>1.16459191 watts</td>
<td></td>
</tr>
</tbody>
</table>

Using the power draws by subsystem above and implementing them into command schemas with associated duty factor adjustments for testing and verification generated the following table 15.
The heat input by each mode was calculated above to support the FD/FEA simulations in section 3.3.5 and the analytical hot and cold case presented in table 11 and table 12 in section 3.3.3. These were based on data from previous testing on both CP2 and CP3. Post physical testing the PPI and SPI downloaded were used to confirm actual power draw. As a note this is not an accurate or precise enough method to confirm anything below ~.3W/hr but was the best available bus power metric, and was considered acceptable for these purposes. If more electrical engineers were available, and if time allowed, detailed measurements both pre and post-testing of the battery voltage along with post-test battery charge timelines could be measured to increase the accuracy of these calculations.

The potential heating modes were evaluated analytically using a steady state average based on the actual power usage cycle (table 15. Heat Generation

<table>
<thead>
<tr>
<th>S/C Modes &amp; Associated Heating (Averaged by Duty Factor)</th>
<th>C&amp;DH</th>
<th>COMM</th>
<th>RF</th>
<th>Side Panels</th>
<th>ADCS</th>
<th>Payload</th>
<th>TOTAL Draw [W]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mode 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Standard Operations</td>
<td>0.0858</td>
<td>0.0792</td>
<td>0.3700</td>
<td>0.1155</td>
<td>0.0370</td>
<td>0.2775</td>
<td>1.062</td>
</tr>
<tr>
<td>Mode 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Increased Beacon Rate (90 sec)</td>
<td>0.0858</td>
<td>0.0792</td>
<td>0.5740</td>
<td>0.1155</td>
<td>0.0370</td>
<td>0.2775</td>
<td>1.286</td>
</tr>
<tr>
<td>Increased Beacon Rate (60 sec)</td>
<td>0.0910</td>
<td>0.0792</td>
<td>0.9600</td>
<td>0.1155</td>
<td>0.0370</td>
<td>0.2775</td>
<td>1.716</td>
</tr>
<tr>
<td>Mode 3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Mission Payload ENABLED (On)</td>
<td>0.0910</td>
<td>0.0792</td>
<td>0.3700</td>
<td>0.1155</td>
<td>0.0370</td>
<td>0.9250</td>
<td>1.779</td>
</tr>
<tr>
<td>Mode 4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ADC Off</td>
<td>0.0910</td>
<td>0.0792</td>
<td>0.3700</td>
<td>0.1155</td>
<td>3.7000</td>
<td>0.2775</td>
<td>5.097</td>
</tr>
<tr>
<td>ADC On &amp; Increased Beacon (90 sec)</td>
<td>0.0910</td>
<td>0.0792</td>
<td>0.3740</td>
<td>0.1155</td>
<td>3.7000</td>
<td>0.2775</td>
<td>5.321</td>
</tr>
<tr>
<td>ADC On &amp; Increased Beacon (60 sec)</td>
<td>0.0910</td>
<td>0.0792</td>
<td>0.9600</td>
<td>0.1155</td>
<td>3.7000</td>
<td>0.2775</td>
<td>5.746</td>
</tr>
<tr>
<td>Mode 5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Increased &quot;Data Drop&quot; Contact (-34DF)</td>
<td>0.0910</td>
<td>0.0792</td>
<td>2.4790</td>
<td>0.1155</td>
<td>0.0370</td>
<td>0.2775</td>
<td>3.387</td>
</tr>
</tbody>
</table>
by Mode). Using the equations and methodology presented in 3.3.3 the different satellite modes produced the following Cold Case by Mode, table 16 below.

**Table 16. Cold Case by Mode**

<table>
<thead>
<tr>
<th>CASE 3 - Mode 1 (Standard Operations)</th>
<th>Solved for T Kelvin</th>
<th>(Celsius)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CASE 3 - Mode 2a, (Inc. Beacon Rate - 90s)</td>
<td>253</td>
<td>-20</td>
</tr>
<tr>
<td>CASE 3 - Mode 2b, (Inc. Beacon Rate - 60s)</td>
<td>258</td>
<td>-15</td>
</tr>
<tr>
<td>CASE 3 - Mode 3, (Payload Enabled - ON)</td>
<td>258</td>
<td>-15</td>
</tr>
<tr>
<td>CASE 3 - Mode 4a, (ADC ON)</td>
<td>287</td>
<td>14</td>
</tr>
<tr>
<td>CASE 3 - Mode 4b, (ADC ON &amp; Beacon Inc. - 90s)</td>
<td>289</td>
<td>16</td>
</tr>
<tr>
<td>CASE 3 - Mode 4c, (ADC ON &amp; Beacon Inc. - 60s)</td>
<td>292</td>
<td>19</td>
</tr>
</tbody>
</table>

From the results obtained by evaluating the adjusted baseline cold case against an identical exterior condition but with the supplementary \(Q_{\text{Internal}}\) heat flux being supplied by each of the modes it became evident that the initial evaluation held true. This took into consideration all of the detailed surface properties along with a comprehensive internal heat generation calculation. The results for each mode were reviewed against verification criteria 1.1, repeated below for reference and calculated in table 17.

1. The thermal condition of the satellite shall be more* stable with the implementation of the thesis than via passive control and standard on-orbit operations.
   1.1. *A 15% reduction in spacecraft subsystem component temperature variability across maximum and minimum orbital heat flux conditions
As can be seen in table 17, the verification criterion that called for a reduction of at least 15% in the variability across minimum and maximum conditions was met by modes 2b, 3, 4a, 4b and 5. Mode 2a had an 11% reduction that shows a significant improvement but did not quite meet the established criterion. Since equilibrium calculations negate transient effects on orbit this mode still could be verified through thermal vacuum testing and on-orbit tests but modes 3, 4a, 4b and 5 obviously meet the verification criterion with margin.

These conditions and heating values assume we are maintaining a constant maximum temperature consistent with our standard operations mode. This assumption has a different level of applicability for each set of modes. Based on the ability to set modes 3 and 4 with distinct expiration timelines, this assumption is inherently true. Mode 5 requires no assumption since it already maintains that positive contact must be made and if no command is sent during maximum heat conditions it will not continue; therefore it is a true assumption. Modes 2a, 2b, 4b and 4c require users to enable/disable them. Assuming that a user can disable the mode prior to coming out of eclipse is not always realistic,
especially if contact or acceptable passes are limited. However modes 4a and 4b inherently will degrade to modes 2a and 2b (ADCS “OFF”) and as such will not drive a significant increase in worst-case hot temperatures (see Appendix H for “Worst-Case Hot Mode Adjusted”).

For a secondary verification, using worst-case conditions, the SEET tool in STK was used to model a single-node CP3 with the mass, surface properties and then placed into the appropriate DNEPR orbit (792 km x 645 km) and allowed to propagate over several orbits. The same simulation was performed for several internal heating conditions (noted in each figure). The conditions that SEET uses as a standard are significantly lower in incident energy than those recommended by both Wertz and Gilmore and as such come in with lower temperatures than were calculated using energy balance thermal equilibrium equations (even using Gilmore’s single face equilibrium equation). In the SEET simulations of CP3’s the standard operations mode the satellite mass varied between -74°C and 33°C.

![CP3 - SEET Thermal](image)

*Figure 43. Baseline Three Hour Post-Launch Run (Detail View)*
As the author noted that the SEET simulations were predicting lower than expected temperatures, the decision was made for all of the simulations performed to add in the solar flux variable thus showing the flux value that SEET was using throughout the lifetime of the simulation. The figure below shows the multiple cycles that SEET predicted for the initial 12 hours of CP3’s orbit. As these temperatures are realistic external exposures this clearly illustrates the demanding environment these electronics are being placed into.

![Figure 44. CP3 with 1.062W “Standard Operations” Internal Heat Dissipation](image)

As mode 2a had almost no visible effect on the lower bound of the satellite temperature it was excluded from the results presented. Mode 2b had a more significant effect and is shown below in figure 45. The lower bound for the satellite temperature was decreased by over 10°C due to the extra heat being generated.
Figure 45. Mode 2b. Increased 60s Beacon Rate with 1.78W Internal Heat Dissipation

The next significant mode that showed an increase worth noting was mode 5 and the total increase for this mode was found to be over 15°C. It was during this mode that the upper bound of the spacecraft’s observed temperatures really began to spike. SEET does not have the capability to deal with step function inputs and as such the upper bounds for modes 4a, 4b, 4c and 5 are not true representations of the satellites temperature (as the heat being generated by these modes would be eliminated before leaving umbra conditions).

Figure 46. Mode 5 – Increased Data Drop Simulation with 3.38 W Internal Heat Dissipation
The final cases run using SEET were for the ADC modes 4a, 4b and 4c. These modes showed significant improvements moving the initial low temperature prediction from -74°C up to -41°C, a 33-degree improvement and a reduction of 31% in the temperature range of the satellite while operating under mode 4a.

![Graph](image1)

**Figure 47. Mode 4a. ADC System “ON” with 5.09 W Internal Heat Dissipation**

For Mode 4c the low temperature condition was found to be -33°C. In this “best case” mode the on-orbit temperature difference was reduced by 38%.

![Graph](image2)

**Figure 48. Mode 4c. ADC System “ON” and 60s Beacon Rate with 5.74 W Internal Heat Dissipation**
The satellite theoretically has approximately a 9 Watt heating capability, unrealistic for any operations as it causes a certain “brown-out” condition, it was simulated in SEET as a “what-if” condition as extra analysis if SEET would not harm any physical hardware (as testing this mode in T-VAC most certainly would). The 9W heating condition resulted in an extremely favorable lower bound of -14°C and reduced the on-orbit SEET predicted temperature swing by 56%.

Figure 49. Heating Case with 9 Watt Internal Heat Dissipation
3.3.5 Heat Flux Path Mapping via Resistor Model

Thermal energy has the unique property of being able to use electrical resistance concepts and theories. Just as voltage from high to low across a resistor cause current, temperatures from high to low across a material cause heat flux. Heat flux can thus be represented as current, temperatures as voltages, material resistances (material through which heat flux is flowing) as resistors, heat sources as current sources and finally thermal masses as capacitors.

Using these substitutions one can take the physical layout of the system in question and apply both Fourier’s Law along with Equation 7 (Section 3.3.2) to create a representation of the satellite system’s heat transfer paths and resistances. The two figures below, 50 and 51 were taken from a surface mount component supplier and give a detailed view of how this theory is applied. Figure 50 shows the physical component layout and the “resistances” it must pass through in order to reject heat.
Figure 50. Surface Mount Component (PIC) Material Layers

Figure 51 below shows the equivalent electrical circuit that equates to the heat transfer for the physical scenario above. This figure also shows the necessary electrical components to not only calculate the equivalent resistance but also to simulate the flow of heat and different temperatures in the system.

Figure 51. Surface Mount Component Resistance Network & Heat Flow (Example)
As an example, the graphic below (figure 52) was created to show the heat flowing to and from a side panel and into the CubeSat. In this figure $R_S$ stands for the resistance of a side panel, $R_{4-40}$ stands for the resistance of each of the bolts connecting the panel to the structure, and $R_{ST}$ represents the aluminum structure of the satellite. This analysis follows the reference from Gilmore and Phillips.\(^2\) The thermal resistance for 4-40 steel fasteners in Gilmore is 3.79°C/W.

\[ R_{EQ} = R_S + \frac{R_{4-40}}{4} + R_{ST} + \frac{R_{4-40}}{4} + R_S \]

**Equation 20. Equivalent Resistance from Side Panel to Side Panel (Lateral)**

This calculation, along with Fourier’s Law for $R_S$ and $R_{ST}$, generates equation 21 where $x_S$ denotes the distance the heat must travel in the side panel, $k_S$ denotes
the conductivity of the side panel, $A_S$ denotes the cross-sectional area that the heat can flow through. The structural resistance follows the same denotation scheme.

$$R_{EQ} = \frac{x_S}{k_S * A_S} + \frac{R_{4-40}}{4} + \frac{x_{ST}}{k_{ST} * A_{ST}} + \frac{R_{4-40}}{4} + \frac{x_S}{k_S * A_S}$$

Equation 21. $R_{EQ}$ from Side Panel to Side Panel (Lateral) with Fourier’s Substitution

Using the equation above the equivalent thermal resistance was calculated (Appendix I) to be 23.3 K/W or 23.3°C/W. This is surprisingly consistent with on-orbit panel-to-panel results. The on-orbit dispersion between an illuminated “hot” side panel and a non-illuminated “cold” panel is in a similar range (shown in figure 53). While the heating load is dispersed and not a uniform 1W condition, if we use a rough measure of solar power incident on one panel (~1W) this resulting resistance shows that the model is reasonable.
Another measure of thermal resistance for the CP3 CubeSat is between batteries mounted on an interior panel and the exterior of the satellite. This is a far more convoluted resistance network as it involves both contact resistance and thermal radiation as a heat transfer forms. The model is shown below next to a clamshell shot of the CP3 structure showing the approximate heat transfer paths in red.

![External Temperatures Graph](image)

**Figure 53. Resistance Model Comparison**

**Figure 54. Battery Panel to Structure Heat Paths**
An equivalent resistance for this scenario would involve capacitors to simulate the battery thermal mass, resistors in parallel running through the aluminum holder, through contact with the PCB below and through radiation to both of the PCB boards it views (one from the aluminum holster, the other from the –Z of the power PCB). Next there would have to be additional serial resistors to carry the conducted and re-radiated heat into the structure of the satellite and on to an external panel from where it could be rejected or accepted depending on the temperature difference (or in electrical modeling the voltage difference). As modeling in this manner was not seen as accurate enough to warrant inclusion in the thesis beyond the simple modeling performed as an example in figure 52 and equation 21, this battery to exterior scenario was not modeled.

For reference the view factor between interior PCBs in the CubeSat form factor also included in Appendix I.

3.3.6 Finite Element Model Generation and Solutions

Finite element modeling can be extremely useful. It is one of the primary methods used by modern engineers for performing analysis on objects with multiple boundary conditions, various materials, and complex geometry. It provides engineers with a valuable tool to create a visual representation of their hardware’s condition. The computer aided engineering tools used for this section of the thesis are COMSOL and COSMOS.
COMSOL is a multi-physics simulator, originally built from Matlab libraries for complex physics simulations. COMSOL evolved into a graphical interface tool resting on those libraries that allows extremely complex and highly integrated simulations to be run. As an example, if you wanted to simulate the electrical resistance of a circuit while under heating loads and inside a magnetic field, COMSOL would be the tool to use. However, it has two major downfalls. First, creating and importing computer aided design (CAD) files is extremely prone to issues. Second, the ability to create physical geometries inside of COMSOL is limited and painstakingly slow. With these issues aside, for simple geometries but complex scenarios it is a powerful simulation tool.

The next software package utilized was COSMOS. COSMOS is a Dassault Systems product that is built into the SolidWorks PE software bundle. It is a widely used computer aided engineering tool for both mechanical and thermal analysis. Previous versions (pre-2006) did not incorporate radiative heat transfer but the 2007 version (used for this thesis work) did incorporate radiative heat transfer and the detailed engineering calculations to support it. COSMOS’ major advantage to other CAE packages is that it integrates very well with CAD packages (both SW and others). The usual issues of joint failures, angle mismatches or IGES/STEP corruption occurring is far less prevalent when using COSMOS. Therefore, for models with fairly simple thermal or mechanical processes, COSMOS is one of the best available tools.
The first step was to decide which portions of the satellite needed to be modeled and to what degree of accuracy they should be modeled. Thermal simulations in both COMSOL and COSMOS are very memory and processor heavy, taking on the order of hours to converge on a transient simulation of only minutes.

The first simulation run for the thesis incorporated the CP3 model shown below (excluded C&DH board). This model was readily available from a previous project done in SW and so it was a perfect test-case for practicing. The model was placed into a 264 K radiative “enclosure” (slightly higher than the predicted analysis in section 3.3.3) and the appropriate emissivity and absorptivity values were placed on both the aluminum structure and the payload boards. The payload C&DH board was given an internal heat flux of .28 W applied to the PCB directly. The battery board was given the remaining .72 W of heat flux. This is a gross simplification because the actual heat being generated is primarily on the C&DH board that would typically be modeled above the battery board. The battery board only accounts for about 6% of the total heat generated internally during discharge conditions (about .07 W) but since this was to be a training run for simulations the simplification was found to be acceptable.
Figure 55. CP3 Internals Exposed – SolidWorks Model w/o C&DH Board and Side-panels

The aluminum structure was giving the proper conductivity (128 Watts/mK) but no heat flux. Contacts between the panels and the structure were given a resistance of 3.80°C per Watt, consistent with 4-40 bolts. All boards were allowed to re-radiate to one another freely with IR emissivity and absorptivity values generated by the testing done in section 3.2.2 adjusted for the results of the study in 3.2.1 and 3.2.3. The model automatically generates independent view factors using the system geometry so the author was not required to develop those factors for the simulation.

The PCB conductivity for every FEA simulation was set to the value provided by Sierra Express for the ISOLA FR406 material with an adjustment for ground plane conductivity one ounce per foot or 1P on the CP payload and
C&DH boards and 2 ounces or 2P (4 ground planes total) on the EP&H board (Appendix B).

<table>
<thead>
<tr>
<th># 0F BURIED 1oz Cu PLANES</th>
<th>PCB EFFECTIVE THERMAL CONDUCTIVITY, W/m-K</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>x &amp; y</td>
</tr>
<tr>
<td>NONE (FR-4 ONLY)</td>
<td>0.87</td>
</tr>
<tr>
<td>ONE PLANE (1P)</td>
<td>9.4</td>
</tr>
<tr>
<td>TWO PLANES (2P)</td>
<td>18</td>
</tr>
</tbody>
</table>

Figure 56. Conductivity Across and Through an FR4 multilayer PCB

The effective thermal conductivity for standard FR4 PCB material is shown above along with optional copper sub-planes. While challenging to design against, adding just a one ounce copper ground plane to the board has a massive effect increasing the in-plane (X-Y) conductivity of the board by over 10 times. As would be expected, conductivity in the Z direction is only minimally affected by the addition. This one-ounce addition would correspond to a 1.4 millimeters layer of copper in the board. The figure below illustrates where these copper ground planes are typically located and how they interface with the PCB.
For reference a ground plane in PCB assembly is not something primarily designed to spread heat it is a layer of copper that appears to most signals as an infinite ground potential. This helps reduce noise and helps ensure that all integrated circuits within a system compare different signals' voltages to the same reference potential.²³
Figure 58: Magnetorquer traces in the CP side panels

No data was available to quantify the weight of the copper trace inside the solar panel magnetorquer coils (shown above in figure 58) so no adjustment was made for their thermal conductivity. From a qualitative standpoint they are definitely adding some amount of in-plane thermal conductivity and a future project to quantify the amount is suggested. Trying to model the actual ground traces inside the PCB (example shown below: Figure 59 NPS CubeSat) was decided to be outside the scope and requirement of this thesis as the 1P ground planes in the panels provided the majority of the thermal conductivity in the board and excluding the individual traces was conservative and would only serve to underpin the thesis results.
Figure 59. NPS CubeSat Payload PCB Board Showing Layers and Traces

After implementing the correct PCB conductivity for the payload and C&DH boards the model was set to run with a 33-minute “cold” case to simulate the eclipse period of the CP3 LEO orbit. The model ran for approximately three hours before COSMOS crashed. This gave the author his first lesson in thermal FEA modeling, simplify, simplify, simplify. After attempting to re-run the simulation twice with no success the author decided that further FEA analyses would use the simplest geometry possible that would still deliver accurate results.

The most important data points that the FEA was required to provide back were confirmation of a reduction in the thermal gradients across boards by mode (verification criteria 1.2). With the analysis performed in section 3.3.1 this could
be done accurately at the board level without requiring integrated and overly complex model geometry.

The first board to be analyzed and arguably the most critical was the CP C&DH board shown in the figure below. First a simplified version of the board was modeled based on the following assumptions and layouts and then all the necessary conditions and properties were placed into COSMOS for the simulation.

Figure 60. Layout for CP2 C&DH Subsystem PCB

Figure 60, above, shows the command and data handling (CDH) board for the CP satellite, which is where the communications subsystem is physically located. In the lower left corner are the footprints for the redundant RF 2117 amplifiers and in the middle left are the footprints for the redundant CC1000 devices. The
lower right corner shows the footprint of a single, PIC18LF6720. The second (redundant) processor is located on the opposite side of the board.\textsuperscript{20}

For the CP3 Comm Board a COSMOS finite element analysis was performed to analyze the board heat distribution, average board temperature and component temperature differences. The first analysis uses an external radiative cold case of -262 K to accurately simulate the interior view of the spacecraft boards but neglects board-to-board re-radiation heating effects.

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure61.png}
\caption{Cold Soak Temperatures for Normal Ops}
\end{figure}

During the normal ops simulation the board ran very cold at 265 K but was in-family with predicted temperatures. The RF amps and PIC 18 had 92\% of the heat load applied to them (62-30 respectively) with the remainder of the internal heat load placed on four components simulating AD627 modules. The RF amps
and PIC ran at approximately 274K with the center of the PIC getting up to 279K. The reason for the higher center temperature is that the material used to model the PIC has a much lower conductivity than both the board and the RF amps. Also the PIC was given twice the power that the normal PIC would use because the redundant PIC on the opposite side of the board were not modeled.

![Figure 62. Cold Soak Temperatures Operating in Mode 2a](image)

During the simulation for cold-soak with .ode 2a there was significant improvement. The board itself was running at 275K, an increase of 7K over the non-adjusted scheme. The components also ran at more reasonable operating temperatures with the RF amps reaching 312K and the PIC 18 reaching about 286K. While the RF amps did get quite high, they were still well within their normal operating temperatures. In all this reduced the total thermal cycle.
difference on the board between hot and cold cases from just over 28K down to about 18K. For the components, this decreased the thermal cycle even further from 38K to about 23K. While computer aided engineering programs make simplifications in geometry, interfaces and loads, these results were still very promising and gave further evidence that changes in the operating mode of components of CP satellites could lower their thermal gradients and orbital cycling.

Figure 63. Solar Panel Mesh Pre-Analysis
3.3.7 Finite Difference Models TRASYS/TSS-SINDA

Another method of CAE is through finite difference (FD) models. The majority of spacecraft thermal designers use one of two FD packages – TRASYS or TSS-SINDA. Both packages have extensive history in the aerospace engineering field. SINDA, which originally was named CINDA for Chrysler Improved Numerical Differencing Analyzer, originated in the 1960’s and has undergone evolutions and improvements for the past forty years. There are multiple versions but the one used for the analysis in this section was SINDA85. TRASYS, which stands for Thermal Radiation Analysis SYStem, takes a user-defined model geometry, surface optical properties, orbital parameters and boundary conditions and then outputs a format compliant with SINDA input. TSS stands for Thermal Synthesizer System and relative to TRASYS and SINDA is a newer tool. It generates similar output to TRASYS but can also use ray-tracing techniques to allow for systems with reflective surfaces to be more accurately modeled. Since TRASYS was not available at the time analysis was performed the only FD model generated was done using TSS and analyzed via SINDA.

The figures in Appendix J show the orbital parameter insertion, material property selections and heating values. The .dat file results for each mode were then individually processed in the SINDA processor. The SINDA output was in the form of a single-node temperature representing the satellites average temperature stepped across the orbit. This was done with over >100 orbits to ensure no significant increase or decrease in the orbital heating would occur post
“launch” of the DNEPR 4/5 mission. Several assumptions were made in the model; the most significant giving the satellite an average \( C_P \) that represented a mixture of aluminum and FR4 (30-70). The satellite was modeled as a sphere with one node and a mass of 1 kilogram. The initial temperature of the node was set to 70 degrees Fahrenheit. A sample of the orbital set, post settling was then selected and plotted. This was done primarily to provide another method of confirming the thesis that the temperature of the satellite could be increased on-orbit with only the small amount of energy input available to its electrical power system. A representative single orbit selection for the output temperature results in each mode tested is shown in Appendix J with the summary of all the modes in table 18 below.

<table>
<thead>
<tr>
<th>Table 18. SINDA/TSS Modes 1, 2a, 2b, 3, 4a, 4b, 4c, 5</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mode 1</td>
</tr>
<tr>
<td>Min(F)</td>
</tr>
<tr>
<td>Min (C)</td>
</tr>
</tbody>
</table>

The SINDA results were in-line with the analytical results albeit with slightly lower overall system temperature predictions. These predictions were found to be a more accurate representation of the true on-orbit system. The reason for the accuracy of the SINDA results versus the analytical results is that SINDA accounts for precise albedo loading, Earthshine and shadowing (penumbra) parameters while the analytical calculations simply use worst-case conditions.
3.4 Active Thermal Vacuum Testing

Thermal vacuum testing began with the creation of standard test plan, using the TVAC SOP as a starting point. Once the testing documentation was written and a representative test profile was generated (discussed below) the first active step in thermal vacuum testing was de-integrating the satellite. This process took advantage of the clamshell structure design on CP3 to place individual thermistors inside the satellite (figure 64). These thermistors allowed for both the person at the T-VAC and the person sending commands in the lab on Marconi or Hertz to see the systems active temperature. It provided a safeguard so that during extreme testing conditions the temperatures inside the satellite could be monitored locally so that a damaging condition would not occur.
Then using kapton approved low-outgassing tape, individual thermistors were placed into CP3 in the critical locations including the battery (1), C&DH board (2), payload face (1) and another on the aluminum side-rail (1). These thermistors were bundled and can be seen in figure 65 below protruding from the test article inside its carrying case.
To perform thermal vacuum testing the T-VAC SOP was used for each start-up procedure. Testing is performed in building 58 (Welding Building) on campus but commanding uses the ground station in Building 007 (Advanced Technology Labs - ATL). One person had to commute back to the ground station every time a command was required for testing. Luckily, other personnel were often already in the lab doing other work and would be used to send commands. The radio to the left of the Labview T-VAC PC (figure 66) was used to communicate between the lab sending the commands and the T-VAC area so the command timing could be noted in the test procedures.
Prior to testing the ends of the antennae were covered in kapton tape to ensure they did not ground themselves when placed inside the conductive copper shroud. While hard to see in figure 66, the end of the tape-measure antennae has been carefully enclosed in kapton tape.
Once the satellite had thermistors integrated and had been completely re-assembled, personnel for the test were coordinated to be present at both the lab and the T-VAC for the duration of the testing. The satellite was removed from its carrying case once inside the antechamber at the Thermal Vacuum facility. Once removed it was checked to ensure none of the visible thermistors had settled or been moved. The antennae, which had been taped down prior to being placed into the carrying case, were released (ends still taped). Finally, before placing the satellite into the chamber its Remove Before Flight (RBF) pin was removed and then the satellite was placed into the chamber onto Teflon stand-offs.

![Figure 68. Preparing the Satellite for Testing; Thermistor Bundle & Antennae Release](image)

After placing the satellite inside the chamber, the normal T-VAC standard operating procedure (SOP) was performed, checking the ring-seal on the T-VAC door, closing the door and checking that the vent valve was closed and finally
Exiting the ante-room. Once inside the secure chamber the now beaconing satellite was checked for connectivity with the ground-station. One of the test personnel exited the T-VAC area and went to the ground station (shown in figure 69), sent a test command and confirmed that they received and acknowledged or ACK back from the satellite. This was important to do before the diffusion pump was engaged and liquid nitrogen was being used. By checking prior to closeout it allowed for adjustments to be made if the link would not close or if there was a grounding issue on the satellite’s antennae. In this manner it could be fixed quickly and without a long penalty “cool-down” time on the vacuum chamber.

Figure 69. Marconi and Hertz, PolySat Groundstation Computers, Antennae Controllers and Radios

Once the RF link was verified to be working satisfactorily the chamber was powered on, the roughing pump was engaged, and the process of lowering the pressure inside the chamber began. Once the roughing pump was at an acceptable level, typically around 70mTorr, the diffusion pump was engaged and allowed to warm up. Typically this takes around 50 minutes to an hour to complete. Once warmed up with low enough (50-100mTorr) pressure on the
backing pump lines, the roughing valve was opened, allowing the chamber to begin pumping down. Around the same time the liquid nitrogen dewar was opened and the lines feeding the cold trap were allowed to fill by engaging the solenoid. Next the roughing valve would be closed allowing the backing pump section to pump down and prepare. Once the cold trap was noticeable iced up the head gate was opened and the chamber began to pull high vacuum. Once the pressure could no longer be read on the standard pressure gauge the ion gauge was powered up for the remainder of the test. As soon as the ion gauge read $10^{-4}$ Torr the test could begin. Since the majority of the tests being performed for this thesis were “cold-soaks” the chamber often reached low $10^{-5}$ Torr due to the cold temperature causing molecules to lose energy and fall to the bottom of the chamber where they would be pulled out.

Once testing began, the following model was used to simulate the on-orbit environment. The chamber typically runs around 20°C prior to testing during the hour and a half pre-test. As high vacuum is achieved (T=0) the Omega temperature controller was set to progressively lower temperatures over a fifteen minute period with the final cold temperature setting being -60°C. This is close to (but not outside) the safe limits of the chamber operations and progression steps were monitored closely so that the system would not have damaging thermal strain effects. This is shown in figure 70 below, Cold Bias Testing Profile.
Figure 70. Cold-Bias Testing Profile

This testing profile actually uses a longer setting at the “cold” station (45-66) than the satellite would normally experience on orbit (33-66). The primary reason for biasing the test parameters in this way is for conservatism. The thermal vacuum chamber testing performed is the most accurate simulation of the thesis available on the ground, significantly outweighing both analysis and CAE programs in response accuracy. However the actual transient radiative environment of space is extremely hard to mimic in a confined chamber. Unlike on orbit, inside the thermal vacuum the transition from hot to cold and vice-versa is not immediate, limiting the ability to “shock” the system. This reality limits the ability of a 33-66 setting to accurately get the satellite as cold or hot as it would on orbit. The testing keys espoused by NASA GEVS guidelines are to bias the conditions towards the scenario that is in question. Thus to add conservatism
towards the scenario in question and more accurately mimic the on-orbit conditions every test was biased cold. The chamber itself is biased warm as it has a large thermal mass exposed to an indoor/outdoor environment anywhere from 21-24°C. While the chamber’s mass is insulated as best as possible from conduction and radiation to the outside environment it still provides a noticeable damping effect on the testing performed.

Another item that must be taken into consideration prior to testing is overshoot. Thermistors cannot be placed immediately on the heaters in question for EMI reasons as the large heater currents cause excessive noise and also due to material reasons as the heaters themselves get hot enough to melt the thermistors cabling. In the same way, the thermistor cannot be placed directly upon the liquid nitrogen piping because it would read a deceptively low temperature for either heating or cooling conditions. The Omega controller used to enable or disable heaters and the liquid nitrogen solenoid valve is reliant upon these thermistors that do not have an immediate reaction to the heating or cooling being performed. They have a ‘lag’ effect that can often catch new (and veteran) thermal vacuum test personnel off-guard as they ramp high or low and suddenly see that despite uncoupling the controller, the measured temperature in question continues to rise or fall. To ensure safety of the hardware and the test personnel the following inertia effects curve in figure 71 was generated based on the set points of the cold-bias testing profile in figure 71. Based on the expected lag and the known cutoff times the hardware should be safe throughout the test,
going only slightly below -60°C and slightly above +55°C, and the actual environment it sees is more accurately represented by the curves in the inertia effects figure. There was expectation, based on experience, that the hot portion of the testing would not actually reach the set limits in the fifteen minutes of ramp up time (safely). Since this was expected the ramp was lowered to a shallow gradient during the initial 45 minutes of hot “soak” slowly ramping from 40°C to 55°C. This still met the intent of cold-biasing the test as the only outcome of this ramp being an un-realistic transient condition on orbit would be lower thermal influx during the “hot” on-orbit transient condition and an overall lower system temperature as a consequence.

![Expected Temperature w/ Inertia](image)

**Figure 71. Expected Environment (Thermal Shroud) Temperature with Inertia Effects**

Using the cold-biased thermal profile, each test profile was standardized to provide for four cold-ramps and three hot-ramps with one final “warm” ramp to return to a safe ambient temperature. While only representative of slightly more
than three orbits, this was found to be a reasonable test plan. This put the entire testing time, including lab set-up, at an estimated 11 hours with no complications. Since this test needed to be performed multiple times, with limited help, on a limited schedule and with limited liquid nitrogen costs, this profile initially met the need. After the first two tests this profile was found to actually require 12 hours and was adjusted for the remaining two tests (4a, b, and c were combined into one test). First a baseline test with the satellite performing nominal operations was executed with the resulting output below. The key items to note are the severe thermal lag between the expected “environment” and the response of the satellite. Partly due to the thermal mass of the chamber itself and also due to the limitations inherent in heating and cooling a thermal shroud as quickly as possible. Still the simulation, when compared to the SINDA/TSS output, was found to be very comparable.
Figure 72. Standard Operations “Mode 1” Cycle Temperatures

This test was meant to establish the baseline four-cycle test and with which to confirm or verify the success criteria. While the initial baseline did not line up perfectly with the expected cold bias testing profile it was still a good calibration run and provided several lessons learned that would have to be used in subsequent tests.

The first lesson learned was that since the controller can not have both the solenoid and the heater circuit attached at same time there was at least a four to five minute lag between stopping the “ramp” temperature and initiating the next opposing “ramp.” Moving to a hot ramp the temperature would be raised above the current cold controller temperature, the operator would note the time quickly, carefully walk along the side-scaffolding around the chamber, remove the solenoid liquid nitrogen electrical NEMA 5-20R connector and after checking, install the heater circuit NEMA 5-20R connector. These connections can be seen
below in figure 73 where the white cabling (disconnected) supplies the heater current and the black cabling (connected) powers the solenoid valve. Also shown below is the location of the solenoid valve and the liquid nitrogen piping entering the chamber.

Figure 73. Omega Controller with National Instruments DAQ

Figure 74 Solenoid Valve & Liquid Nitrogen Dewar during Testing

Then the operator would walk back around the chamber and begin to
increase the temperature, taking note of the ramp settings and associated times. For every cold ramp the operator would slow the ramp temperature beginning around 50°C ensuring that the heaters wouldn't peak beyond the test profile and then perform a similar process as was done for the hot ramp but instead removing the heater circuit and re-installing the solenoid circuit. This actually caused some noticeable leveling at the hot test point and was considered for future tests to be overly conservative as the heater overshoot was still within safe ranges. All of the ramp procedures were done while carefully noting the times and the ramp settings for future tests.

The author had not initially expected, nor accounted for, the added time delay between hot and cold settings. Since the four to five minute changeover was within the additional cold bias timeline the author evaluated it and found it to be negligible. Thus it did not force a redesign of the test criteria or a re-test of mode 1. Also during this test, the first ramp performed had two points where the ramp was slowed due to safety concerns. As the final cold temperature still reflected well against the other cycles and the first cycle was not found to be the minimum condition this inconsistency was considered negligible as well. All subsequent tests performed, except where specifically noted, used ramp times that endeavored to follow this initial baseline without the two point excursion as precisely as possible and thus mimic the exact same heating conditions. Using precise ramp settings allows for a better mode to mode comparison as changes in the temperature curves are attributable to the mode itself instead of being due
to a change in ramp values or timing issues.

With this reference test established, post vacuum chamber shutdown, the next step was to check the satellite post-testing for any mechanical issues, contamination or otherwise. Once completed the operator and support personnel re-taped the satellite’s antennae and placed it back into its container. Also, per the T-VAC standard operating procedure, the chamber was inspected for any mechanical or contamination issues post-test. This closeout process was repeated for each test performed and the satellite examination by the operators is shown in figure 75.

![Figure 75. Post Test Inspection & Antennae Tape-Down](image)

The next test to be performed was the mode 2a increased beacon rate. The same procedures discussed for mode 1 thermal vacuum chamber testing were used for this test and the same ramp profile was used with the exception of
the heater-safety issue mentioned above. By decreasing the overshoot expectations on the hot side the test was able to more accurately mimic on-orbit temperature swings and the thermal shock that the spacecraft is actually being exposed to. It also allowed for a shorter duration at the peak heating condition, also adding to the accuracy of the test environment. The cold cycle ramps were retained.

![Graph](image1.png)

**Figure 76. Mode 2a Increased Beacon Rate 90s**

With the increased beacon rate at 90s test completed, the next test of mode 2b was prepared and executed. This test was coordinated beforehand to ensure that the 60s beacon rate would not interfere with any other tests or operations being performed.

During the testing for mode 2b, on the second cold ramp cycle the timer being used was not properly set for the short increments and the cold ramp was extended, causing a slower ramp gradient but with little effect on the overall cold temperature. This excursion caused the entire mode 2b test to run thirty minutes
longer than the mode 2a and mode 1 tests. By extending the test, the next two ramps were done appropriately with the correct ramp gradients and timing. Overall it had no noticeable effect on the minimum temperature experienced during the test, just on the total test duration. This mode was found, post-testing, to be a noticeable improvement over the baseline (results in table 19).

Figure 77. Mode 2b Increased Beacon Rate 60s

The mode 3 payload enabled test is shown below. For reference, this mode was calculated to use 1.779W and was expected to have a slightly improved temperature versus the baseline and both modes previously tested. This test was started late due to other work being performed at the time. Initially the resistive heater circuit was connected, causing some initial heating and a slower first cold ramp. Also coordination challenges between the ground station and the chamber location occurred during the second cold ramp. The assisting operator was unsure of the ramp time intervals, causing some delay.
In addition to challenges in ramp temperatures and the late start a conflict with other schoolwork at the time came up and the assistant supporting the test had to leave earlier than planned. For safety purposes thermal vacuum testing cannot be done with only one operator and so the author had to cut the test short by about ninety minutes or one full cycle. As the amount of increased heating in mode 3 was calculated to be only one tenth of a watt greater than the mode 2b this decision was justified and the data collected during the test executed was considered to be acceptable to measure this mode. The results in table 19 show that this mode, while an improvement over the mode 1 baseline, was not in-line with the expected heating.

![Graph](image)

**Figure 78. Mode 3 Payload Enabled Test**

Mode 3 actually had less overall effect on the key temperature parameters than did Mode 2b. This was postulated to be due to the actual location of the heating in question being on the payload boards and only a slight increase in the battery
heat generated due to $I^2R$ losses from that additional payload power. There was noticeable improvement over the baseline mode 1 but still not enough to qualify this as an effective mode for heating the satellite. Since the payload current draw is highly irregular, it may be that the actual duty cycle is much longer than the predicted duty cycle thus causing the total heat generation to be lower. As the baseline being used was from testing by the two electrical team leads for CP3 the power draw had to be considered an accurate estimation, but improvements in the total current draw and duty cycle measurements would definitively explain why this mode appeared to be placing less heat into the system than other “lower” power modes. The uncertainties caused by the shortened and altered testing parameters also cannot be overlooked as a potential source of error in the outcome of this test. For the reasons mentioned above (minimal increased heating load over mode 2b and questionable duty cycle predictions) this mode was not re-tested.

To test mode 4 accurately a few initial assumptions had to be made. The high current required to implement ADC modes can easily brown-out the satellite. Since this would cause serious issues for both testing and true utility of the mode on orbit the commanding was adjusted to a reasonable 50% of each mux’s duty cycle, beginning with torquers on approximately ten minutes into the cold ramp and off approximately ten minutes before the hot ramp began. This again adjusted the total usage to a level consistent with the estimated power usage for ADC and in-line with the analyses done previously via other methods. Also mode
4c was not tested due to concerns with browning-out or over-stressing the test-article. By comparing the T-VAC results for modes 4a and 4b with the analytical method results for correlation it was assumed that a correlation for Mode 4c could also be derived without putting hardware at risk. So modes 4a and 4b were tested with mode 4a being tested during the first and second cycles only. Then a return to mode 1 was allowed during the third cycle. Finally, mode 4b was tested during the fourth and fifth cycle. Mode 1 was used during the third cycle for two reasons. First it was intended to “reset” the temperature condition of the satellite so that two modes, 4a and 4b, could be examined separately in one thermal vacuum test. Second it was intended to display the effect of multiple modes in one test while inspecting for any thermal inertia effects visible in the system.

Figure 79. Combined Mode 4a, 4b and Mode 1 Insert Test
During the hot ramp leading to the second cold cycle approximately four hours into active testing the heaters were ramped higher than the previously noted ramp profile called for to test the thermal inertia of the system during the “mode 1” section of the test. This caused spiking on the external side temperatures and some minor spiking on the internal temperatures but not as great of an effect as was expected. As the third cycle was set as a “mode 1” cycle this hot spike did not directly affect the other cycles, excepting a slight rise in the temperature on the RF amp and Comm power. The results in table 19 for modes 4a and 4b show strong gradient reductions, well above the required verification criterion. As can be seen in figure 79, above, the internal temperatures benefitted significantly from having both the ADC system engaged along with an increased beacon rate. With just the ADC alone engaged (mode 4a) the battery temperature delta from hot to cold was reduced by 32.5%. The side panels also saw significant heating, reducing their exterior and interior delta temperatures by 35.4% and 31.7% respectively. With both the ADC and beacon rate increase (mode 4b) the battery temperature delta was reduced by 38.1% and the RF amp temperature delta saw a 29.4% decrease. The increased beacon rate made almost no impact on the exterior and interior side panel temperatures with decreases of .4% and .7% respectively. This is probably due to the increased heating from RF transmission being on an interior board with low view factor and a fairly low overall increase in the heat being generated.
The next mode tested was the increased “data drop.” This mode utilizes an increase in the contacts (ACK) with the satellite and increasing the regularity of “data dumps” which cause a large amount of processing and communication subsystem heating. The thermal vacuum test used the duty cycle established for section 3.3.4 for consistency. Figure 80 shows that mode 5 displayed improvements in several key parameters.

![Figure 80. Mode 5 Increased Data Drop (Contact) Test](image)

The increased contact mode 5 had only minimal effect (10.3%) on the external temperatures, slightly better than mode 2b (9.7%). The increase on the internal temperatures for key components was more significant. RFA temperature deltas were reduced by 15.4%, battery deltas by 14.1%, C&DH temperatures by 14.5% and Comm power deltas by 15.7%. The RF and Comm decreases met the necessary verification criteria and the C&DH and battery temps were close enough that, taking into consideration the cold-biasing of the
test, these too met the intent of the verification criteria.

The summary of all the testing temperature minimums, by mode, is presented below in Table 19. This compares the reduction to the temperature delta for the baseline on that component or surface using +20°C as the “hot” condition temperature. Overall the modes tested compared favorably to the baseline condition and the cold condition minima’s were increased for every mode tested.

Table 19. Thermal Vacuum Chamber Test Results

<table>
<thead>
<tr>
<th>MINIMUM</th>
<th>AVG SIDE PANEL EXT</th>
<th>AVG SIDE PANEL INT</th>
<th>RFA TEMP</th>
<th>BATT TEMP</th>
<th>CDH Temp</th>
<th>COMM PWR TEMP</th>
</tr>
</thead>
<tbody>
<tr>
<td>BASELINE</td>
<td>-36.7</td>
<td>-32.4</td>
<td>-21.5</td>
<td>-19.1</td>
<td>-19.2</td>
<td>-19.4</td>
</tr>
<tr>
<td>% Difference</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>90s</td>
<td>-34.02</td>
<td>-30</td>
<td>-19.05</td>
<td>-16.6</td>
<td>-16.8</td>
<td>-16.3</td>
</tr>
<tr>
<td>% Difference</td>
<td>4.7%</td>
<td>4.6%</td>
<td>5.9%</td>
<td>6.4%</td>
<td>6.1%</td>
<td>7.9%</td>
</tr>
<tr>
<td>60s</td>
<td>-31.2</td>
<td>-28.49</td>
<td>-17.7</td>
<td>-15.7</td>
<td>-13.7</td>
<td>-14.5</td>
</tr>
<tr>
<td>% Difference</td>
<td>9.7%</td>
<td>7.5%</td>
<td>9.2%</td>
<td>8.7%</td>
<td>14.0%</td>
<td>12.4%</td>
</tr>
<tr>
<td>PAY ENABLED</td>
<td>-34.482</td>
<td>-30.0708</td>
<td>-19.35</td>
<td>-16.72</td>
<td>-14.45</td>
<td>-15.54</td>
</tr>
<tr>
<td>% Difference</td>
<td>3.9%</td>
<td>4.4%</td>
<td>5.2%</td>
<td>6.1%</td>
<td>12.1%</td>
<td>9.8%</td>
</tr>
<tr>
<td>ADC ON</td>
<td>-16.6</td>
<td>-15.8</td>
<td>-13.2</td>
<td>-6.4</td>
<td>-8.85</td>
<td>-12.2</td>
</tr>
<tr>
<td>% Difference</td>
<td>35.4%</td>
<td>31.7%</td>
<td>20.0%</td>
<td>32.5%</td>
<td>26.4%</td>
<td>18.3%</td>
</tr>
<tr>
<td>ADC ON + 90s</td>
<td>-16.4</td>
<td>-15.4</td>
<td>-9.3</td>
<td>-4.2</td>
<td>-5.7</td>
<td>-5.6</td>
</tr>
<tr>
<td>% Difference</td>
<td>35.8%</td>
<td>32.4%</td>
<td>29.4%</td>
<td>38.1%</td>
<td>34.4%</td>
<td>35.0%</td>
</tr>
<tr>
<td>% Difference</td>
<td>10.3%</td>
<td>9.2%</td>
<td>15.4%</td>
<td>14.1%</td>
<td>14.5%</td>
<td>15.7%</td>
</tr>
</tbody>
</table>

The summary presented in table 19 uses minimums over all cycles for each mode tested. The realized improvements were readily apparent in this table and it can be seen that three of the five modes tested (3, 4a, 4b) meet or exceed the verification criteria. Modes 2a and 2b similarly make non-trivial improvements, especially in reducing the battery temperature total delta by 8.7% and 6.4% respectively. The increase of 8.7% on the battery delta by mode 2b corresponds to an increase of around 115 mAh or 6.9% on the total discharge capacity of the CGA103450A batteries. The 38.1% delta decrease from mode 4b corresponds
to an increase of around 235 mAh or 14.2% of the total discharge capacity of the CGA103450A batteries.

As can be seen from the graphs and results, while there were challenges in maintaining the timeline, the heat ramps were accurately retained so that steps and the associated system temperature responses could be evaluated with each test’s results and easily compared to one another.

Further discussion of these results and comparisons to the analytical and thermal models are shown in section 4.1, Review of Results Against Verification Criteria.

3.5 On-Orbit Commanding of Modes 1, 2a and 5

During the late stages of this thesis, on April 17th 2007, the first successful DNEPR CubeSat launch took place. The author was privileged to be one of the operators during the initial two months of on orbit operations. It was during this time that the first anomaly resolutions occurred and a few limited opportunities arose for on-orbit testing of this thesis. Since communication with the satellites was restricted and challenges with contacting CP3 were numerous, the amount of testing performed was constrained to limited tests of only mode 2a and mode 5. These modes could be tested on CP2 rather early because they complied with the team’s CONOPs at the time, and CP3 only later once a stable command link had been established. The graphs below in figure 83 show a snapshot of the on-orbit thermal response of the CP3 side panels after one month on orbit.
Figure 81. External On-Orbit Temperatures 1, CP3

Figure 82. External On-Orbit Temperatures 2, CP-3
Using the SRI dish to overcome receive sensitivity issues on May 24th 2007, two successful sensor dump operations were performed on CP2, raising the internal temperatures on the C&DH temp by approximately 5°C on the sunlit side of the orbit and 2°C on the umbra side while also raising the RFA temp 2°C
on the umbra side of the orbit. Also, the beacon rate was set to 120 seconds
due to the 300 seconds that it was initially at. This was the first indication that data
dump (mode 5) could be useful on orbit. CP3 still could not be reached, even
using the SRI 20m dish, and the operations team postulated this was due to the
extremely low amount of power being utilized on CP3 versus CP2 making the
receive sensitivity on CP3 even lower than that of CP2 because it had no thermal
damping effect from internal heat generation. Contacting CP2 became easier
after this adjustment was made, both because more regular beacons were easier
to track manually and also because the additional on-board heating being
generated.

On June 11th 2007, CP2 was tested in mode 2b. The two pass data
responses showed an increase of 3°C in the RF temperature and an increase of
4°C on the Comm processor. In addition communication acknowledgement from
the satellite on the subsequent passes was improved, despite being lower on the
horizon and thus further from the ground station. The RSSI improved from 53 to
4F corresponding to a 4 point decrease (83 to 79 – 95dbM). This was in line with
the results from thermal vacuum testing.

On July 14th 2007, CP3 underwent partial testing of mode 5 with several
successful downlinks and two data dumps completed. As expected, the internal
temperature of the satellite rose. However, the two completed passes were
daytime passes and as such were successful only in increasing the rise of the
internal temperatures and had almost no effect on the cold pass. As a
comparison illustrating the difference in internal temperatures for both satellites once in “normal ops” the graph below (figure 85) shows four CP2 orbits on the left and four CP3 orbits on the right. It should be noted that the CP2 internal temperatures are consistently 3-7°C higher during the umbra than the equivalent CP3 temperature. This is due to the increased heat generation from both increased beacon activity and increased processing activity on CP2. The sunlit portions compare almost evenly between the two satellites with CP2 having significantly more instability on the external panels but very similar temperature profiles internally.

Figure 85. CP2 vs CP3 On-Orbit Temperature Comparison
4.0 Thesis Results and Conclusions

4.1 Review of Results Against Verification Criteria

The results obtained from the analytical method, two unique modeling methods, thermal vacuum testing and finally the limited on-orbit testing are summarized in this section. The conclusion reached after all of these analyses, modeling and testing were completed, is that a 1-U CubeSat can improve its thermal condition utilizing existing hardware and software. A table summarizing the verification criteria established in section 2.3.3 is shown below for reference (table 20).

<table>
<thead>
<tr>
<th>Criteria</th>
<th>Metrics or Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>Criteria 1.1 (Primary) 1 of 2</td>
<td>1.1. A 15% reduction in spacecraft subsystem component temperature variability across maximum and minimum orbital heat flux conditions.</td>
</tr>
<tr>
<td>Criteria 1.2 (option)</td>
<td>1.2. A 10% reduction in in-plane thermal gradients on the subsystem printed circuit boards (PCBs) and an associated (equivalent) reduction in heat-flux across the subsystem PCBs.</td>
</tr>
<tr>
<td>Criteria 2.1 (Secondary) 1 of 4</td>
<td>2.1. Increased on-orbit component lifespans (potential, via analysis)</td>
</tr>
<tr>
<td>Criteria 2.2 (option)</td>
<td>2.2. Improved RF or Electrical Power and Handling (EP&amp;H) subsystem performance (stability) and/or reliability</td>
</tr>
<tr>
<td>Criteria 2.3 (option)</td>
<td>2.3. Improved Command and Data Handling (C&amp;DH) or Inter-Integrated Circuit I2C reliability.</td>
</tr>
<tr>
<td>Criteria 2.4 (option)</td>
<td>2.4. Enhanced payload performance (Realized or Potential)</td>
</tr>
<tr>
<td>Criteria 3.1 (Constraint) 2 of 2</td>
<td>3.1 EP&amp;H subsystem values (BATTA and BATTB) must not have overcurrent or undercurrent faults</td>
</tr>
<tr>
<td>Criteria 3.2</td>
<td>3.2 EP&amp;H subsystem depth of discharge (DoD) must not exceed recommended manufacturer limits</td>
</tr>
</tbody>
</table>

The first method used to generate verification evidence was analytical calculations using the thermal balance equation for a CubeSat. The initial analytical results indicated that the satellite could make significant reductions in its onboard temperature swings and specifically modes 2b, 3, 4a, 4b, 4c and 5 had the capability to meet the verification criterion 1.1.
The next step was using the SEET modeling tool based in STK to model each scenario. The SEET tool generated large deltas, in family with the analytical method, but was still able to accurately model the resulting improvement from each mode. Using the SEET function modes 4a, 4b, 4c and 5 were all found to meet the verification Criterion 1.1 15% reduction. Mode 3 was close to but not quite able to meet the criteria in this analysis. While the total reduction may not have met the criterion for the first three modes, both the SEET analysis and the SINDA/TSS analysis (table 22 below) assumes a single node for the satellite temperature. This means that those modes could still be proven via other methods to be effective at reducing the key temperature parameters, but for a lumped mass analysis via these methods they could not.

Table 21. Criteria 1.1 Verification Evidence; Percent Reduction Max to Min

<table>
<thead>
<tr>
<th>Verification Criteria 1.1 - 15% Orbital Max-Min Reduction</th>
<th>Solved for T Kelvin</th>
<th>(Celsius)</th>
<th>% Reduction</th>
</tr>
</thead>
<tbody>
<tr>
<td>CASE 2b - Cold w/ Adjustments</td>
<td>241</td>
<td>-32</td>
<td></td>
</tr>
<tr>
<td>CASE 5 - Hot w/ Adjustments</td>
<td>346</td>
<td>73</td>
<td></td>
</tr>
<tr>
<td>CASE 3 - Mode 1 (Standard Operations)</td>
<td>241</td>
<td>-32</td>
<td>0.0%</td>
</tr>
<tr>
<td>CASE 3 - Mode 2a. (Inc. Beacon Rate - 90s)</td>
<td>253</td>
<td>-20</td>
<td>11.3%</td>
</tr>
<tr>
<td>CASE 3 - Mode 2b. (Inc. Beacon Rate - 60s)</td>
<td>258</td>
<td>-15</td>
<td>15.6%</td>
</tr>
<tr>
<td>CASE 3 - Mode 3. (Payload Enabled - ON)</td>
<td>258</td>
<td>-15</td>
<td>16.2%</td>
</tr>
<tr>
<td>CASE 3 - Mode 4a. (ADC ON)</td>
<td>287</td>
<td>14</td>
<td>43.4%</td>
</tr>
<tr>
<td>CASE 3 - Mode 4b. (ADC ON &amp; Beacon Inc. - 90s)</td>
<td>289</td>
<td>16</td>
<td>45.0%</td>
</tr>
<tr>
<td>CASE 3 - Mode 4c. (ADC ON &amp; Beacon Inc. - 60s)</td>
<td>292</td>
<td>19</td>
<td>47.9%</td>
</tr>
<tr>
<td>CASE 3 - Mode 5. (Increased &quot;Data Drop&quot;)</td>
<td>273</td>
<td>0</td>
<td>30.5%</td>
</tr>
</tbody>
</table>

Table 22. SINDA Output Results Summary by Mode

<table>
<thead>
<tr>
<th>Mode</th>
<th>Mode 1</th>
<th>Mode 2a</th>
<th>Mode 2b</th>
<th>Mode 3</th>
<th>Mode 4a</th>
<th>Mode 4b</th>
<th>Mode 4c</th>
<th>Mode 5</th>
</tr>
</thead>
<tbody>
<tr>
<td>Min (F)</td>
<td>1.2</td>
<td>3.2</td>
<td>5.7</td>
<td>6.3</td>
<td>24.1</td>
<td>26.2</td>
<td>27.8</td>
<td>15.6</td>
</tr>
<tr>
<td>Min (C)</td>
<td>-17.1</td>
<td>-16.0</td>
<td>-14.6</td>
<td>-14.3</td>
<td>-4.4</td>
<td>-3.2</td>
<td>-2.3</td>
<td>-9.1</td>
</tr>
<tr>
<td>% Reduction</td>
<td>0.0%</td>
<td>3.0%</td>
<td>6.7%</td>
<td>7.6%</td>
<td>34.3%</td>
<td>37.4%</td>
<td>39.8%</td>
<td>21.6%</td>
</tr>
</tbody>
</table>
The satellite and orbit were modeled in TSS and solved in SINDA with the different heating modes more accurately portrayed as step functions (SEET was unable to model step functions). The results showed that modes 4a, 4b, 4c and 5 were all predicted to meet the verification criterion 1.1 reduction of 15% in on-orbit temperature differences.

The next method of reviewing the thesis against the verification criteria was via thermal vacuum testing. Table 23 summarizes the results of the thermal vacuum chamber tests. Modes 4a, 4b and 5 were all able to meet the verification criteria (mode 5 for key parameters only). Mode 2b was also found to have significant improvement in both RFA temperatures and C&DH temperatures. With the inherent conservatism of the test there is reason enough to justify considering mode 2b to have met the criteria for those key parameters.

<table>
<thead>
<tr>
<th>MINIMUM</th>
<th>AVG SIDE PANEL EXT</th>
<th>AVG SIDE PANEL INT</th>
<th>RFA TEMP</th>
<th>BATT TEMP</th>
<th>CDH Temp</th>
<th>COMM PWR TEMP</th>
</tr>
</thead>
<tbody>
<tr>
<td>BASELINE</td>
<td>-36.7</td>
<td>-32.4</td>
<td>-21.5</td>
<td>-19.1</td>
<td>-19.2</td>
<td>-19.4</td>
</tr>
<tr>
<td>% Difference</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>90s</td>
<td>-34.02</td>
<td>-30</td>
<td>-19.05</td>
<td>-16.6</td>
<td>-16.8</td>
<td>-16.3</td>
</tr>
<tr>
<td>% Difference</td>
<td>4.7%</td>
<td>4.6%</td>
<td>5.9%</td>
<td>6.4%</td>
<td>6.1%</td>
<td>7.9%</td>
</tr>
<tr>
<td>60s</td>
<td>-31.2</td>
<td>-28.49</td>
<td>-17.7</td>
<td>-15.7</td>
<td>-13.7</td>
<td>-14.5</td>
</tr>
<tr>
<td>% Difference</td>
<td>9.7%</td>
<td>7.5%</td>
<td>9.2%</td>
<td>8.7%</td>
<td>14.0%</td>
<td>12.4%</td>
</tr>
<tr>
<td>PAY ENABLED</td>
<td>-34.482</td>
<td>-30.0708</td>
<td>-19.35</td>
<td>-16.72</td>
<td>-14.45</td>
<td>-15.54</td>
</tr>
<tr>
<td>% Difference</td>
<td>3.9%</td>
<td>4.4%</td>
<td>5.2%</td>
<td>6.1%</td>
<td>12.1%</td>
<td>9.8%</td>
</tr>
<tr>
<td>ADC ON</td>
<td>-16.6</td>
<td>-15.8</td>
<td>-13.2</td>
<td>-6.4</td>
<td>8.85</td>
<td>12.2</td>
</tr>
<tr>
<td>% Difference</td>
<td>35.4%</td>
<td>31.7%</td>
<td>20.0%</td>
<td>32.5%</td>
<td>26.4%</td>
<td>18.3%</td>
</tr>
<tr>
<td>ADC ON + 90s</td>
<td>-16.4</td>
<td>-15.4</td>
<td>-9.3</td>
<td>-4.2</td>
<td>5.7</td>
<td>5.6</td>
</tr>
<tr>
<td>% Difference</td>
<td>35.8%</td>
<td>32.4%</td>
<td>29.4%</td>
<td>38.1%</td>
<td>34.4%</td>
<td>35.0%</td>
</tr>
<tr>
<td>% Difference</td>
<td>10.3%</td>
<td>9.2%</td>
<td>15.4%</td>
<td>14.1%</td>
<td>14.5%</td>
<td>15.7%</td>
</tr>
</tbody>
</table>

The primary criterion 1 was met through the testing previously discussed. Now using the analyses and test data obtained, the remainder of the criteria could be verified.
Using the data obtained from the COSMOS simulation on in-plane temperatures and heating for each mode, it was found that the C&DH board, specifically the PIC18 processor, had a 21% lower gradient between itself and the PCB substrate. When the hypothesis first took form it was developed to allow success through multiple paths since “improvement” was a difficult parameter to quantify and the testing to be performed was not yet established. Since only one of the two primary constraints was required, the results shown above and the additional data from section 3.3.6 are sufficient to fulfill criterion 1.2.

The first of the four optional secondary verification criteria (2.1) was extending the operable lifetime of the satellite. Two specific failure modes; delamination of the PCB and solder joint cracking on the surface mount components described in Appendix K can be effectively reduced (increase # cycles to failure) by the implementation of this thesis. While delamination is unlikely given the parameters and cycling in question, the cracking experienced by the surface mount components definitely could occur and the ability to reduce the panel temperatures by up to 35.8% would provide a reduction in the fatigue on those joints. In addition to the reduction in panel cycling the increase in internal temperature helps in limiting lithium metal plating on the anode during cold temperature charging.

Lithium ion batteries are typically not rated for charging below 0°C due to their anode plating phenomenon. Panasonic rates the battery in CP3 for charging exclusively between 0 and 45°C. This creates a serious issue as the
battery temperatures predicted through analysis (-32\(^\circ\)C), verified in thermal vacuum testing (-19.1\(^\circ\)C), and finally experienced on orbit (-18.3\(^\circ\)C), all are well below this rated limit (0\(^\circ\)C). At subzero temperatures lithium metal begins to plate on the anode of the batteries during charging. This plating is amplified by the charging amount as well, since the CP3 solar panels are most efficient at the coldest temperature and the coldest temperature for both the solar panels and the batteries is experienced immediately before leaving eclipse this has a compounding effect. Just as the battery is coldest the solar panels are attempting to charge it at the highest rate.

As the charging from the solar panel occurs the battery loses potential (shown in Appendix M, Figure 129). At -10\(^\circ\)C the potential is anywhere from 10\% to 50\% less than the equivalent battery charged at 0\(^\circ\)C. The cumulative effect of such low-temperature plating has been shown via testing to reduce the total capacity over equivalent cycles by 12.5\% from 0\(^\circ\)C to -10\(^\circ\)C and by 21.9\% from 0\(^\circ\)C to -20\(^\circ\)C.\(^\text{24}\) As the battery temperature during thermal vacuum testing was increased from a minimum of -19.1\(^\circ\)C down to a minimum of -4.2\(^\circ\)C this provides a significant improvement (10-20\% estimated) in the total capacity available over the lifetime of the battery by limiting the losses due to low temperature charge plating effects. The actual percentage improvement had to be estimated as the results from the study performed were non-linear and only six data points were presented for the capacity reduction.

Next on the list, criterion 2.2 called for an improved RF or EP&H
performance. The metric used to quantify this performance increase was either increased ACK or improved RSSI values. From the testing performed and summarized in Appendix L the RSSI values were found to trend strongly upwards during temperature ramps and cold soaks. They trended down during ‘warm’ periods and under smaller temperature ramps. Given that the testing and analysis shows that both smaller ramps and ‘warmer’ system temperatures can be achieved, the RSSI parameter for criterion 2.2 was verified. The on-orbit testing also supported this verification, though not to the same degree as the thermal vacuum testing did. During on orbit operations testing the same trends were observed for RSSI and system “ACK,” as more heat was placed into the satellite via increased contacts & increased beacon rates the sensitivity of the satellite showed improvement.\textsuperscript{A} The second metric of the satellite providing an “acknowledge” or “ACK” to commands sent was also tested during the RSSI tests (Appendix L) and break points were found both on the cold dwell and on the hot/cold and cold/hot ramps. This further evidenced that large gradients were at least part of the origin for receive sensitivity issues.

Criterion 2.3 was established to show improvement C&DH or $I^2C$

\textsuperscript{A} Since the on-orbit testing opportunities were so limited this was only considered an “observation” of the system and not evidence of an outcome
performance and reliability. The best metric for this was found to be C&DH reset values and lowered drift on the oscillator crystal (as this directly impacts the I²C performance). The reset value was later found to be a poor metric as the software & electrical design team did an excellent job in building a system that did not reset easily, or at least not due to thermal inputs. As no resets occurred during testing this metric was found to be unusable. The lowered drift rate metric however was easy to verify once the temperature increases were tested and quantified. Per the data in Appendix M, the temperature variability of the I²C clock and the temperature improvements that were found to be possible the frequency stability could be decreased from 3.5ppm down to 2ppm, an improvement of 75%. In addition the trim register frequency per Least Significant Bit, which also could be reduced by 48% in total variability (from 28.4ppb to 14.6ppb). These improvements are considered to verify criterion 2.3.

The last criterion for verification was number 2.4 and reads “Enhanced Payload Performance – Realized or Potential.” This was the most challenging of all the criteria, as it requires strong indication of improvements on a system that is not easily tested. Using data acquired during the author’s support to a fifteen hour thermal vacuum cycle test performed for K. Glassey’s thesis specifically addressing the payload on CP3, this criterion can be verified. Figure 86 below illustrates the payloads current draw over a cycle (with constant duty cycle on the payload).
The payload’s current draw was greatly affected during cycle ramping, after review the only explanation that could be ascertained was that thermal strain on connections was increasing the resistance and causing the current to increase. In addition to the current increase the cameras were not rated to below $10^\circ$C and $-10^\circ$C for the KAC 9638 and 9648 respectively. During testing, as the payload temperature dropped below the rated levels sensitivity quickly degraded. At extremely low temperatures the CMOS would white out. The required black levels could either be manually adjusted to account for this issue, but over several orbits and with limited communication, manual adjustment was not reasonable for true operations. Thus the payload operations would definitively be improved with the application of the modes discussed and Criterion 2.4 was met.
The last criteria were ‘constraints’ or items that were not goals to be met but instead limits that could not be broken, in order to consider the implementation successful. Constraint 3.1 called for EP&H subsystem values for BATTA and BATTB to show no overcurrent or undercurrent faults due to the implementation of the modes. Although modes 4a, 4b and 4c were the highest current draws of all the postulated and tested schemes, none of them caused an overcurrent fault during testing, showing that constraint 3.1 was met. Constraint 3.2 called for the EP&H subsystem not to exceed the manufacturers recommended depth of discharge (DoD) limits. Based on the duty cycle and modes utilized the maximum DoD for the batteries was calculated to be 22.9% after one eclipse under the largest power draw. This is well within the manufacturer’s recommended lifetime DoD limit specified (Appendix A). As this is within the DoD limit while taking into consideration charging only during the satellite’s sunlit period the constraint was successfully met.

With all the verification criteria met, and based on the aforementioned evidence presented and tests performed, the conclusion can be made that adaptive component usage is a viable thermal management technique for CubeSats. The modes presented each have different applicability but each one demonstrates strong capability for improving the satellites thermal condition. Mode 5 presented the best system level response with a lower DoD than other modes while mode 4a, 4b and 4c presented the greatest heating opportunity if DoD were not a factor. Even modes 2a and 2b, while providing only a small
thermal improvement had secondary benefits as higher beacon rates also increase the ability of ground tracking systems and personnel to contact the satellite providing a better opportunity to implement mode 5.

4.2 Future Studies

4.2.1 Commanding

One of the key enablers to making this form of thermal management a reality is having available commanding time. Because the most majority of these modes require some form of care and feeding, especially the high-power modes, the amount of available uplink time is critical. The figure below, taken from a separate CubeSat thesis regarding ground station automation routines, illustrates how non-steady commands can use changes in ground operations to enable them to still be a practical part of the adaptive component utilization thermal scheme. The increase from 2% to 67% command availability is very much a game changing item for the implementation of portions of this thesis.

![Figure 87. Spacecraft ops availability before and after GENSO](image)

Figure 87. Spacecraft ops availability before and after GENSO
The figure below shows the layout of the current Global Educational Network for Satellite Operations (GENSO) member sites and their associated coverage for LEO satellites. The exponential growth of the GENSO project is another enabler to making on-orbit adaptive component utilization a reality, especially for satellites and teams that either do not have an automated ground station or have very limited pass-times and no “steady commanding” built into their satellites on-board operations.

![GENSO Network coverage for LEO Satellites](image)

**Figure 88. GENSO Network coverage for LEO Satellites**

With the help and support of previous Cal Poly PolySat engineers Kyle Leveque (SRI) and Bryan Klofas (SRI) the GENSO effort is taking hold and coming to maturity in the United States. A study regarding how these new capabilities could facilitate this thesis and other innovative concepts is recommended.
4.2.2 Passive Thermal Control Improvements

Based on the knowledge gained regarding the surface properties of the CP satellites there are improvements that could be made to increase the eclipse low temperatures and decrease the sunlit high temperatures. Specifically, utilizing a solder mask overlay with low absorptivity and low emissivity such as a DuPont 4817 or Epson 828 would improve the capability of the satellite to retain what little heat it produces while rejecting the heat incident upon its exposed non-solar panel surfaces. If other options, such as mylar or kapton insulation, were available a single layer of aluminized kapton integrated under the external panels, encapsulating the interior of the satellite would serve a similar purpose and would be a simpler integration.

4.2.3 Improved Capabilities with Increased Power Budgets

The power budget available to CubeSats has been increasing rapidly over the past five years. With the advent of a dedicated CubeSat power supply company, ClydeSpace, and the introduction of new 3U deployable solar panels by Pumpkin and Northrop Grumman, CubeSats are entering the realm of >50W power supplies. This drastic increase in the available power makes CubeSat thermal design both simpler and more challenging. It becomes easier because satellites can now be designed more in-line with industrial satellites. These new power levels allow for cold-biased designs with heaters to protect key subsystems and maintain internal temperatures. The new challenge comes from the fact that these high power CubeSats now have the opposite problem of too
little thermal energy. They now have to find a way to safely and reliably reject 50 watts of thermal energy. This is a huge amount of energy for a 4 kilogram satellite to reject and if the designers are not careful they could quickly find themselves overheating their satellite’s key sensors and electronics.

As an example of the challenges CubeSats face when implementing heater circuits, the COMPASS CubeSat mission launched on 28 April 2008 and had trouble initially contacting their satellite. They had integrated a foil heater similar to the one shown below in Figure 91.

![Kapton Enclosed Foil Strip Heater](image)

**Figure 89. Kapton Enclosed Foil Strip Heater**

The COMPASS satellite (University of Aachen, Germany) underwent a reset after in mid-August of 2008 that turned off all the major satellite electronics and then turned them back on. Unfortunately during the off-period the satellite had cooled down enough that when the satellite turned back on it immediately engaged the heater which, in normal fashion for a resistive heater, is a current heavy operation. The current draw forces an under-voltage on the satellite bus, which unfortunately then sends a reset command turning the main bus back off.
It continued limping along in this fashion until the Aachen team finally, in desperation, released all of their uplink command information to the amateur radio community in the hopes that someone would be able to catch the satellite in between its destructive on-off cycle and alter the thermistor settings that enabled the heater on command. On September 10\textsuperscript{th} 2008 the team finally, thanks to amateur satellite operators around the world pinging COMPASS with the adjustment, got it back into a stable configuration with an adjusted temperature setting. This is one example of just how challenging onboard thermal control can be for a CubeSat team and a perfect illustration of how a passive thermal solution is always a safer alternative.
REFERENCES


   Assessment Procedures for Limiting Orbital Debris NSS 1740.14”. Washington, D.C.,
   1995.


   Specification for STS & ELV Payloads, Subsystems and Components”. Goddard Space
   Flight Center, Greenbelt, Maryland, 1996.


   Retrieved from http://www.pc104.org/

9. Mortenson, C., “NPS-SCAT; Communications System Design, Test and Integration of

    us.com/>.

    from <http://Panasonic.com>


APPENDIX A. CP3 Power Storage Thermal & Life Properties

Shown below in Figure 90 are the properties of the batteries chosen for CP3.

Note typical discharge by temperature and typical life characteristics.

Figure 90. Panasonic Prismatic Lithium Ion Operating Characteristics
Appendix B. ISOLA FR406 PCB Material Properties Sheet

**High Performance Epoxy Laminate and Prepreg**

Isola's FR406 sets the industry standard for high performance epoxy materials. This product is engineered to meet the demands of the multilayer printed circuit board industry, while maintaining standard FR-4 processing. FR406 offers improved dimensional control, superior chemical and thermal performance and product consistency.

**Performance and Processing Advantages**

- **High Tg – 170 °C (DSC)**
  Superior performance through multiple thermal excursions
  Superior chemical and thermal resistance
  Lower CTE from ambient to 288 DegC

- **Ultraviolet (UV) Blocking & AOI Compatible**
  UV blocking and enhanced florescence
  Laser enhanced reflectance compatible with all AOI equipment

- **Standard FR-4 Processing**
  No post bake after pressing.
  Drilling parameters and hole wall preparation are standard

**Purchasing Information**

- **Industry Approvals**
  IPC-4101B /24, /26 and /28
  UL Recognized – FR-4, File Number E41625 (Part of Isola’s FR-4 Family)

- **Availability**
  Thickness: 0.002” [0.05 mm] to 0.125” [3.2 mm]
  Available in sheet or panel form

  Copper Foil Cladding: Grade 3 (HTE), 1/8 to 3 oz.

  Foil Options: Double treat, reverse treat

  Prepregs: Available in roll or panel form

  Glass Styles: standard offerings

---

Figure 91. FR406 Material Properties Sheet
### FR406 Typical Laminate Properties

<table>
<thead>
<tr>
<th>Property</th>
<th>English</th>
<th>Metric</th>
<th>Test Method</th>
</tr>
</thead>
<tbody>
<tr>
<td>Glass Transition Temperature (Tg)</td>
<td>170 °C</td>
<td>170 °C</td>
<td>ASTM D6045</td>
</tr>
<tr>
<td>Decomposition Temperature (Td)</td>
<td>300 °C</td>
<td>300 °C</td>
<td>ASTM D2867</td>
</tr>
<tr>
<td>CTE @ 25°C</td>
<td>10 ppm/°C</td>
<td>10 ppm/°C</td>
<td></td>
</tr>
<tr>
<td>2-Axis Expansion (50 – 200°C) (%)</td>
<td>3.5 %</td>
<td>3.5 %</td>
<td></td>
</tr>
<tr>
<td>Thermal Shock @ 15 sec</td>
<td>Pass</td>
<td>Pass</td>
<td></td>
</tr>
<tr>
<td>@ 25°C (30.6°C), spec minimum</td>
<td>Pass</td>
<td>Pass</td>
<td></td>
</tr>
<tr>
<td>DH (Permanently, Laminate &amp; planed)</td>
<td>2 G</td>
<td>2 G</td>
<td></td>
</tr>
<tr>
<td>DX (Laminate &amp; planed)</td>
<td>10 G</td>
<td>10 G</td>
<td></td>
</tr>
<tr>
<td>DL (Laminate &amp; planed)</td>
<td>2 G</td>
<td>2 G</td>
<td></td>
</tr>
<tr>
<td>Jigged #200K, spec minimum</td>
<td>2 G</td>
<td>2 G</td>
<td></td>
</tr>
<tr>
<td>Volume Resistivity, spec minimum</td>
<td>0.010</td>
<td>0.010</td>
<td></td>
</tr>
<tr>
<td>Surface Resistivity, spec minimum</td>
<td>0.010</td>
<td>0.010</td>
<td></td>
</tr>
<tr>
<td>Thermal Conductivity</td>
<td>0.110</td>
<td>0.110</td>
<td></td>
</tr>
<tr>
<td>Dielectric Breakdown, spec minimum</td>
<td>&gt;1000</td>
<td>&gt;1000</td>
<td></td>
</tr>
<tr>
<td>Arc Resistance, spec minimum</td>
<td>&gt;1000</td>
<td>&gt;1000</td>
<td></td>
</tr>
<tr>
<td>Electric Strength, spec minimum</td>
<td>1150 ±50</td>
<td>1150 ±50</td>
<td></td>
</tr>
<tr>
<td>Peel Strength, spec minimum</td>
<td>500</td>
<td>500</td>
<td></td>
</tr>
<tr>
<td>Moisture Absorption, spec maximum</td>
<td>0.03</td>
<td>0.03</td>
<td></td>
</tr>
<tr>
<td>CI</td>
<td>5</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>PV</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>THI</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>Max Operating Temp</td>
<td>130</td>
<td>130</td>
<td></td>
</tr>
<tr>
<td>SDR</td>
<td>YES</td>
<td>YES</td>
<td></td>
</tr>
<tr>
<td>Insulation Strength (psi)</td>
<td>320</td>
<td>320</td>
<td></td>
</tr>
<tr>
<td>Insulation Strength (psi)</td>
<td>320</td>
<td>320</td>
<td></td>
</tr>
<tr>
<td>Power Loss Ratio</td>
<td>0.01</td>
<td>0.01</td>
<td></td>
</tr>
<tr>
<td>Insulation Resistance (ohm/ft)</td>
<td>5.0</td>
<td>5.0</td>
<td></td>
</tr>
<tr>
<td>Voltage Insulation Resistance (ohm/ft)</td>
<td>5.0</td>
<td>5.0</td>
<td></td>
</tr>
</tbody>
</table>

Figure 92. FR406 Material Properties Sheet (cont’d)
Appendix C CP3 Payload (KAC-9638/48) Thermal Properties

The properties for the CP3 payload camera KAC-9638 are shown below. Note that the monochromatic CMOS (below figure 93) has only 67% of the thermal operating temperature range of the Color CMOS shown in the second figure.

![Kodak Image Sensor](https://example.com/kodak-image-sensor.png)

**Figure 93. KAC-9638 Monochromatic CMOS Operating Temperature Limits**
The properties for the CP3 payload camera KAC-9648 are shown below in figure 94. Note that it has a 33% wider thermal operating temperature tolerance than the monochromatic CMOS.

Figure 94. KAC-9648 Color CMOS Operating Temperature Limits
Appendix D – Populated PCB Calculations & Adjustments

The tables below were used to calculate the adjusted surface properties based on component sizes, component surface properties and populated numbers. The first step was to quantify the representative surface mount components, count and number them and finally calculate the adjusted surface emissivity and absorptivity based on the replaced and added surface area amounts. Table 24 below shows the initial calculations for component size, replaced surface area amount, added surface area amount and the averaged properties for both.

Table 24. Surface Area by Representative Component & Property Adjustment

<table>
<thead>
<tr>
<th>L*W</th>
<th>L*H</th>
<th>H*W</th>
<th>Leads</th>
<th>Extra Surface Area</th>
<th>Replaced Surface Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.5</td>
<td>0.5</td>
<td>0.25</td>
<td>0.125</td>
<td>1.50</td>
<td>0.75</td>
</tr>
<tr>
<td>Surface Emissivity</td>
<td>Avg Emissivity (Extra)</td>
<td>Avg Emissivity (Replaced)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.9</td>
<td>0.9</td>
<td>0.02</td>
<td>0.02</td>
<td>0.61</td>
<td>0.61</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>L*W</th>
<th>L*H</th>
<th>H*W</th>
<th>Leads</th>
<th>Extra Surface Area</th>
<th>Replaced Surface Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.5</td>
<td>0.35</td>
<td>0.175</td>
<td>0.125</td>
<td>1.05</td>
<td>0.75</td>
</tr>
<tr>
<td>Surface Emissivity</td>
<td>Avg Emissivity (Extra)</td>
<td>Avg Emissivity (Replaced)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.9</td>
<td>0.81</td>
<td>0.02</td>
<td>0.02</td>
<td>0.55</td>
<td>0.61</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>L*W</th>
<th>L*H</th>
<th>H*W</th>
<th>Leads</th>
<th>Extra Surface Area</th>
<th>Replaced Surface Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>19.071</td>
<td>7.5795</td>
<td>6.045</td>
<td>4.24</td>
<td>27.25</td>
<td>27.55</td>
</tr>
<tr>
<td>Surface Emissivity</td>
<td>Avg Emissivity (Extra)</td>
<td>Avg Emissivity (Replaced)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.9</td>
<td>0.9</td>
<td>0.9</td>
<td>0.02</td>
<td>0.90</td>
<td>0.63</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>L*W</th>
<th>L*H</th>
<th>H*W</th>
<th>Leads</th>
<th>Extra Surface Area</th>
<th>Replaced Surface Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>19.11</td>
<td>7.595</td>
<td>6.045</td>
<td>3.44</td>
<td>27.28</td>
<td>25.99</td>
</tr>
<tr>
<td>Surface Emissivity</td>
<td>Avg Emissivity (Extra)</td>
<td>Avg Emissivity (Replaced)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0.9</td>
<td>0.9</td>
<td>0.9</td>
<td>0.02</td>
<td>0.90</td>
<td>0.67</td>
</tr>
</tbody>
</table>

Using the representative components each board in the CP system was quantified in terms of total surface area (for the CP3 configuration), component population and then calculated to find the actual emissive and absorptive
properties of each board, top and bottom. Tables 25-30 contain the results for each of these calculations along with a summary in Table 31 of the total system area (external) and its averaged property, taking into account the anodized aluminum side rails. These are the values utilized in all supporting calculations and simulations (post Appendix 5 Initial Calculation).

**Table 25. Side Panel Interior Emissivity Calculation (Correction)**

<table>
<thead>
<tr>
<th>Solar Panels Interior</th>
<th>&quot;Resistors&quot;</th>
<th>&quot;Capacitors&quot;</th>
<th>&quot;Maxims&quot;</th>
<th>&quot;ADCs&quot;</th>
</tr>
</thead>
<tbody>
<tr>
<td>Extra Surface Area</td>
<td>48.00</td>
<td>35.00</td>
<td>4.00</td>
<td>3.00</td>
</tr>
<tr>
<td>Replaced Surface Area</td>
<td>50.40</td>
<td>52.50</td>
<td>109.00</td>
<td>81.84</td>
</tr>
<tr>
<td>Avg. Emissivity (Extra)</td>
<td>0.55</td>
<td>0.61</td>
<td>0.90</td>
<td>0.90</td>
</tr>
<tr>
<td>Avg. Emissivity (Replaced)</td>
<td>0.61</td>
<td>0.61</td>
<td>0.63</td>
<td>0.67</td>
</tr>
<tr>
<td>Weighted E By Area (Extra)</td>
<td>27.55</td>
<td>31.85</td>
<td>98.10</td>
<td>73.66</td>
</tr>
<tr>
<td>Weighted E By Area (Repl)</td>
<td>21.84</td>
<td>15.93</td>
<td>69.33</td>
<td>55.13</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TOTAL (mm^2)</th>
<th>TOTAL (m^2)</th>
<th>% of Panel</th>
<th>% of Panel</th>
</tr>
</thead>
<tbody>
<tr>
<td>293.74</td>
<td>0.00029</td>
<td>4%</td>
<td>7%</td>
</tr>
<tr>
<td>255.11</td>
<td>0.00026</td>
<td>3%</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>E AVG (SA)</th>
<th>E AVG (EX + RE)</th>
<th>New E</th>
<th>Previous E</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.79</td>
<td>0.72</td>
<td>0.81</td>
<td>0.90</td>
</tr>
<tr>
<td>0.64</td>
<td></td>
<td></td>
<td>10%</td>
</tr>
</tbody>
</table>

**Table 26. Side Panel Exterior Absorptivity and Emissivity Calculation (Correction)**

<table>
<thead>
<tr>
<th>Solar Panels Exterior</th>
<th>Cells</th>
<th>Leads</th>
<th>Averaged</th>
<th>% Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>Surface Area</td>
<td>54.6</td>
<td>2.32</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Avg. Emissivity</td>
<td>0.85</td>
<td>0.02</td>
<td>0.82</td>
<td>4.0%</td>
</tr>
<tr>
<td>Avg. Absorptivity</td>
<td>0.92</td>
<td>0.3</td>
<td>0.89</td>
<td>2.7%</td>
</tr>
<tr>
<td>Averaged E For Panel</td>
<td>Remainder of Panel is Soldermask @ .9</td>
<td></td>
<td>0.84</td>
<td></td>
</tr>
</tbody>
</table>

**Table 27. C&DH Board Emissivity Calculation (Correction)**
<table>
<thead>
<tr>
<th>C&amp;DH Board</th>
<th>&quot;Resistors&quot;</th>
<th>&quot;Capacitors&quot;</th>
<th>&quot;Maxims&quot;</th>
<th>&quot;ADCs&quot;</th>
</tr>
</thead>
<tbody>
<tr>
<td>Extra Surface Area</td>
<td>23.00</td>
<td>79.00</td>
<td>11.00</td>
<td>2.00</td>
</tr>
<tr>
<td>Replaced Surface Area</td>
<td>24.15</td>
<td>118.50</td>
<td>299.74</td>
<td>54.56</td>
</tr>
<tr>
<td>Avg. Emissivity (Extra)</td>
<td>0.55</td>
<td>0.61</td>
<td>0.90</td>
<td>0.90</td>
</tr>
<tr>
<td>Avg. Emissivity (Replaced)</td>
<td>0.61</td>
<td>0.61</td>
<td>0.63</td>
<td>0.67</td>
</tr>
<tr>
<td>Weighted E By Area (Extra)</td>
<td>13.20</td>
<td>71.89</td>
<td>269.77</td>
<td>49.10</td>
</tr>
<tr>
<td>Weighted E By Area (Repl)</td>
<td>10.47</td>
<td>35.95</td>
<td>190.67</td>
<td>36.76</td>
</tr>
<tr>
<td><strong>TOTAL (mm^2)</strong></td>
<td><strong>496.95</strong></td>
<td><strong>0.00050</strong></td>
<td><strong>6%</strong></td>
<td><strong>12%</strong></td>
</tr>
<tr>
<td><strong>TOTAL (m^2)</strong></td>
<td><strong>434.66</strong></td>
<td><strong>0.00043</strong></td>
<td><strong>5%</strong></td>
<td></td>
</tr>
<tr>
<td>E AVG (SA)</td>
<td>0.81</td>
<td>0.73</td>
<td>0.76</td>
<td>0.90</td>
</tr>
<tr>
<td>E AVG (EX +</td>
<td>New E</td>
<td>Previous E</td>
<td></td>
<td></td>
</tr>
<tr>
<td>E AVG (SA)</td>
<td>0.63</td>
<td></td>
<td></td>
<td>16%</td>
</tr>
</tbody>
</table>

Table 28. Standard Side Panel Corrected Emissivity and Absorptivity Values

<table>
<thead>
<tr>
<th>Standard Side-Panels (1-3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCB Panel Emissivity</td>
</tr>
<tr>
<td>PCB Panel Absorptivity</td>
</tr>
<tr>
<td>PCB Panel Area (m^2)</td>
</tr>
<tr>
<td>Primary Structure Emissivity</td>
</tr>
<tr>
<td>Primary Structure Absorptivity</td>
</tr>
<tr>
<td>Primary Structure Area (m^2)</td>
</tr>
<tr>
<td>PANEL (Emissivity)</td>
</tr>
<tr>
<td>PANEL (Absorptivity)</td>
</tr>
</tbody>
</table>

Table 29. +Z & -Z Side Panel Corrected Emissivity and Absorptivity Values
<table>
<thead>
<tr>
<th>+Z &amp; -Z Side-Panels</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>PCB Panel Emissivity</td>
<td>0.84</td>
</tr>
<tr>
<td>PCB Panel Absorptivity</td>
<td>0.89</td>
</tr>
<tr>
<td>PCB Panel Area (m²)</td>
<td>0.0080487</td>
</tr>
<tr>
<td>Primary Structure Emissivity</td>
<td>0.86</td>
</tr>
<tr>
<td>Primary Structure Absorptivity</td>
<td>0.73</td>
</tr>
<tr>
<td>Primary Structure Area (m²)</td>
<td>0.00100584</td>
</tr>
</tbody>
</table>

| PANEL (Emissivity) | 0.84 |
| PANEL (Absorptivity) | 0.87 |

Table 30. Antenna Side Panel Corrected Emissivity and Absorptivity Values

<table>
<thead>
<tr>
<th>Antenna Side-Panel</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>PCB Panel Emissivity</td>
<td>0.84</td>
</tr>
<tr>
<td>PCB Panel Absorptivity</td>
<td>0.89</td>
</tr>
<tr>
<td>PCB Panel Area (m²)</td>
<td>0.0066717</td>
</tr>
<tr>
<td>Primary Structure Emissivity</td>
<td>0.86</td>
</tr>
<tr>
<td>Primary Structure Absorptivity</td>
<td>0.73</td>
</tr>
<tr>
<td>Primary Structure Area (m²)</td>
<td>0.00150876</td>
</tr>
<tr>
<td>Delrin (black) Emissivity</td>
<td>0.87</td>
</tr>
<tr>
<td>Delrin (black) Absorptivity</td>
<td>0.96</td>
</tr>
<tr>
<td>Delrin Area</td>
<td>0.001377</td>
</tr>
</tbody>
</table>

| PANEL (Emissivity) | 0.85 |
| PANEL (Absorptivity) | 0.87 |

Table 31. Corrected Satellite Surface Averages (Satellite Whole)

<table>
<thead>
<tr>
<th>SATELLITE SURFACE AVERAGED</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Emissivity</td>
<td>0.84</td>
</tr>
<tr>
<td>Absorptivity</td>
<td>0.87</td>
</tr>
<tr>
<td>E/Alpha</td>
<td>0.97</td>
</tr>
</tbody>
</table>
APPENDIX E. Sierra Proto Express Solder Mask Properties

William,

I could not find a lot of information regarding the thermal properties of LPI solder mask. I've attached the data sheet for the solder mask that we use but I saw no mention of thermal conductivity, emissivity or absorptivity. The link below does have a callout, but its not specific on the type of mask that is being referenced.


LeRoy
LeRoy Hendy
Technical Specialist
Sierra Proto Express
408-735-7137 ext.107
- Hide quoted text -

-----Original Message-----

From: wdwhalen@gmail.com [mailto:wdwhalen@gmail.com] On Behalf Of William Whalen

Sent: Thursday, June 28, 2007 11:49 PM

To: LeRoy Hendy

Subject: Re: Data sheet for FR406

Dear Leroy,

On our PCB boards that we've purchased from Proto express, the top layer is typically coated in either black or blue enamel solder mask. Do you have any information regarding the thermal properties on this? We're trying to figure out the radiative properties specifically, but any material data you can obtain would be helpful.

Thanks,

William

Figure 95. Sierra Proto Express Tech Specialist Reference Letter
Figure 96. Initial Proof-of-Concept Calculation with Assumptions
APPENDIX G. Projected Area Calculations

Maximum projected area calculation table. Cont’d on following page.

Table 32. Tumbling Satellite Panel Area Calculations

<table>
<thead>
<tr>
<th>Solar Flux</th>
<th>Face Area</th>
<th>X Face</th>
<th>Y Face</th>
<th>Z Face</th>
<th>Total Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>173.2032367 m^2</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
</tr>
<tr>
<td>0.01732032364</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
</tr>
</tbody>
</table>

CPX Area Projection Calculator

<table>
<thead>
<tr>
<th>Solar Flux</th>
<th>Face Area</th>
<th>X Face</th>
<th>Y Face</th>
<th>Z Face</th>
<th>Total Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>1730.32367 m^2</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
</tr>
<tr>
<td>0.01732032364</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
</tr>
</tbody>
</table>

Maximum Projected Area Calculation Table

<table>
<thead>
<tr>
<th>Maximum Projected Area</th>
<th>Solar Flux</th>
<th>Face Area</th>
<th>X Face</th>
<th>Y Face</th>
<th>Z Face</th>
<th>Total Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>1730.32367 m^2</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
</tr>
<tr>
<td>0.01732032364</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
<td>1000</td>
</tr>
<tr>
<td>0</td>
<td>160.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>0.00</td>
<td>70.00</td>
</tr>
<tr>
<td>10</td>
<td>200.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>10.00</td>
<td>70.00</td>
</tr>
<tr>
<td>20</td>
<td>240.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>0.00</td>
<td>70.00</td>
</tr>
<tr>
<td>25</td>
<td>300.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>10.00</td>
<td>70.00</td>
</tr>
<tr>
<td>50</td>
<td>300.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>0.00</td>
<td>70.00</td>
</tr>
<tr>
<td>55</td>
<td>300.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>10.00</td>
<td>70.00</td>
</tr>
<tr>
<td>60</td>
<td>300.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>0.00</td>
<td>70.00</td>
</tr>
<tr>
<td>75</td>
<td>300.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>10.00</td>
<td>70.00</td>
</tr>
<tr>
<td>90</td>
<td>300.00</td>
<td>342.02</td>
<td>960.00</td>
<td>1920.00</td>
<td>0.00</td>
<td>70.00</td>
</tr>
</tbody>
</table>

Table 33. Tumbling Satellite Panel Area Calculations (cont’d)
Appendix H. Thermal Equilibrium Worst-Case Hot/Cold

Using the equations from section 3.2.3 several calculations were run evaluating multiple heating conditions with multiple environmental conditions over both worst-case cold and worst-case hot scenarios. A sample calculation is shown below for a worst-case cold scenario.

\[ Q_{\text{Earth IR}} + Q_{\text{Internal}} = \varepsilon_{\text{Satellite}} \sigma A \left(T_{\text{Satellite}}^4 - T_{\text{Space}}^4\right) \]

Equation 22. Radiative Exchange Equation

Plugging in the Earthshine value, the surface area of the satellite, the Stefan-Boltzmann constant, the temperature of space (effectively zero) and the emissivity value for the spacecraft gives the next equation.

\[ 218 \frac{W}{m^2} \times .84 \times .05 m^2 + 1.06 W = .84 \times \left(\frac{5.67 \times 10^{-8} W}{m^2 K^4}\right) \times .06 m^2 \left(T_{\text{Satellite}}^4 - 0\right) \]

Equation 23. Radiative Exchange Equation with Values

Solving for the temperature of the satellite gives the following equation.

\[ \left[ \frac{218 \frac{W}{m^2} \times .84 \times .05 m^2 + 1.06 W}{.84 \times \left(\frac{5.67 \times 10^{-8} W}{m^2 K^4}\right) \times .06 m^2} \right]^{1/4} = T_{\text{Satellite}} \]

Equation 24. Radiative Equation Solved for Satellite Temperature

For this case the temperature of the satellite was found to be -29°C.
### Table 34. Inputs for Thermal Equilibrium Calculations

<table>
<thead>
<tr>
<th>CASE</th>
<th>Description</th>
<th>Absorptivity</th>
<th>Emissivity</th>
<th>Q solar</th>
<th>Q Albedo</th>
<th>Q IR</th>
<th>Internal Power</th>
<th>Eclipse Factor</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Initial</td>
<td>0.92</td>
<td>0.85</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>2a</td>
<td>Cold w/o Adjustments</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.0615</td>
<td>0</td>
</tr>
<tr>
<td>2b</td>
<td>Cold w/ Adjustments</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.0615</td>
<td>0</td>
</tr>
<tr>
<td>2c</td>
<td>Cold w/ Single Face (Gilmore)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.0615</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>Mode 1 (Standard Operations)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.0615</td>
<td>0</td>
</tr>
<tr>
<td>3a</td>
<td>Mode 2a. (Inc. Beacon Rate - 90s)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.2859</td>
<td>0</td>
</tr>
<tr>
<td>3b</td>
<td>Mode 2b. (Inc. Beacon Rate - 60s)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.71822</td>
<td>0</td>
</tr>
<tr>
<td>3c</td>
<td>Mode 3. (Payload Enabled - ON)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>1.77947</td>
<td>0</td>
</tr>
<tr>
<td>3d</td>
<td>Mode 4a. (ADC ON)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>5.09652</td>
<td>0</td>
</tr>
<tr>
<td>3e</td>
<td>Mode 4b. (ADC ON &amp; Beacon Inc. - 90s)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>5.32092</td>
<td>0</td>
</tr>
<tr>
<td>3f</td>
<td>Mode 4c. (ADC ON &amp; Beacon Inc. - 60s)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>5.74552</td>
<td>0</td>
</tr>
<tr>
<td>3g</td>
<td>Mode 5. (Increased &quot;Data Drop&quot;)</td>
<td>0.87</td>
<td>0.84</td>
<td>0</td>
<td>0</td>
<td>218</td>
<td>3.38712</td>
<td>0</td>
</tr>
<tr>
<td>4a</td>
<td>Hot w/o Adjustments</td>
<td>0.87</td>
<td>0.84</td>
<td>1414</td>
<td>805.98</td>
<td>244</td>
<td>1.0615</td>
<td>1</td>
</tr>
<tr>
<td>4b</td>
<td>Hot w/ Adjustments</td>
<td>0.87</td>
<td>0.84</td>
<td>1414</td>
<td>805.98</td>
<td>244</td>
<td>1.0615</td>
<td>1</td>
</tr>
<tr>
<td>4c</td>
<td>Hot w/ Single Face (Gilmore)</td>
<td>0.87</td>
<td>0.84</td>
<td>1414</td>
<td>805.98</td>
<td>244</td>
<td>1.0615</td>
<td>1</td>
</tr>
</tbody>
</table>

### Table 35. Outputs for Thermal Equilibrium Calculations

<table>
<thead>
<tr>
<th>CASE</th>
<th>Earthshine A</th>
<th>Solar A</th>
<th>Albedo</th>
<th>Stefan Boltzman</th>
<th>Q external</th>
<th>Adjusted (E&amp;A)</th>
<th>Q total</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.06</td>
<td>0</td>
<td>0</td>
<td>5.67E-08</td>
<td>11.182092</td>
<td>12.182092</td>
<td>13.182092</td>
</tr>
<tr>
<td>2a</td>
<td>0.06</td>
<td>0.0173</td>
<td>0.0173</td>
<td>5.67E-08</td>
<td>10.9872</td>
<td>12.0487</td>
<td>13.1102</td>
</tr>
<tr>
<td>2b</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>10.2175</td>
<td>11.279</td>
</tr>
<tr>
<td>2c</td>
<td>0.01</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>1.8312</td>
<td>2.8927</td>
<td>3.9542</td>
</tr>
<tr>
<td>3</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>10.2175</td>
<td>11.279</td>
</tr>
<tr>
<td>3a</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>10.4419</td>
<td>11.7278</td>
</tr>
<tr>
<td>3b</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>10.87222</td>
<td>12.58844</td>
</tr>
<tr>
<td>3c</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>10.93547</td>
<td>12.71494</td>
</tr>
<tr>
<td>3d</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>14.25252</td>
<td>19.34904</td>
</tr>
<tr>
<td>3e</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>14.47692</td>
<td>19.79784</td>
</tr>
<tr>
<td>3f</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>14.90152</td>
<td>20.64704</td>
</tr>
<tr>
<td>3g</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>9.156</td>
<td>12.54312</td>
<td>15.93024</td>
</tr>
<tr>
<td>4a</td>
<td>0.05</td>
<td>0.0173</td>
<td>0.0173</td>
<td>5.67E-08</td>
<td>43.660919</td>
<td>44.72241898</td>
<td>45.783919</td>
</tr>
<tr>
<td>4b</td>
<td>0.05</td>
<td>0.0149</td>
<td>0.0149</td>
<td>5.67E-08</td>
<td>39.0256007</td>
<td>40.08710074</td>
<td>41.1486007</td>
</tr>
<tr>
<td>4c</td>
<td>0.01</td>
<td>0.01</td>
<td>0.01</td>
<td>5.67E-08</td>
<td>21.363426</td>
<td>22.424926</td>
<td>23.486426</td>
</tr>
</tbody>
</table>
Table 36. Thermal Equilibrium Calculation Results

<table>
<thead>
<tr>
<th>CASE</th>
<th>Solved for T Kelvin</th>
<th>(Celsius)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CASE 1 - Initial</td>
<td>259</td>
<td>-14</td>
</tr>
<tr>
<td>CASE 2a - Cold w/o Adjustments</td>
<td>260</td>
<td>-13</td>
</tr>
<tr>
<td>CASE 2b - Cold w/ Adjustments</td>
<td>241</td>
<td>-32</td>
</tr>
<tr>
<td>CASE 2c - Cold w/ Single Face (Gilmore)</td>
<td>205</td>
<td>-68</td>
</tr>
<tr>
<td>CASE 3 - Mode 1 (Standard Operations)</td>
<td>241</td>
<td>-32</td>
</tr>
<tr>
<td>CASE 3 - Mode 2a. (Inc. Beacon Rate - 90s)</td>
<td>253</td>
<td>-20</td>
</tr>
<tr>
<td>CASE 3 - Mode 2b. (Inc. Beacon Rate - 60s)</td>
<td>258</td>
<td>-15</td>
</tr>
<tr>
<td>CASE 3 - Mode 3. (Payload Enabled - ON)</td>
<td>258</td>
<td>-15</td>
</tr>
<tr>
<td>CASE 3 - Mode 4a. (ADC ON)</td>
<td>287</td>
<td>14</td>
</tr>
<tr>
<td>CASE 3 - Mode 4b. (ADC ON &amp; Beacon Inc. - 90s)</td>
<td>289</td>
<td>16</td>
</tr>
<tr>
<td>CASE 3 - Mode 4c. (ADC ON &amp; Beacon Inc. - 60s)</td>
<td>292</td>
<td>19</td>
</tr>
<tr>
<td>CASE 3 - Mode 5. (Increased &quot;Data Drop&quot;)</td>
<td>273</td>
<td>0</td>
</tr>
<tr>
<td>CASE 4a - Hot w/o Adjustments</td>
<td>356</td>
<td>83</td>
</tr>
<tr>
<td>CASE 4b - Hot w/ Adjustments</td>
<td>346</td>
<td>73</td>
</tr>
<tr>
<td>CASE 4c - Hot w/ Single Face (Gilmore)</td>
<td>315</td>
<td>42</td>
</tr>
</tbody>
</table>

Table 37. Adjusted “Worst-Case” Hot w/ Adjusted Internal Heat Flux

<table>
<thead>
<tr>
<th>CASE</th>
<th>Solved for T Kelvin</th>
<th>(Celsius)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CASE 5-2a - Hot w/ Adjustments</td>
<td>347</td>
<td>74</td>
</tr>
<tr>
<td>CASE 5-2b - Hot w/ Adjustments</td>
<td>349</td>
<td>76</td>
</tr>
<tr>
<td>CASE 5-3 - Hot w/ Adjustments</td>
<td>349</td>
<td>76</td>
</tr>
<tr>
<td>CASE 5-4a - Hot w/ Adjustments</td>
<td>362</td>
<td>89</td>
</tr>
<tr>
<td>CASE 5-4b - Hot w/ Adjustments</td>
<td>363</td>
<td>90</td>
</tr>
<tr>
<td>CASE 5-4c - Hot w/ Adjustments</td>
<td>365</td>
<td>92</td>
</tr>
<tr>
<td>CASE 5-5 - Hot w/ Adjustments</td>
<td>356</td>
<td>83</td>
</tr>
</tbody>
</table>
APPENDIX I. THERMAL RESISTANCE CALCULATIONS

Using the equations in section 3.2.5 and applying the physical properties of the satellite and its materials gives the equation below.

\[ R_{EQ} = \frac{0.025m}{18W/mK \cdot 0.0014m^2} + \frac{3.79K}{4 \cdot W} + \frac{.1m}{128W/mK \cdot 0.001m^2} + \frac{3.79K}{4 \cdot W} + \frac{.025m}{18W/mK \cdot 0.0014m^2} \]

Equation 25. \( R_{EQ} \) from Side Panel to Side Panel (Lateral) with Fourier’s Substitution - Solved

Solving this equation gave a total resistance of 23.3 K/W. Figure 99 below was used to calculate the view factor between parallel panels inside the satellite.

With the values of beta and alpha at 8.5 and 7.6 respectively the view factor was found to be approximately .85 between the central PCBs of the CP3 CubeSat.

![Figure 97 Parallel Flat Plates View Factor (Configuration Factor)](image)

Figure 97 Parallel Flat Plates View Factor (Configuration Factor)
APPENDIX J. TSS and SINDA Modeling
The figures below illustrate the method for input into TSS and the resulting data, plotted, from the SINDA post-processor. Shown in Figure 103 is the orbital heating input generated by TSS. The satellite was modeled as a simple sphere with the equivalent surface area as a 10x10x10cm CubeSat. The parameters for emissivity and absorptivity were directly from the calculations in Appendix 1. The orbit used was the DNEPR mission orbit for CP3. The internal heat generation settings were all specified per the Appendix 2 measured and calculated values (orbital average by duty cycle). Heating duty cycles were applied using the .tsshr file specified.

The one assumption made in the SINDA modeling was giving the satellite the conductivity of aluminum. For the purposes of a single node simulation this is a non-issue, but a multi-node simulation would require adequate conductivity information in order to accurately model the difference between nodes.
Figure 98. TSS Orbital Input Window and Heating Propagation

Figure 99. TSS Variables Input
Figure 100. TSS Variables Input Cont’d

Figure 101. SINDA Output – 24 Hour Nominal (Standard Operations)
Figure 102. SINDA Orbital Heating for One Orbit

Figure 103. SINDA Output Mode 1 Standard Operations
Figure 104. SINDA Output Mode 2a Increased Beacon Rate

Figure 105. SINDA Output w/ High Beacon Rate & High Sensor Snap Rate (Schema 2b)
Figure 106. SINDA Output w/ Payload Enabled (Schema 3)

Figure 107. SINDA Output w/ ADC On (Schema 4a)
Figure 108. SINDA Output w/ ADC On & 90s (Schema 4b)

Figure 109. SINDA Output w/ ADC On & 60s (Schema 4c)
Figure 110. SINDA Output w/ Increased Contact/ Data Drop (Schema 5)
APPENDIX K. SMC THERMAL GRADIENT FAILURE RATES

To provide background the following information was gathered that compares the fatigue, cracking and issues caused by thermal cycling on surface mount components. The following figures and data are from a testing performed by Takayuki Nagai at Japan’s Espec Test Center Corporation.32

![Figure 111. PCB Article for Surface Mount Component Failure Rate Test](image)

The surface mount components shown in figure 111 above and used for the test are identical to those used in a typical CubeSat including 1608, 2012 and occasionally 3216 resistors. In the figure below you can clearly see the increased void distance, cracking and strain being induced by the large thermal ramp or gradient rates.
In Figure 131 below you can see the higher ramp rate clearly affecting the overall solder joint connection.

Figure 112. Solder Joint Cracking due to Thermal Cycling & Alteration in Resistance

Figure 113. 1000 Cycle Thermal Test on 3216 Surface Mount Resistor
The last figure presented illustrates the drastic reduction in cycles to failure caused by just a 15°C higher ramp. This same mechanism holds true during on-orbit gradients and is part of the reason for maintaining as steady of a system temperature as possible will have a lifespan effect on the surface mount components that make up the majority CubeSat electronics.

Figure 114. Failure by Resistor Type and Cycle Number for Different Ramp Gradients\(^\text{32}\)
APPENDIX L. RSSI – Test Procedure & Results

Testing CP2 & 3 RSSI Temperature Effects

<table>
<thead>
<tr>
<th>Revision Number</th>
<th>Changes Made</th>
<th>Changes Made By</th>
<th>Date Changes Made</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>Document Creation</td>
<td>William Whalen</td>
<td>May 28, 2007</td>
</tr>
<tr>
<td>1</td>
<td>Alteration for 2nd Test</td>
<td>William Whalen</td>
<td>June 4, 2007</td>
</tr>
</tbody>
</table>

Antenna Positions:

Marconi:

Elevation: 0 degrees

Azimuth: 60 degrees

Hertz:

Elevation: 0 degrees

Azimuth: 60 degrees
# Table of Contents

TESTING CPX RSSI TEMPERATURE EFFECTS ................................................................. 179

1 ...................................................................................................................................... PURPOSE

................................................................. 182

1.1 Primary Objective 129

1.2 Secondary Objective ......................................................................................... 182

1.3 Third Objective ............................................................................................... 129

1.4 Hardware Setup .............................................................................................. 182

1.5 Power Board Verification ............................................................................... 182

1.6 Side Board Connection ................................................................................... 182

1.7 Run through 2.1 then return to this point ..................................................... 183

1.8 Prepare TestSat for moving to T-Vac location ............................................ 183

1.9 Once TestSat is at T-Vac location................................................................. 183

2 .................................................................................................................................. TEST CONDITIONS

................................................................................................................................. 183

2.1 ATL RSSI Testing ................................................................. 183

2.2 T-VAC RSSI Testing .................................................................................. 184

2.3 Required equipment ...................................................................................... 184

2.4 Process ........................................................................................................... 184

2.5 RSSI – Standard Values ............................................................................... 184

2.5.3..................................................................................................................... Cold Conditions

................................................................................................................................. 185

2.6 Break-Point Receive Procedure ................................................................. 186

2.6.1..................................................................................................................... Break Point Step 1

................................................................................................................................. 186
2.6.2..............................................................................................................Break Point Step 2
......................................................................................................................186

2.6.3..............................................................................................................Hot Conditions - NONE
......................................................................................................................186

3..............................................................................................................DATA ANALYSIS
......................................................................................................................186

3.1 Determining the relationship .................................................................186
Purpose

**Primary Objective**

The primary purpose of this experiment is to find the relationship between RSSI readings and the temperature in which the CP3 and CP2 assembly has been placed. We will stress the objective of placing the boards in its on-orbit situations.

**Secondary Objective**

The secondary purpose of this experiment is to test magnetometer readings fluctuations with temperature. Previous tests only ran the magnetometers to -10 C at which point some erroneous readings were found. This test will take the magnetometers down to actual on-orbit temperatures of -35 to -40 C.

**Third Objective**

The last objective of this experiment is, if possible, to quantify heating values with operational commands and operations. This will require data analysis post-test.

**Hardware Setup**

1.1.1 Prepare TestSat for Thermal Vacuum chamber.
1.1.2 Check for any loose connections between boards
1.1.3 Prepare ground station for downloading data during test
   1.1.3.1 Create “T-VAC Test” Folder on Hertz and Marconi
   1.1.3.2 Make directory forParsed file saving as aforementioned folder

**Power Board Verification**

1.1.4 Turn the multimeter on to the voltmeter mode.
1.1.5 Use the banana-to-banana connectors to connect the multimeter to the “V Batt” and “Batt Gnd” ports on the umbilical box.
1.1.6 If the multimeter reads a voltage less than 3.8 V, replace or charge the Power board and repeat steps 1.1.4 to 1.1.6 until the voltage is 3.8 V or greater.
1.1.7 Disconnect multimeter.

**Side Board Connection**
1.1.8 Put on gloves.
1.1.9 Put on a grounding strap and snap into a ground.
1.1.10 Connect the 10-pin cable coming out of JP7 on the power board into JP1 on the side board.
1.1.11 This allows for sideboard readings to be taken. Run through 2.1 then return to this point

**Prepare TestSat for moving to T-Vac location**

**Once TestSat is at T-Vac location**

1.1.12 Check RSSI 5 times outside chamber
   1.1.12.1 X____30__________
   1.1.12.2 X____30__________
   1.1.12.3 X____32__________
   1.1.12.4 X____33__________
   1.1.12.5 X____3a__________
   1.1.12.6 X____2e__________

1.1.13 Check RSSI 5 times inside chamber
   1.1.13.1 X____2E__________
   1.1.13.2 X____38__________
   1.1.13.3 X____48__________
   1.1.13.4 X____48__________
   1.1.13.5 X____3C__________

1.1.14 Prepare to T-Vac – go to section 2.2

**Test Conditions**

**ATL RSSI Testing**

1.1.15 Record the name of the test satellite being used.
1.1.16 While inside ATL lab, follow procedure 1.4.
   1.1.16.1 Record power level.
   1.1.16.2 Point the antennas towards the T-Vac chamber
       0 degrees elevation is still approximately 3 (speculate that the reason is because the weight of the coaxial cable is pulling it down).
1.1.17 While still inside ATL, remove RBF pin and using interior lab antenna, check to be sure TestSat is beaconing.
       Testsat is beaconing.
1.1.18 Change beacon rate from 1 every 5 minutes to 1 every 2 minutes
   1.1.18.1 Record 5 RSSI level readings inside lab.
       1.1.18.1.1 X____0__________
1.1.19 Change beacon rate from 1 every minute.
   1.1.19.1 Record 5 RSSI level readings from inside lab – make sure all beacons are decoded
      1.1.19.1.1 X_____0F_________
      1.1.19.1.2 X_____0F_________
      1.1.19.1.3 X_____0F_________
      1.1.19.1.4 X_____0F_________
      1.1.19.1.5 X_____0E_________

T-VAC RSSI Testing

Required equipment

1.1.20 CDH/power board sandwich
1.1.21 Side boards
1.1.22 Structure-Integrated
1.1.23 Kapton tape
1.1.24 Ribbon cable and extensions

Process

1.1.25 Set up equipment in the lab to ensure all the components are working before going to the thermal vac chamber – per instruction in section 1 & 2
1.1.26 Place the TestSat on an isothermal & isolation stand

RSSI – Standard Values

1.1.27 Record initial mag field and temperature readings
1.1.28 Cycle the board thru the determined temperature range.

1.1.29 Cold Conditions
Using the thermal vacuum chamber, the satellite’s external temperatures will be brought down to -36 C, internally it will reach -20 C, the following levels will be checked for RSSI and Magnetometer characterization.

1.1.29.1.1 0 C – RSSI CHECK / MAG READ
X___28___/____________
X_2C___/____________
X_2C___/____________
X_30___/____________
X_29___/____________
X_29___/____________
X_____/____________
X_____/____________
X_____/____________
X_____/____________

1.1.29.1.2 -10 C – RSSI CHECK / MAG READ (-15 actually)
X____27____/___________
X____27____/___________
X____2c____/___________
X____2c____/___________
X____29____/___________
X__2a___/____________
X__27___/____________
X__27___/____________
X__27___/____________
X__2c___/____________

1.1.29.1.3 -20 C – RSSI CHECK / MAG READ
X__26___/____________
X__22___/____________
X__23___/____________
X__24___/____________
X__24___/____________
X__2c___/____________
X__27___/____________
X__27___/____________
X__27___/____________
X__2B___/____________
X__2A___/____________

1.1.29.1.4 -20 C – RSSI CHECK / MAG READ
X__2B_______/____________
Break-Point Receive Procedure

If Satellite is still receiving signal from ground station at -20 C perform the following two steps.

**Break Point Step 1**

Lower RF gain on groundstation until RSSI is at “break point” (HEX Recieved / 255 decimal * 3 volts = RSSI)

**Break Point Step 2**

As satellite warms monitor RSSI readings from 30 second snapshots.

**Hot Conditions - NONE**

**Data Analysis**

**Determining the relationship**

1.1.30 Plot RSSI values on the y-axis against temperature on the x-axis
1.1.31 Plot cut-off points
1.1.32 Plot sensor dump information
1.1.33 Plot Temperature on the x-axis and mag readings on the y-axis.
1.1.34 Plot using points only and then add a best-fit line to find the relationship on all graphs

**Additional Notes:**

1\textsuperscript{st} Test - Breakpoint was found to be one deg (mark) less than half RF power

2\textsuperscript{nd} Test - Breakpoint was found to be four deg (mark) less than half RF power

Plots done using data point # and not by interval #.

---

**Figure 115. RSSI Receive Sensitivity vs. Power Inputs (Use for Comparison)**
Figure 116. C&DH Temperature During Cycle #1

Figure 117. MAG Data Readings During Cycle #1
Figure 118. RSSI Values For Cycle #1
APPENDIX M. COMPONENT TEMPERATURE SENSITIVITIES

The following figures all relate to the effect of temperature on different components inside the CP3 system. These are all representative of components integrated into the majority of picosatellites.

Figure 119. Citizen Crystal Oscillator Variances

The crystals used for timing on both the CC1000 transceivers and the C&DH processors have a strong temperature dependence. As can be seen in figure 119 above and figure 120 below the HCM49’s have a +/-25 ppm frequency value at normal operating temperatures which increases to +/- 50 ppm when off the standard temperature (25°C).
For our CubeSat operating at approximately 400 MHz the normal clock frequency shift would equate to a window of around 10.4 KHz but with the added temperature uncertainty the frequency shift increases to 21Khz. This could not only cause bus affects for timing purposes but also issues on the communication system as well. The figure below shows an uncompensated crystal oscillator’s shift over temperature.
Figure 121 XO (Uncompensated) Crystal Oscillator Temperature Shift

The figure below illustrates the supply current against temperature for the DS3231 Maxim I^2C real-time clock (RTC) IC.

Figure 122. Supply Current vs. Temperature for the DS3231^{34}
The next figure illustrates the frequency deviation for the I²C RTC.

![Frequency Deviation](image)

**Figure 123. Frequency Deviation vs. Temperature and Aging Value for the DS3231[^34]**

The next figure illustrates the DS3231 temperature compensated crystal oscillator (TCXO) performance against uncompensated crystal oscillators like the Citizen HCM49.

![Delta Time and Frequency](image)
Figure 124. Delta Time and Frequency vs. Temperature for the DS3231

Figure 125. Shutdown Supply Current vs. Temperature for MAXIM 1239 ADCs

Figure 123 below illustrates the drop in conductivity for the electrolyte solution inside a Lithium Ion battery over temperature. This is a major contributor to the corresponding loss in specific energy shown in Figure 124.

Figure 126. Lithium Ion Electrolyte Conductivity Over Temperature
Figure 127. Specific Energy versus Temperature by Battery Type\textsuperscript{35}

Figure 128. Lithium Ion Anode Degradation\textsuperscript{36}
**Technical Rationale:**

Thermal cycle modeling has shown that the general form of the thermal cycling test math model is given by Equation (1).

\[
TE = F \times P_d [1 - \exp(-\lambda_0 N K^\Delta T)]
\]

(1)

Where:
- **TE** = Test Effectiveness
- **F** = Fraction of total failures that can be precipitated by a thermal cycle
- **P_d** = Probability of detection
- **\lambda_0** = Failure rate at **T_0**
- **N** = Number of thermal cycles
- **K** = A constant
- **\Delta T** = **T** - **T_0**
- **T** = Operating temperature for \(\lambda\)
- **T_0** = Operating temperature for \(\lambda_0\)

Fig. 1 shows that the failures available are the sum of three parts:

1. Failures detected by thermal cycle tests
2. Undetected failures
3. Failures not precipitated

For single temperature range of 50°C, the test effectiveness equation reduces to Equation (2).

\[
TE = 0.9 \times P_d (1 - e^{-0.0864N})
\]

(2)

Figure 2 shows a plot of Equation (2) based on a probability of detection, **P_d**, of 0.9. The equation is based on values of \(\lambda_0\) and **K** that were found by solving two simultaneous equations derived from the data base provided in Table 1.

Printed circuit boards (PCBs) are especially prone to solder joint cracking. The design is required to minimize the mechanical forces, as generated by thermal mismatch of materials or vibration, in the solder joints.
THERMAL CYCLING

\[
f = f_a F P_d (1 - e^{\lambda_0 N K A T})
\]

\[
T_E = f / f_a = F P_d (1 - e^{\lambda_0 N K A T})
\]

Fig. 1 General Form of TC Test Model

\[
T_E = 0.9 P_d (1 - e^{-0.0864N})
\]

\[
P_d = 0.9 (FOR \ LAST \ CYCLE \ TESTING)
\]

Fig. 2 Test Effectiveness Plot for \(\Delta T = 50^\circ C\)
Appendix O. Temperature Measurement on the CP Bus

This appendix is an excerpt from Chris Alan Day’s Masters Thesis “The Design of an Efficient, Elegant, and Cubic Pico-Satellite Electronics System” regarding the design of the temperature monitoring system onboard the CP satellites.

Temperature Measurement

The temperature is measured by the NHQM103B375T10 thermistor designed by Thermometrics. These thermistors are much like the thermistors successfully designed and tested on CP1. The thermistor will change resistance with temperature and the temperature is converted to a voltage by using the thermistor in a voltage divider. In Figure 26, RT1 and RT2 are the thermistors and R14 and R15 are used with the thermistors to create an analog voltage that will then be sensed by the ADC.

![Temperature vs. Thermistor Resistance Graph](image-url)
The thermistor resistance is a log function of the temperature. This makes the voltage out of the voltage divider highly non-linear. The resistance to temperature curve is shown in Figure 27. The ADC has an analog input voltage range from zero to two volts. With the ADC input voltage range and resolution known, a graph ADC code to temperature has been created. This graph is shown in figure 130.

![ADC Code vs. Thermistor Temperature](image)

The values of the 10kΩ thermistor and the 100kΩ resistor in series with it have both been chosen for a few reasons. First, the 100kΩ resistor has been chosen because it prevents the maximum current consumption through the temperature sensing circuit to be a maximum of 30uA. The actual current consumption varies
with the thermistor's resistance. Resistances considerably higher than 100kΩ cause the input leakage current of the ADC to affect the accuracy of the temperature measurement.

The 10kΩ thermistor's value is selected because it gives an appropriate range of temperature over which the ADC will receive an appropriate voltage range. The thermistor voltage divider circuit is supplied with 3V from the regulated side panel 3V line. Since the thermistor will vary the voltage divider's output voltage and the ADC has an input voltage range of 0-2V, the thermistor resistance must not cause the ADC's input voltage to reach above 2V over the operating temperature range of the satellite. In order to keep the resistor divider voltage in the correct range, the 10kΩ thermistor is selected. This thermistor resistance value give a voltage in the range that the ADC can read between temperatures of -35°C and 80°C. The 10kΩ thermistor's resistance value means that its resistance is around 10kΩ at 25°C.

From Figure 28, it is clear that the ADC codes change less frequently at higher temperatures than at low temperatures. It is desirable for ADC codes to change frequently as it gives better resolution. For example, if the temperature changed by 0.5°C per ADC code, this would provide a one half degree temperature sensing resolution. While the temperature sensing resolution is very good or adequate over most of the operating temperature range, the number of degrees per ADC code increases significantly near the upper end of the operating temperature range. For example, at 60°C, the temperature sensing resolution is
3°C per ADC code. While this is a widely spaced temperature resolution, we do not need a very precise resolution and this circuit meets our requirements.

The Thevenin equivalent resistance of the thermistor voltage divider as seen from the ADC varies widely and can affect the accuracy of the ADC reading. The ADC's input leakage current can reach ±1uA and the resistance of the voltage divider is between 58kΩ and 1.4kΩ. This range gives a worst case error of about 7 ADC codes at -30 degC and 4 ADC codes or less at -10 degC and higher temperatures.

Some other methods of converting temperature to an analog voltage are with a temperature sensing IC such as the LM20 series or with a thermocouple and thermocouple interface using an IC such as the AD594.
APPENDIX P. GeneSat-1 Telemetry and Temperature Ranges

Some CubeSats, such as NASA’s GENESAT shown below in figure 131, must have a thermal control system in order to complete their mission. GENESAT was carrying live bacteria that would quickly perish over one orbital revolution in the normal environment of a CubeSat so a small strip heater was integrated with their payload system. As a triple cube they had the power and mass available and as a NASA mission their budget also provided for the effort required to integrate a closed loop heater system.

Even with an active heater element, 4 kg for thermal inertia and an attractive 410 km circular orbit the GeneSat bus sees constant thermal cycling.

Figure 131. GeneSat-1 Temperature History (External)
<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
<th>Type</th>
<th>Plant</th>
<th>Date</th>
<th>Sensor Location</th>
<th>Curve Type</th>
<th>Formula</th>
<th>m</th>
<th>b</th>
<th>Comments</th>
</tr>
</thead>
</table>
| Temp1 | Terra Solar Panel 1 | ASH, BR | Deg C | 16 | Center, Mounting Plates | line | $T = T_{0} + \frac{1}{1.16875}$ | 0.0602 | 1.16875 | $X_{INT} = 0$; Curve range $-1.62$ to $+4.25 C$
| Temp2 | Terra Solar Panel 2 | ASH, BR | Deg C | 16 | Center, Mounting Plates | line | $T = T_{0} + \frac{1}{1.22}$ | 0.0602 | 1.22 | $X_{INT} = 0$; Curve range $-1.62$ to $+4.25 C$
| Temp3 | Terra Solar Panel 3 | ASH, BR | Deg C | 16 | Center, Mounting Plates | line | $T = T_{0} + \frac{1}{1.22}$ | 0.0602 | 1.22 | $X_{INT} = 0$; Curve range $-1.62$ to $+4.25 C$
| Solar1 | Solar Panel 1 | Current | mA | | | line | $I = I_{0} + 0.0688$ | 0.0688 | 0 | $X_{INT} = 0$; Curve range $-0.33$ to $+3.33 A$
| Solar2 | Solar Panel 2 | Current | mA | | | line | $I = I_{0} + 0.0688$ | 0.0688 | 0 | $X_{INT} = 0$; Curve range $-0.33$ to $+3.33 A$
| Solar3 | Solar Panel 3 | Current | mA | | | line | $I = I_{0} + 0.0688$ | 0.0688 | 0 | $X_{INT} = 0$; Curve range $-0.33$ to $+3.33 A$
| Solar4 | Solar Panel 4 | Current | mA | | | line | $I = I_{0} + 0.0688$ | 0.0688 | 0 | $X_{INT} = 0$; Curve range $-0.33$ to $+3.33 A$
| Pro | Pull-up Resistor | Current | mA | | | line | $R = R_{0} + 0.0479$ | 0.0479 | 0 | $X_{INT} = 0$; Curve range $-1.51$ to $+1.51 A$
| Panel | Multi-Contact Voltage | Voltage | V | | | line | $V = V_{0} + 0.012$ | 0.012 | 0 | $X_{INT} = 0$; Curve range $-10$ to $+10 V$
| Rstd | Radiation Detector | Radiation | µSv | 6 | 656 CT average | line | $R_{d} = R_{0} + 0.0012$ | 0.0012 | 0 | $X_{INT} = 0$; Curve range $-10$ to $+10 V$
| Fe | Iron | | | | | line | $T = T_{0} + 0$ | 0 | 0 | $X_{INT} = 0$; Curve range $-10$ to $+10 V$

**Figure 132. GeneSat-1 Beacon Data Including Thermal Ranges**