**FEATURES**

- 950nA Input Quiescent Current (Output in Regulation – No Load)
- 450nA Input Quiescent Current in UVLO
- 2.7V to 20V Input Operating Range
- Integrated Low-Loss Full-Wave Bridge Rectifier
- Up to 100mA of Output Current
- Selectable Output Voltages of 1.8V, 2.5V, 3.3V, 3.6V
- High Efficiency Integrated Hysteretic Buck DC/DC
- Input Protective Shunt – Up to 25mA Pull-Down at VIN ≥ 20V
- Wide Input Undervoltage Lockout (UVLO) Range
- Available in 10-Lead MSE and 3mm × 3mm DFN Packages

**APPLICATIONS**

- Piezoelectric Energy Harvesting
- Electro-Mechanical Energy Harvesting
- Wireless HVAC Sensors
- Mobile Asset Tracking
- Tire Pressure Sensors
- Battery Replacement for Industrial Sensors
- Remote Light Switches
- Standalone Nanopower Buck Regulator

**DESCRIPTION**

The LTC®3588-1 integrates a low-loss full-wave bridge rectifier with a high efficiency buck converter to form a complete energy harvesting solution optimized for high output impedance energy sources such as piezoelectric transducers. An ultralow quiescent current undervoltage lockout (UVLO) mode with a wide hysteresis window allows charge to accumulate on an input capacitor until the buck converter can efficiently transfer a portion of the stored charge to the output. In regulation, the LTC3588-1 enters a sleep state in which both input and output quiescent currents are minimal. The buck converter turns on and off as needed to maintain regulation.

Four output voltages, 1.8V, 2.5V, 3.3V and 3.6V, are pin selectable with up to 100mA of continuous output current; however, the output capacitor may be sized to service a higher output current burst. An input protective shunt set at 20V enables greater energy storage for a given amount of input capacitance.

**TYPICAL APPLICATION**

100mA Piezoelectric Energy Harvesting Power Supply

**LTC3588-1 3.3V Regulator Start-Up Profile**

![Diagram of 100mA Piezoelectric Energy Harvesting Power Supply](image)

![Graph of LTC3588-1 3.3V Regulator Start-Up Profile](graph)
### LTC3588-1

#### ABSOLUTE MAXIMUM RATINGS (Note 1)

**V\textsubscript{IN}**  
- Low Impedance Source ....................... –0.3V to 18V*  
- Current Fed, I\textsubscript{SW} = 0A ...................................... 25mA†  
- PZ1, PZ2 ........................................................... 0V to V\textsubscript{IN}  
- D0, D1 .............. –0.3V to [Lesser of (V\textsubscript{IN2} + 0.3V) or 6V]  
- CAP ...................... [Higher of –0.3V or (V\textsubscript{IN} – 6V)] to V\textsubscript{IN}  
- V\textsubscript{IN2} ............ –0.3V to [Lesser of (V\textsubscript{IN} + 0.3V) or 6V]

**V\textsubscript{OUT}**  
- –0.3V to Lesser of (V\textsubscript{IN2} + 0.3V) or 6V  
- PGOOD ............... –0.3V to Lesser of (V\textsubscript{OUT} + 0.3V) or 6V  
- IPZ1, IPZ2 .............................................................. ±50mA  
- ISW ....................................................................... 350mA

* V\textsubscript{IN} has an internal 20V clamp  
† For t < 1ms and Duty Cycle < 1%,  
Absolute Maximum Continuous Current = 5mA

#### Operating Junction Temperature Range
(Notes 2, 3) ................................................ –40 to 125°C

#### Storage Temperature Range  
–65 to 125°C

#### Lead Temperature (Soldering, 10 sec)  
MSE Only .......................................................... 300°C

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#### PIN CONFIGURATION

**TOP VIEW**

- PZ1  
- PZ2  
- CAP  
- V\textsubscript{IN}  
- SW  
- D0  
- D1  
- VIN2  
- CAP  
- VIN  
- SW  
- PGOOD  
- DO PACKAGE  
10-LEAD (3mm × 3mm) PLASTIC DFN  
T\textsubscript{J\textsubscript{MAX}} = 125°C, \( \theta J = 43^\circ\text{C/W}, \theta JC = 7.5^\circ\text{C/W} \)  
EXPOSED PAD (PIN 11) IS GND, MUST BE SOLDERED TO PCB

---

#### ORDER INFORMATION

<table>
<thead>
<tr>
<th>LEAD FREE FINISH</th>
<th>TAPE AND REEL</th>
<th>PART MARKING*</th>
<th>PACKAGE DESCRIPTION</th>
<th>TEMPERATURE RANGE</th>
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<tbody>
<tr>
<td>LTC3588EDD-1#PBF</td>
<td>LTC3588EDD-1#TRPBF</td>
<td>LFKY</td>
<td>10-Lead (3mm × 3mm) Plastic DFN</td>
<td>–40°C to 85°C</td>
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<tr>
<td>LTC3588IDD-1#PBF</td>
<td>LTC3588IDD-1#TRPBF</td>
<td>LFKY</td>
<td>10-Lead (3mm × 3mm) Plastic DFN</td>
<td>–40°C to 125°C</td>
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<tr>
<td>LTC3588EMSE-1#PBF</td>
<td>LTC3588EMSE-1#TRPBF</td>
<td>LFKY</td>
<td>10-Lead Plastic MSOP</td>
<td>–40°C to 85°C</td>
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<tr>
<td>LTC3588IMSE-1#PBF</td>
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<td>LFKY</td>
<td>10-Lead Plastic MSOP</td>
<td>–40°C to 125°C</td>
</tr>
</tbody>
</table>

Consult LTC Marketing for parts specified with wider operating temperature ranges.  
*The temperature grade is identified by a label on the shipping container.

For more information on lead free part marking, go to: [http://www.linear.com/leadfree/](http://www.linear.com/leadfree/)  
This product is only offered in trays. For more information go to: [http://www.linear.com/packaging/](http://www.linear.com/packaging/)
ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating junction temperature range, otherwise specifications are at $T_J = 25^\circ C$. $V_{IN} = 5.5V$ unless otherwise specified.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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</thead>
<tbody>
<tr>
<td>$V_{IN}$</td>
<td>Input Voltage Range</td>
<td>Low Impedance Source on $V_{IN}$</td>
<td>●</td>
<td>18.0</td>
<td>V</td>
<td></td>
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<tr>
<td>$I_{VIN}$</td>
<td>$V_{IN}$ Quiescent Current</td>
<td>$V_{IN} = 2.5V$, Not PGOOD</td>
<td>450</td>
<td>700</td>
<td>nA</td>
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<td></td>
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<td>Buck Enabled, Sleeping</td>
<td>950</td>
<td>1500</td>
<td>nA</td>
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<td>Buck Enabled, Not Sleeping</td>
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<td>250</td>
<td>μA</td>
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<td>$V_{UVLO}$</td>
<td>$V_{IN}$ Undervoltage Lockout Threshold</td>
<td>$V_{IN}$ Rising</td>
<td>1.8V Output Selected; $D1 = 0$, $D0 = 0$</td>
<td>3.77</td>
<td>4.04</td>
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<td>2.5V Output Selected; $D1 = 0$, $D0 = 1$</td>
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<td>3.6V Output Selected; $D1 = 1$, $D0 = 1$</td>
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<tr>
<td>$V_{SHUNT}$</td>
<td>$V_{SHUNT}$ Shunt Regulator Voltage</td>
<td>$I_{VIN} = 1mA$</td>
<td>19.0</td>
<td>20.0</td>
<td>21.0</td>
<td>V</td>
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<td>$I_{SHUNT}$</td>
<td>Maximum Protective Shunt Current</td>
<td>1ms Duration</td>
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<td>mA</td>
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<td>Internal Bridge Rectifier Loss ($</td>
<td>V_{PZ1} - V_{PZ2}</td>
<td>- V_{IN}$)</td>
<td>$I_{BRIDGE} = 10μA$</td>
<td>350</td>
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<td>Internal Bridge Rectifier Reverse Leakage Current</td>
<td>$V_{REVERSE} = 18V$</td>
<td>20</td>
<td>nA</td>
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<td>Internal Bridge Rectifier Reverse Breakdown Voltage</td>
<td>$I_{REVERSE} = 1μA$</td>
<td>$V_{SHUNT}$</td>
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<td>V</td>
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<td>$V_{OUT}$</td>
<td>Regulated Output Voltage</td>
<td>1.8V Output Selected</td>
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<td>1.788</td>
<td>1.890</td>
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<td>Sleep Threshold</td>
<td>1.170</td>
<td>1.188</td>
<td>1.200</td>
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<td>2.488</td>
<td>2.550</td>
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<td>2.5V Output Selected</td>
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<td>Sleep Threshold</td>
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<td>3.288</td>
<td>3.370</td>
<td>V</td>
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<td>Wake-Up Threshold</td>
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<td>3.399</td>
<td>3.480</td>
<td>V</td>
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<td>3.612</td>
<td>3.708</td>
<td>3.800</td>
<td>V</td>
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<td>Sleep Threshold</td>
<td>3.492</td>
<td>3.588</td>
<td>3.680</td>
<td>V</td>
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<td>Wake-Up Threshold</td>
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<td>3.08</td>
<td>3.30</td>
<td>V</td>
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<td>3.6V Output Selected</td>
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<td>Wake-Up Threshold</td>
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<td>4.02</td>
<td>4.28</td>
<td>V</td>
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<td>PGOOD Falling Threshold</td>
<td>As a Percentage of the Selected $V_{OUT}$</td>
<td>83</td>
<td>92</td>
<td>%</td>
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<tr>
<td>$I_{VOUT}$</td>
<td>Output Quiescent Current</td>
<td>$V_{OUT} = 3.6V$</td>
<td>89</td>
<td>150</td>
<td>nA</td>
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<td>$I_{PEAK}$</td>
<td>Buck Peak Switch Current</td>
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<td>200</td>
<td>260</td>
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<td>mA</td>
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<td>$I_{LOAD}$</td>
<td>Available Buck Output Current</td>
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<td>100</td>
<td>mA</td>
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<td>$R_P$</td>
<td>Buck PMOS Switch On-Resistance</td>
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<td>1.1</td>
<td>Ω</td>
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<td>$R_N$</td>
<td>Buck NMOS Switch On-Resistance</td>
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<td>1.3</td>
<td>Ω</td>
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<td>Max Buck Duty Cycle</td>
<td>100</td>
<td>%</td>
<td></td>
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<tr>
<td>$V_{IH(D0, D1)}$</td>
<td>$D0/D1$ Input High Voltage</td>
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<td>1.2</td>
<td>V</td>
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<td>$V_{IL(D0, D1)}$</td>
<td>$D0/D1$ Input Low Voltage</td>
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<td>0.4</td>
<td>V</td>
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<td>$I_{IH(D0, D1)}$</td>
<td>$D0/D1$ Input High Current</td>
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<td>10</td>
<td>nA</td>
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<td></td>
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<tr>
<td>$I_{IL(D0, D1)}$</td>
<td>$D0/D1$ Input Low Current</td>
<td></td>
<td>10</td>
<td>nA</td>
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</table>
**ELECTRICAL CHARACTERISTICS**

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The LTC3588E-1 is guaranteed to meet performance specifications from 0°C to 85°C. Specifications over the −40°C to 85°C operating junction temperature range are assured by design, characterization, and correlation with statistical process controls. The LTC3588I-1 is guaranteed over the full −40°C to 125°C operating junction temperature range.

**Note 3:** 
Note that the maximum ambient temperature is determined by specific operating conditions in conjunction with board layout, the rated package thermal resistance and other environmental factors.

**Note 4:** Dynamic supply current is higher due to gate charge being  

---

**TYPICAL PERFORMANCE CHARACTERISTICS**

**IVIN in UVLO vs VIN**

- **UVLO Rising vs Temperature:**
  - $D_1 = D_0 = 1$
  - $D_1 = 1, D_0 = 0$
  - $D_1 = D_0 = 0$

- **UVLO Falling vs Temperature:**
  - $D_1 = D_0 = 1$
  - $D_1 = D_0 = 0$

- **VSHUNT vs Temperature:**
  - $I_{SHUNT} = 25\text{mA}$
  - $I_{SHUNT} = 1\text{mA}$

- **Total Bridge Rectifier Drop vs Bridge Current:**
  - $V_{PZ1} - V_{PZ2} - V_{VIN}$

---

**MISCELLANEOUS**

**Note 1:**

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The LTC3588E-1 is guaranteed to meet performance specifications from 0°C to 85°C. Specifications over the −40°C to 85°C operating junction temperature range are assured by design, characterization, and correlation with statistical process controls. The LTC3588I-1 is guaranteed over the full −40°C to 125°C operating junction temperature range.

**Note 3:** 
Note that the maximum ambient temperature is determined by specific operating conditions in conjunction with board layout, the rated package thermal resistance and other environmental factors.

**Note 4:** Dynamic supply current is higher due to gate charge being...
TYPICAL PERFORMANCE CHARACTERISTICS

I<sub>PEAK</sub> vs Temperature

R<sub>DS(ON)</sub> of PMOS/NMOS vs Temperature

Efficiency vs I<sub>LOAD</sub>, L = 10μH

Efficiency vs V<sub>IN</sub> for I<sub>LOAD</sub> = 100mA, L = 10μH

Efficiency vs V<sub>IN</sub> for I<sub>LOAD</sub> = 100mA, L = 100μH

Efficiency vs V<sub>IN</sub> for V<sub>OUT</sub> = 3.3V, L = 10μH

Efficiency vs V<sub>IN</sub> for V<sub>OUT</sub> = 3.3V, L = 100μH

Efficiency vs V<sub>IN</sub> for V<sub>OUT</sub> = 3.6V, L = 10μH

Efficiency vs V<sub>IN</sub> for V<sub>OUT</sub> = 3.6V, L = 100μH

Efficiency vs V<sub>IN</sub> for V<sub>OUT</sub> = 3.3V, L = 100μH

I<sub>PEAK</sub> vs Temperature

R<sub>DS(ON)</sub> vs Temperature

Operating Waveforms

Vin = 5V, V<sub>OUT</sub> = 3.3V
I<sub>LOAD</sub> = 1mA, L = 10μH, C<sub>OUT</sub> = 47μF

Vin = 5V, V<sub>OUT</sub> = 3.3V
I<sub>LOAD</sub> = 100mA, L = 10μH, C<sub>OUT</sub> = 47μF

Vin = 5V, V<sub>OUT</sub> = 3.3V
I<sub>LOAD</sub> = 100mA, L = 100μH, C<sub>OUT</sub> = 47μF
PIN FUNCTIONS

PZ1 (Pin 1): Input connection for piezoelectric element or other AC source (used in conjunction with PZ2).

PZ2 (Pin 2): Input connection for piezoelectric element or other AC source (used in conjunction with PZ1).

CAP (Pin 3): Internal rail referenced to VIN to serve as gate drive for buck PMOS switch. A 1μF capacitor should be connected between CAP and VIN. This pin is not intended for use as an external system rail.

VIN (Pin 4): Rectified Input Voltage. A capacitor on this pin serves as an energy reservoir and input supply for the buck regulator. The VIN voltage is internally clamped to a maximum of 20V (typical).

SW (Pin 5): Switch Pin for the Buck Switching Regulator. A 10μH or larger inductor should be connected from SW to VOUT.

VOUT (Pin 6): Sense pin used to monitor the output voltage and adjust it through internal feedback.

VIN2 (Pin 7): Internal low voltage rail to serve as gate drive for buck NMOS switch. Also serves as a logic high rail for output voltage select bits D0 and D1. A 4.7μF capacitor should be connected from VIN2 to GND. This pin is not intended for use as an external system rail.

D1 (Pin 8): Output Voltage Select Bit. D1 should be tied high to VIN2 or low to GND to select desired VOUT (see Table 1).

D0 (Pin 9): Output Voltage Select Bit. D0 should be tied high to VIN2 or low to GND to select desired VOUT (see Table 1).

PGOOD (Pin 10): Power good output is logic high when VOUT is above 92% of the target value. The logic high is referenced to the VOUT rail.

GND (Exposed Pad Pin 11): Ground. The Exposed Pad should be connected to a continuous ground plane on the second layer of the printed circuit board by several vias directly under the LTC3588-1.
OPERATION

The LTC3588-1 is an ultralow quiescent current power supply designed specifically for energy harvesting and/or low current step-down applications. The part is designed to interface directly to a piezoelectric or alternative A/C power source, rectify a voltage waveform and store harvested energy on an external capacitor, bleed off any excess power via an internal shunt regulator, and maintain a regulated output voltage by means of a nanopower high efficiency synchronous buck regulator.

Internal Bridge Rectifier

The LTC3588-1 has an internal full-wave bridge rectifier accessible via the differential PZ1 and PZ2 inputs that rectifies AC inputs such as those from a piezoelectric element. The rectified output is stored on a capacitor at the VIN pin and can be used as an energy reservoir for the buck converter. The low-loss bridge rectifier has a total drop of about 400mV with typical piezo generated currents (~10μA). The bridge is capable of carrying up to 50mA. One side of the bridge can be operated as a single-ended DC input. PZ1 and PZ2 should never be shorted together when the bridge is in use.

Undervoltage Lockout (UVLO)

When the voltage on VIN rises above the UVLO rising threshold the buck converter is enabled and charge is transferred from the input capacitor to the output capacitor. A wide (~1V) UVLO hysteresis window is employed with a lower threshold approximately 300mV above the selected regulated output voltage to prevent short cycling during buck power-up. When the input capacitor voltage is depleted below the UVLO falling threshold the buck converter is disabled. Extremely low quiescent current (450nA typical) in UVLO allows energy to accumulate on the input capacitor in situations where energy must be harvested from low power sources.

Internal Rail Generation

Two internal rails, CAP and VIN2, are generated from VIN and are used to drive the high side PMOS and low side NMOS of the buck converter, respectively. Additionally the VIN2 rail serves as logic high for output voltage select bits D0 and D1. The VIN2 rail is regulated at 4.8V above GND while the CAP rail is regulated at 4.8V below VIN2. These are not intended to be used as external rails. Bypass capacitors are connected to the CAP and VIN2 pins to serve as energy reservoirs for driving the buck switches. When VIN is below 4.8V, VIN2 is equal to VIN and CAP is held at GND. Figure 1 shows the ideal VIN, VIN2 and CAP relationship.

Buck Operation

The buck regulator uses a hysteretic voltage algorithm to control the output through internal feedback from the VOUT sense pin. The buck converter charges an output capacitor through an inductor to a value slightly higher than the regulation point. It does this by ramping the inductor current up to 260mA through an internal PMOS switch and then ramping it down to 0mA through an internal NMOS switch. This efficiently delivers energy to the output capacitor. The ramp rate is determined by VIN, VOUT, and the inductor value. If the input voltage falls below the
UVLO falling threshold before the output voltage reaches regulation, the buck converter will shut off and will not be turned on until the input voltage again rises above the UVLO rising threshold. During this time the output voltage will be loaded by less than 100nA. When the buck brings the output voltage into regulation the converter enters a low quiescent current sleep state that monitors the output voltage with a sleep comparator. During this operating mode load current is provided by the buck output capacitor. When the output voltage falls below the regulation point the buck regulator wakes up and the cycle repeats. This hysteretic method of providing a regulated output reduces losses associated with FET switching and maintains an output at light loads. The buck delivers a minimum of 100mA of average load current when it is switching.

When the sleep comparator signals that the output has reached the sleep threshold the buck converter may be in the middle of a cycle with current still flowing through the inductor. Normally both synchronous switches would turn off and the current in the inductor would freewheel to zero through the NMOS body diode. The LTC3588-1 keeps the NMOS switch on during this time to prevent the conduction loss that would occur in the diode if the NMOS were off. If the PMOS is on when the sleep comparator trips the NMOS will turn on immediately in order to ramp down the current. If the NMOS is on it will be kept on until the current reaches zero.

Though the quiescent current when the buck is switching is much greater than the sleep quiescent current, it is still a small percentage of the average inductor current which results in high efficiency over most load conditions. The buck operates only when sufficient energy has been accumulated in the input capacitor and the length of time the converter needs to transfer energy to the output is much less than the time it takes to accumulate energy. Thus, the buck operating quiescent current is averaged over a long period of time so that the total average quiescent current is low. This feature accommodates sources that harvest small amounts of ambient energy.

Four selectable voltages are available by tying the output select bits, D0 and D1, to GND or V_{IN2}. Table 1 shows the four D0/D1 codes and their corresponding output voltages.

<table>
<thead>
<tr>
<th>D1</th>
<th>D0</th>
<th>V_{OUT}</th>
<th>V_{OUT QUIESCENT CURRENT (I_{VOUT})}</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1.8V</td>
<td>44nA</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>2.5V</td>
<td>62nA</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>3.3V</td>
<td>81nA</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>3.6V</td>
<td>89nA</td>
</tr>
</tbody>
</table>

The internal feedback network draws a small amount of current from V_{OUT} as listed in Table 1.

### Power Good Comparator

A power good comparator produces a logic high referenced to V_{OUT} on the PGOOD pin the first time the converter reaches the sleep threshold of the programmed V_{OUT}, signaling that the output is in regulation. The PGOOD pin will remain high until V_{OUT} falls to 92% of the desired regulation voltage. Several sleep cycles may occur during this time. Additionally, if PGOOD is high and V_{IN} falls below the UVLO falling threshold, PGOOD will remain high until V_{OUT} falls to 92% of the desired regulation point. This allows output energy to be used even if the input is lost. Figure 2 shows the behavior for V_{OUT} = 3.6V and no load. At t = 75s V_{IN} becomes high impedance and is discharged by the quiescent current of the LTC3588-1 and through servicing V_{OUT} which is discharged by its own leakage current. V_{IN} crosses UVLO falling but PGOOD remains high until V_{OUT} decreases to 92% of the desired regulation point. The PGOOD pin is designed to drive a microprocessor or other chip I/O and is not intended to drive higher current loads such as an LED.
**OPERATION**

The D0/D1 inputs can be switched while in regulation as shown in Figure 3. If V_{OUT} is programmed to a voltage with a PGOOD falling threshold above the old V_{OUT}, PGOOD will transition low until the new regulation point is reached. When V_{OUT} is programmed to a lower voltage, PGOOD will remain high through the transition.

**Energy Storage**

Harvested energy can be stored on the input capacitor or the output capacitor. The wide input range takes advantage of the fact that energy storage on a capacitor is proportional to the square of the capacitor voltage. After the output voltage is brought into regulation any excess energy is stored on the input capacitor and its voltage increases. When a load exists at the output the buck can efficiently transfer energy stored at a high voltage to the regulated output. While energy storage at the input utilizes the high voltage at the input, the load current is limited to what the buck converter can supply. If larger loads need to be serviced the output capacitor can be sized to support a larger current for some duration. For example, a current burst could begin when PGOOD goes high and would continuously deplete the output capacitor until PGOOD went low.

*Figure 3. PGOOD Operation During D0/D1 Transition*
**APPLICATIONS INFORMATION**

**Introduction**

The LTC3588-1 harvests ambient vibrational energy through a piezoelectric element in its primary application. Common piezoelectric elements are PZT (lead zirconate titanate) ceramics, PVDF (polyvinylidene fluoride) polymers, or other composites. Ceramic piezoelectric elements exhibit a piezoelectric effect when the crystal structure of the ceramic is compressed and internal dipole movement produces a voltage. Polymer elements comprised of long-chain molecules produce a voltage when flexed as molecules repel each other. Ceramics are often used under direct pressure while a polymer can be flexed more readily. A wide range of piezoelectric elements are available and produce a variety of open-circuit voltages and short-circuit currents. Typically the open-circuit voltage and short-circuit currents increase with available vibrational energy as shown in Figure 4. Piezoelectric elements can be placed in series or in parallel to achieve desired open-circuit voltages.

The LTC3588-1 is well-suited to a piezoelectric energy harvesting application. The 20V input protective shunt can accommodate a variety of piezoelectric elements. The low quiescent current of the LTC3588-1 enables efficient energy accumulation from piezoelectric elements which can have short-circuit currents on the order of tens of microamps. Piezoelectric elements can be obtained from manufacturers listed in Table 2.

<table>
<thead>
<tr>
<th>Table 2. Piezoelectric Element Manufacturers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Advanced Cerametrics</td>
</tr>
<tr>
<td>Piezo Systems</td>
</tr>
<tr>
<td>Measurement Specialties</td>
</tr>
<tr>
<td>PI (Physik Instrumente)</td>
</tr>
<tr>
<td>MIDE Technology Corporation</td>
</tr>
<tr>
<td>Morgan Technical Ceramics</td>
</tr>
</tbody>
</table>

The LTC3588-1 will gather energy and convert it to a usable output voltage to power microprocessors, wireless sensors, and wireless transmission components. Such a wireless sensor application may require much more peak power than a piezoelectric element can produce. However, the LTC3588-1 accumulates energy over a long period of time to enable efficient use for short power bursts. For continuous operation, these bursts must occur with a low duty cycle such that the total output energy during the burst does not exceed the average source power integrated over an energy accumulation cycle. For piezoelectric inputs the time between cycles could be minutes, hours, or longer depending on the selected capacitor values and the nature of the vibration source.
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PGOOD Signal

The PGOOD signal can be used to enable a sleeping microprocessor or other circuitry when V_OUT reaches regulation, as shown in Figure 5. Typically V_IN will be somewhere between the UVLO thresholds at this time and a load could only be supported by the output capacitor. Alternatively, waiting a period of time after PGOOD goes high would let the input capacitor accumulate more energy allowing load current to be maintained longer as the buck efficiently transfers that energy to the output. While active, a microprocessor may draw a small load when operating sensors, and then draw a large load to transmit data. Figure 5 shows the LTC3588-1 responding smoothly to such a load step.

Input and Output Capacitor Selection

The input and output capacitors should be selected based on the energy needs and load requirements of the application. In every case the V_IN capacitor should be rated to withstand the highest voltage ever present at V_IN. For 100mA or smaller loads, storing energy at the input takes advantage of the high voltage input since the buck can deliver 100mA average load current efficiently to the output. The input capacitor should then be sized to store enough energy to provide output power for the length of time required. This may involve using a large capacitor, letting V_IN charge to a high voltage, or both. Enough energy should be stored on the input so that the buck does not reach the UVLO falling threshold which would halt energy transfer to the output. In general:

\[ P_{LOAD} t_{LOAD} = \frac{1}{2} \eta C_{IN} (V_{IN}^2 - V_{UVLOFALLING}^2) \]

\[ V_{UVLOFALLING} \leq V_{IN} \leq V_{SHUNT} \]

The above equation can be used to size the input capacitor to meet the power requirements of the output for the desired duration. Here \( \eta \) is the average efficiency of the buck converter over the input range and V_IN is the input voltage when the buck begins to switch. This equation may overestimate the input capacitor necessary since load current can deplete the output capacitor all the way to the lower PGOOD threshold. It also assumes that the input source charging has a negligible effect during this time.

The duration for which the regulator sleeps depends on the load current and the size of the output capacitor. The sleep time decreases as the load current increases and/or as the output capacitor decreases. The DC sleep hysteresis window is ±12mV around the programmed output voltage. Ideally this means that the sleep time is determined by the following equation:

\[ t_{SLEEP} = C_{OUT} \frac{24mV}{I_{LOAD}} \]
APPLICATIONS INFORMATION

This is true for output capacitors on the order of 100μF or larger, but as the output capacitor decreases towards 10μF delays in the internal sleep comparator along with the load current may result in the VOUT voltage slewing past the ±12mV thresholds. This will lengthen the sleep time and increase VOUT ripple. A capacitor less than 10μF is not recommended as VOUT ripple could increase to an undesirable level.

If transient load currents above 100mA are required then a larger capacitor can be used at the output. This capacitor will be continuously discharged during a load condition and the capacitor can be sized for an acceptable drop in VOUT:

\[
C_{\text{OUT}} = \frac{(V_{\text{OUT+}} - V_{\text{OUT-}})I_{\text{LOAD}} - I_{\text{BUCK}}t_{\text{LOAD}}}{t_{\text{LOAD}}}
\]

Here \( V_{\text{OUT+}} \) is the value of VOUT when PGOOD goes high and \( V_{\text{OUT-}} \) is the desired lower limit of VOUT. \( I_{\text{BUCK}} \) is the average current being delivered from the buck converter, typically \( I_{\text{PEAK}}/2 \).

A standard surface mount ceramic capacitor can be used for \( C_{\text{OUT}} \), though some applications may be better suited to a low leakage aluminum electrolytic capacitor or a supercapacitor. These capacitors can be obtained from manufacturers such as Vishay, Illinois Capacitor, AVX, or CAP-XX.

**Inductor**

The buck is optimized to work with an inductor in the range of 10μH to 22μH, although inductor values outside this range may yield benefits in some applications. For typical applications, a value of 10μH is recommended. A larger inductor will benefit high voltage applications by increasing the on-time of the PMOS switch and improving efficiency by reducing gate charge loss. Choose an inductor with a DC current rating greater than 350mA. The DCR of the inductor can have an impact on efficiency as it is a source of loss. Tradeoffs between price, size, and DCR should be evaluated. Table 3 lists several inductors that work well with the LTC3588-1.

**Table 3. Recommended Inductors for LTC3588-1**

<table>
<thead>
<tr>
<th>INDUCTOR TYPE</th>
<th>L (μH)</th>
<th>MAX I_{\text{DC}} (mA)</th>
<th>MAX DCR (Ω)</th>
<th>SIZE in mm (L × W × H)</th>
<th>MANUFACTURER</th>
</tr>
</thead>
<tbody>
<tr>
<td>CDRH2D18/LDNP</td>
<td>10</td>
<td>430</td>
<td>0.180</td>
<td>3 × 3 × 2</td>
<td>Sumida</td>
</tr>
<tr>
<td>107AS-100M</td>
<td>10</td>
<td>650</td>
<td>0.145</td>
<td>2.8 × 3 × 1.8</td>
<td>Toko</td>
</tr>
<tr>
<td>EPL3015-103ML</td>
<td>10</td>
<td>350</td>
<td>0.301</td>
<td>2.8 × 3 × 1.5</td>
<td>Coilcraft</td>
</tr>
<tr>
<td>MLP3225s100L</td>
<td>10</td>
<td>1000</td>
<td>0.130</td>
<td>3.2 × 2.5 × 1.0</td>
<td>TDK</td>
</tr>
<tr>
<td>XLP2010-163ML</td>
<td>10</td>
<td>490</td>
<td>0.611</td>
<td>2.0 × 1.9 × 1.0</td>
<td>Coilcraft</td>
</tr>
<tr>
<td>SLF7045T</td>
<td>100</td>
<td>500</td>
<td>0.250</td>
<td>7.0 × 7.0 × 4.5</td>
<td>TDK</td>
</tr>
</tbody>
</table>

**VIN2 and CAP Capacitors**

A 1μF capacitor should be connected between VIN and CAP and a 4.7μF capacitor should be connected between VIN2 and GND. These capacitors hold up the internal rails during buck switching and compensate the internal rail generation circuits. In applications where the input source is limited to less than 6V, the CAP pin can be tied to GND and the VIN2 pin can be tied to VIN as shown in Figure 6. An optional 5.6V Zener diode can be connected to VIN to clamp VIN in this scenario. The leakage of the Zener diode below its Zener voltage should be considered as it may be comparable to the quiescent current of the LTC3588-1. This circuit does not require the capacitors on VIN2 and CAP, saving components and allowing a lower voltage rating for the single VIN capacitor.

**Figure 6. Smallest Solution Size 1.8V Low Voltage Input Piezoelectric Power Supply**
APPLICATIONS INFORMATION

Additional Applications with Piezo Inputs

The versatile LTC3588-1 can be used in a variety of configurations. Figure 7 shows a single piezo source powering two LTC3588-1s simultaneously, providing capability for multiple rail systems. This setup features automatic supply sequencing as the LTC3588-1 with the lower voltage output (i.e. lower UVLO rising threshold) will come up first. As the piezo provides input power both $V_{IN}$ rails will initially come up together, but when one output starts drawing power, only its corresponding $V_{IN}$ will fall as the bridges of each LTC3588-1 provide isolation. Input piezo energy will then be directed to this lower voltage capacitor until both $V_{IN}$ rails are again equal. This configuration is expandable to any number of LTC3588-1s powered by a single piezo as long as the piezo can support the sum total of the quiescent currents from each LTC3588-1.

A piezo powered LTC3588-1 can also be used in concert with a battery connected to $V_{IN}$ to supplement the system if ambient vibrational energy ceases as shown in Figure 8. A blocking diode placed in series with the battery to $V_{IN}$ prevents reverse current in the battery if the piezo source charges $V_{IN}$ past the battery voltage. A 9V battery is shown, but any stack of batteries of a given chemistry can be used as long as the battery stack voltage does not exceed 18V. In this setup the presence of the piezo energy harvester can greatly increase the life of the battery. If the piezo source is removed the LTC3588-1 can serve as a standalone nanopower buck converter. In this case the bridge is unused and the blocking diode is unnecessary.

Figure 7. Dual Rail Power Supply with Single Piezo and Automatic Supply Sequencing

Figure 8. Piezo Energy Harvester with Battery Backup
APPLICATIONS INFORMATION

DANGER! HIGH VOLTAGE!

DANGEROUS AND LETHAL POTENTIALS ARE PRESENT IN OFFLINE CIRCUITS!
BEFORE PROCEEDING ANY FURTHER, THE READER IS WarnED THAT
CAUTION MUST BE USED IN THE CONSTRUCTION, TESTING AND USE OF
OFFLINE CIRCUITS. EXTREME CAUTION Must BE USED IN WORKING WITH
AND MAKING CONNECTIONS TO THESE CIRCUITS. REPEAT: OFFLINE
CIRCUITS CONTAIN DANGEROUS, AC LINE-CONNECTED HIGH VOLTAGE
POTENTIALS. USE CAUTION. ALL TESTING PERFORMED ON AN OFFLINE
CIRCUIT MUST BE DONE WITH AN ISOLATION TRANSFORMER CONNECTED
BETWEEN THE OFFLINE CIRCUIT’S INPUT AND THE AC LINE. USERS AND
CONSTRUCTORS OF OFFLINE CIRCUITS MUST OBSERVE THIS PRECAUTION
WHEN CONNECTING TEST EQUIPMENT TO THE CIRCUIT TO AVOID ELECTRIC
SHOCK. REPEAT: AN ISOLATION TRANSFORMER MUST BE CONNECTED
BETWEEN THE CIRCUIT INPUT AND THE AC LINE IF ANY TEST EQUIPMENT IS
TO BE CONNECTED.

Alternate Power Sources

The LTC3588-1 is not limited to use with piezoelectric elements but can accommodate a wide variety of input sources depending on the type of ambient energy available. Figure 9 shows the LTC3588-1 internal bridge rectifier connected to the AC line in series with four 150k current limiting resistors. This is a high voltage application and minimum spacing between the line, neutral, and any high voltage components should be maintained per the applicable UL specification. For general off-line applications refer to UL regulation 1012.

Figure 10 shows an application where copper panels are placed near a standard fluorescent room light to capacitively harvest energy from the electric field around the light. The frequency of the emission will be 120Hz for magnetic ballasts but could be higher if the light uses electronic ballast. The LTC3588-1 bridge rectifier can handle a wide range of input frequencies.

The LTC3588-1 can also be configured for use with DC sources such as a solar panel or thermal couple as shown in Figures 11 and 12 by connecting them to one of the PZ1/PZ2 inputs. Connecting the two sources in this way prevents reverse current from flowing in each element. Current limiting resistors should be used to protect the PZ1 or PZ2 pins. This can be combined with a battery backup connected to VIN with a blocking diode.

Figure 9. AC Line Powered 3.6V Buck Regulator with Large Output Capacitor to Support Heavy Loads

Figure 10. Electric Field Energy Harvester
Figure 11. 5V to 16V Solar-Powered 2.5V Supply with Supercapacitor for Increased Output Energy Storage and Battery Backup

Figure 12. Thermoelectric Energy Harvester
DD Package
10-Lead Plastic DFN (3mm × 3mm)
(Reference LTC DWG # 05-08-1699)

**PACKAGE OUTLINE**

**RECOMMENDED SOLDER PAD PITCH AND DIMENSIONS**

**NOTE:**
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE M0-229 VARIATION OF (WEED-2).
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE TOP AND BOTTOM OF PACKAGE
PACKAGE DESCRIPTION

MS Package
10-Lead Plastic MSOP
(Reference LTC DWG # 05-08-1661 Rev E)

RECOMMENDED SOLDER PAD LAYOUT

NOTE:
1. DIMENSIONS IN MILLIMETER/(INCH)
2. DRAWING NOT TO SCALE
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
   MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
   INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX
TYPICAL APPLICATION

Piezoelectric 3.3V Power Supply with LDO
Post Regulator for Reduced Output Ripple

Peak-to-Peak Output Ripple vs COUT1

RELATED PARTS

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>DESCRIPTION</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>LT1389</td>
<td>Nanopower Precision Shunt Voltage Reference</td>
<td>800mA Operating Current, 1.25V/2.5V/4.096V</td>
</tr>
<tr>
<td>LTC1540</td>
<td>Nanopower Comparator with Reference</td>
<td>0.3μA Iq, Drives 0.01μF, Adjustable Hysteresis, 2V to 11V Input Range</td>
</tr>
<tr>
<td>LT3009</td>
<td>3μA Iq, 20mA Low Dropout Linear Regulator</td>
<td>Low 3μA Iq, 1.6V to 20V Range, 20mA Output Current</td>
</tr>
<tr>
<td>LT3652</td>
<td>Power Tracking 2A Battery Charger for Solar Power</td>
<td>MPPT for Solar, 4.95V to 32V, Up to 2A Charge Current</td>
</tr>
<tr>
<td>LT3690</td>
<td>40V, 350mA Step-Down Regulator with 2.5μA Iq</td>
<td>Integrated Boost and Catch Diodes, 4.2V to 40V Operating Range</td>
</tr>
<tr>
<td>LT3691</td>
<td>38V, 1.2A, 2MHz Step-Down Regulator with 2.8μA Iq</td>
<td>4.3V to 38V Operating Range, Low Ripple Burst Mode® Operation</td>
</tr>
<tr>
<td>LT3692</td>
<td>55V, 1.2A 2MHz Step-Down Regulator with 2.8μA Iq</td>
<td>4.3V to 55V Operating Range, Low Ripple Burst Mode Operation</td>
</tr>
<tr>
<td>LTC3631</td>
<td>45V, 100mA, Synchronous Step-Down Regulator with 12μA Iq</td>
<td>4.5V to 45V Operating Range, Overvoltage Lockout Up to 60V</td>
</tr>
<tr>
<td>LTC3642</td>
<td>45V, 50mA, Synchronous Step-Down Regulator with 12μA Iq</td>
<td>4.5V to 45V Operating Range, Overvoltage Lockout Up to 60V</td>
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