PHASED ARRAY 802.11g ANTENNA

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COMMITTEE MEMBERSHIP

TITLE: PHASED ARRAY 802.11g ANTENNA

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This thesis involves the development of a 2.4GHz phased array antenna for consumer wireless applications. 802.11g specifications include 11 channels from 2.412 GHz to 2.472 GHz. Each channel has a 22MHz minimum bandwidth with a 5 MHz center-to-center interchannel spacing. Design goals for the phased array include an operating frequency of 2.437GHz (channel 6 center frequency). The array antenna design procedure can be integrated into a future antenna laboratory experiment.

Design considerations for the antenna array include cost, producibility, compatibility with 802.11b/g devices, and performance. Dipole antenna elements are arranged in a linear array to simplify calculations and to provide an intuitive understanding of array fundamentals. A linear array can maximize signal to interference ratio (SIR) by placing pattern nulls in the directions of noise sources. The design includes eight radiating elements mounted at $\lambda/2$ intervals. Design specifications include a scanning range of 360 degrees in the H-plane. Each element’s phase and gain is controlled by a network capable of 180 degrees of phase delay and up to 15.5dB of attenuation to enable sidelobe cancellation.

This project includes array design, simulations and theoretical calculations, antenna array construction, and final design characterization. Measurements are compared to theoretical predictions yielding good results. The antenna array was connected to an access point and tested on a lab network. Successfully associating a laptop to the wireless router and browsing the router configuration pages confirm connectivity. Array functionality is evaluated at channel 6, the 802.11g center frequency. The array is also tested at the lower and upper frequencies of channel 6 to ensure less than 3dB SNR variation.

The thesis includes a sample lab, which includes: A prelab, a procedure, and sample questions.

Keywords: Phased Array Antenna, Signal to Interference Ratio (SIR), Signal to Noise Ratio (SNR), Sidelobe Cancellation (SLC), Array Factor (AF)
ACKNOWLEDGMENTS

First, and foremost, I would like to thank my God and Savior, Jesus Christ, who died for me while I was still a sinner (Romans 5:8). I would also like to thank my thesis advisor, Dean Arakaki, whose attention to detail and dedication to excellence were an inspiration throughout this project. I would like to thank my dad for letting me bounce countless ideas off him, and who helped me through some rough patches in troubleshooting. And finally, a special thank you to Jianbiao (John) Pan, who provided guidance in how to solder the digital step attenuator and provided access to the Heller reflow oven.
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I. Beam Shaping in Wireless Networking

Electronic communications began via copper wire, a controlled medium with minimal interference from outside sources. Wireless technology, however, uses a common transmission medium, over-the-air communications, for multiple users, which today encounters congestion.

IEEE standard 802.11 defines specifications for consumer wireless networking and operates under FCC regulations on the ISM (Industrial Scientific Medical) S-band. Licensing is not required if maximum transmit output power is 1W (30 dBm) and maximum effective isotropically radiated power (EIRP) is 4W (36 dBm) (i.e.: for every dB of antenna gain above 6dBi, transmitter output must be reduced by 1dB). In fixed, point-to-point operation, this requirement is reduced to 1dB reduction in transmit power per 3dBi antenna gain increase.

Home routers use a contention-based system for medium sharing [1]. A router must verify the channel is clear before sending a message. If two routers verify the channel is clear and transmit any part of their messages simultaneously, both messages are lost and both routers wait a random time period before attempting to retransmit. Again, each router will verify the channel is clear before attempting to transmit. Two system commands used to prevent collisions caused by blind nodes (nodes that are in transmit range of the receiver but not the transmitter) are the Request to Send (RTS) and Clear to Send (CTS) messages [1]. In this system, the transmit device sends out an RTS, and the receiving node replies with a CTS.
acknowledging RTS receipt. The blind node will receive the CTS, but not the RTS message. Any device in range of the RTS or CTS message waits the transmission duration (contained inside the RTS and CTS messages) before attempting to transmit.

While the 802.11g specification enables a contention-based shared transmission medium, interference sources can severely limit connection throughput. The standard defines 11 channels at 5 MHz spacing, in the ISM S-band. The 11 channels (additional channels are available outside the United States) are shown in Figure 1. Because the channel bandwidth is 22 MHz, or five channels wide, access points must operate in five channel increments (five channels between center frequencies) to avoid cross-channel interference. Therefore, a maximum of three channels (1, 6, 11) can be used without cross-channel interference [1]. If two routers operate on neighboring channels, they will perceive each other as interferers, creating co-channel or adjacent channel interference [2]. Other interference sources include microwave ovens, cordless phones, wireless game controllers, and fluorescent lights. “RF interference can be a major inhibitor to wireless [system] performance, creating security vulnerabilities and wireless network instability.” [2]

Figure 1 Overlapping Wi-Fi Channels
Solution

Access point antenna directivity can be increased to mitigate service degradation in interfering environments. This approach can provide 10dB of gain, but only doubles the access point to interferer perceived distance [2]. Table 1 defines approximate operating ranges for a few common antenna types [1].

Table 1 Antenna types and typical operating specifications

<table>
<thead>
<tr>
<th>Antenna Type</th>
<th>Gain</th>
<th>Beamwidth</th>
<th>Range (with $P_{TX}$ adhering to FCC regulations)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dipole</td>
<td>3-10dBi</td>
<td>360 degrees</td>
<td>&lt;1 mile</td>
</tr>
<tr>
<td>Yagi</td>
<td>12-18dBi</td>
<td>20-30°</td>
<td>1-3 miles</td>
</tr>
<tr>
<td>Parabolic</td>
<td>Up to 24dBi</td>
<td>6.5 degrees</td>
<td>20 miles or more</td>
</tr>
</tbody>
</table>

Yagi and parabolic antennas have a single mainlobe and are unsuitable for multiple mobile users; they are generally used for site-to-site links. To track a mobile user, these antennas are usually mechanically scanned. Beamshaping enables electronically scanned mainlobes which can track multiple users without physical repositioning. Phased array antenna service is redirected more quickly than physically adjusted antenna service.

Parabolic antennas are difficult to direct due to narrow beamwidth; another reason they are not recommended for mobile users [1]. Directional antennas rely on a narrow mainlobe and attenuation in other directions to enhance signal to noise ratio (SNR).

A phased array with sidelobe cancellation (SLC) can enhance signal to interference ratio (SIR) by placing nulls at incident interference angles. Note that SIR and SNR can be used interchangeably; however SIR is more
commonly used with distinct interference sources. Unlike directive antennas, phased arrays can create multiple mainlobes. Ideally, a “smart” array is employed to dynamically identify desired signal and interference incident angles. However, due to complexity and cost, “smart” arrays are outside the scope of this thesis. For more information on smart antennas, see [3].

**Table 2  SLC phased array design specifications**

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Value</th>
</tr>
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<tbody>
<tr>
<td>Frequency Range</td>
<td>2.401-2.473GHz</td>
</tr>
<tr>
<td>Center Frequency</td>
<td>Channel 6- 2.437GHz</td>
</tr>
<tr>
<td>Impedance (at center frequency)</td>
<td>50Ω</td>
</tr>
<tr>
<td>Gain</td>
<td>0-10dBi</td>
</tr>
<tr>
<td>HPBW</td>
<td>20°&lt;HPBW&lt;360°</td>
</tr>
<tr>
<td>Max Steering Step Size</td>
<td>5°</td>
</tr>
<tr>
<td>VSWR</td>
<td>&lt;3</td>
</tr>
<tr>
<td>Connector Type</td>
<td>SMA(F)</td>
</tr>
<tr>
<td>Max Input Power</td>
<td>29dBm</td>
</tr>
</tbody>
</table>
II. **System Level Design**

Phased array designs are composed of three key elements: a distribution network, weighting channels, and radiating elements. The block diagram for the phased array is shown in Figure 2.

Phased antenna arrays require a feed network to distribute a signal from a single source. Common feed networks include series, corporate, and hybrid. For all but the series feed, power splitter/combiners are required. Available microwave power splitter designs include Wilkinson power dividers and branch-line and hybrid couplers. The Wilkinson power splitter/combiner offers several advantages over other designs: equal phase shift (critical), higher efficiency, and greater isolation between output ports 2 and 3. This project utilizes a corporate feed network comprised of seven Wilkinson power splitters.

Phased arrays generally require variable phase shift between elements to control mainbeam direction. SLC requires variable attenuation per element in addition to variable phase shift. Therefore, each weighting board
is composed of a phase shifter and attenuator. Weighting boards are positioned between the distribution network and radiating elements. The project utilizes H-plane omnidirectional dipole antennas since directional patterns adversely affect the array’s steerability.
III. Wilkinson Power Splitter

The Wilkinson power splitter includes two quarter-wave, \(\sqrt{2}Z_0\) characteristic impedance transmission lines and a \(2Z_0\) resistor: see Figure 3. Because the Wilkinson power splitter is linear and symmetric, even and odd mode analysis is employed which specifies a resistor value of \(2Z_0\) and quarter wave transformer characteristic impedance of \(\sqrt{2}Z_0\). With these specifications, each network port is matched to \(Z_0\). For forward splitter operation, power at port 1 is evenly split between ports 2 and 3 with equal phase. Equal amplitude, in-phase inputs received at ports 2 and 3 are combined at port 1. If the incident signal appears only at port 2 (or similarly: only at port 3), half the power received at the port is transmitted to port 1, the other half is dissipated through the \(2Z_0\) resistor. Ports 2 and 3 are isolated.

![Figure 3 Wilkinson Power Splitter](image-url)
Simulation

The microstrip Wilkinson design is defined in ADS for design optimization and Gerber file export for PCB milling.

Wilkinson Power Splitter Simulation

The ADS model for a realizable Wilkinson power splitter is shown in Figure 4, while Figure 5 defines simulation and optimization parameters. Termination blocks, substrate information, and additional microstrip lines to accommodate a chip resistor and SMA connectors are all added for construction on FR-4 with 59 mil height and 4.4 relative permeability.
Figure 4  ADS Model of Single Stage Wilkinson Power Splitter
Simulation results indicate equal phase shift from input to both outputs, ranging from 159° to 165° over the WiFi band (2.401-2.473GHz): see Figure 6. Forward transmission gain is -3.1dB and port 2 to port 3 isolation is greater than 40dB over the entire frequency range.
Figure 6  Wilkinson Power Splitter ADS Simulation Results
**Cascaded Wilkinson Power Splitter**

Three single-stage Wilkinson dividers are cascaded to create a one-port to four-port splitter/combiner; see Figure 7. Cascaded Wilkinson power splitters decrease the number of required connectors and boards. For an 8-element array, one single-stage Wilkinson is followed by two cascaded designs. ADS simulation results are shown in Figure 8 and Figure 9. Transmission gain coefficients are -6.3dB, approximately 3dB greater than a single Wilkinson splitter and 0.3dB less than an ideal 5 port splitter. Isolation between output ports is between 40dB and 59dB. Figure 10 shows the phase shift between the input and each output, which exhibit equal phase delay.

![Cascaded Wilkinson Power Splitter Layout](image-url)
Figure 8  Cascaded Wilkinson Power Splitter VSWR and Attenuation Simulations, 2.375GHz-2.500GHz
Figure 9 Cascaded Wilkinson Power Splitter VSWR and Attenuation Simulations, 2.375GHz-2.500GHz (cont'd)

\[ \text{VSWR}_{\text{in}} = \frac{1 + \text{mag}(S(1,1))}{1 - \text{mag}(S(1,1))} \]

- m6
  - freq = 2.401GHz
  - VSWR\(_{\text{in}} = 1.032\)

- m7
  - freq = 2.473GHz
  - VSWR\(_{\text{in}} = 1.038\)
Wilkinson Power Splitter Results

Both a single-stage and a cascaded 5-port splitter are characterized from 2.4 to 2.5GHz.

**Single-Stage Power Splitter**

The constructed Wilkinson power splitter/combiner is shown in Figure 11. Input and output VSWR measurements are shown in Figure 12. The “stair-stepping” effect occurs because the measurement resolution approaches that of the VNA.
Worst-case VSWR, 1.088, occurs at port 2 at 2.5GHz. Best-case VSWR, 1.055, occurs at port 3 at 2.4GHz. Transmission S-parameters, including $S_{21}$, $S_{12}$, $S_{31}$, and $S_{13}$, are plotted in Figure 13, and isolation.
parameters, $S_{23}$ and $S_{32}$, in Figure 14. Forward gain ranges between -3.177dB and -3.589dB, and reverse gain between -3.145dB and -3.525dB. Measured gain was up to 0.4dB less than simulated forward and reverse gains of -3.1dB.

![Transmission S-Parameters](image)

**Figure 13** Transmission S-Parameters, Single Stage Wilkinson Power Divider

Figure 14 shows isolation ranges from 25.73dB to 29.24dB despite simulations indicating greater than 40dB isolation. High isolation values are dependent on Wilkinson power splitter symmetry: an off-center chip resistor placement is the most likely cause for the discrepancy. Figure 15 shows phase shift through the power splitter from port 1 to both ports 2 and 3 and ports 2 and 3 to port 1. In the forward direction, the difference in phase delay between ports 1 and 2 and between ports 1 and 3 at any given
frequency is $1.12° \pm 0.14°$ and in the reverse direction is $0.60° \pm 0.14°$. This is compensated for in the final configuration with phase shifter delay.

Figure 14  Isolation Measurements Between Ports Two and Three for a Single Wilkinson Power Divider
**Cascaded Power Splitter**

The cascaded power splitter consists of two Wilkinson power splitters, cascaded from the outputs of a third splitter, see Figure 16. Figure 17 shows measured VSWR at each port (other ports 50 \( \Omega \) terminated). The largest VSWR, 1.25, occurs at the input port.
Figure 16  Constructed, Cascaded Wilkinson Power Splitter/Combiner

Figure 17  Cascaded Wilkinson Power Splitter Input and Output VSWRs
Figure 18 shows cascaded Wilkinson power splitter measured forward and reverse gain. Measured results show forward IL between -6.65dB and -7.15dB. Measurements are 0.3dB – 0.8dB less than simulated values.

Isolation measurements (Figure 19) indicate minimum 26dB attenuation between any two output ports. Minimum isolation, 26dB, is noted between ports 2 and 3. Maximum isolation, 34dB, occurs between ports 2 and 5, which is expected due to maximum separation distance and multiple resistors. In the cascaded configuration, the 1.12° ± 0.14° phase shift difference observed between port 1 and each of the two output ports in the single Wilkinson power splitter increases to 2.2° ± 0.3° in the cascaded
power splitter, as shown in Figure 20. This difference is compensated for in the phase shifter component.

![Figure 19 Isolation Measurements, Port 2 to Ports 3 Through 5](image1)

![Figure 20 Phase Shift, Cascaded Wilkinson Power Splitter](image2)
IV. Element Signal Weighting

Each array element requires a complex valued weight to produce desired pattern maximums and nulls. Hence, each channel includes a weighting board comprised of a phase shifter and attenuator; see Figure 21.

![Weighting Board Diagram](image)

Figure 21  Block Diagram of Basic Weighting Board

Phase Shifter

The Mini-Circuits JSPHS-2484+, shown in Figure 22, is a 180° maximum phase shift, J-lead, surface mount phase shifter designed to operate from 2.150GHz to 2.484GHz, with a maximum input power of 20dBm, which satisfies phased array specifications. Typical performance data for swept control frequencies is shown in Table 3.

![Phase Shifter Image](image)

Figure 22  JSPHS-2484+ from Mini-Circuits
Table 3  Typical Performance Data (from Mini-Circuits), JSPHS-2484+ Phase Shifter

<table>
<thead>
<tr>
<th>Control Voltage (V)</th>
<th>Phase Shift* (Degrees)</th>
<th>VSWR (::1)</th>
<th>Insertion Loss (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>2150 MHz</td>
<td>2300 MHz</td>
<td>2484 MHz</td>
</tr>
<tr>
<td>0.0</td>
<td>0.01</td>
<td>0.00</td>
<td>0.00</td>
</tr>
<tr>
<td>1.0</td>
<td>0.06</td>
<td>4.69</td>
<td>4.22</td>
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<td>15.0</td>
<td>320.70</td>
<td>266.91</td>
<td>201.15</td>
</tr>
</tbody>
</table>

* Normalized at control voltage = 0V

Attenuator

The Mini-Circuits DAT-15RS-PP was selected for its single positive power supply requirement, low cost, 50Ω input impedance, 20dBm maximum input power, and digitally controlled attenuation. The DAT-15RS-PP, (Figure 23), is a DC-4GHz digital step attenuator with 0-15.5dB attenuation in 0.5dB steps. The leadless design (10 mil wide pads with 10 mil pad separation) minimizes attenuation, but presents manufacturing complications.
Layout

The weighting board layout (Figure 24) includes one set of 50Ω microstrip lines (connecting the input to the phase shifter, attenuator, and output). All other traces accommodate DC biasing or logic.

![Figure 24 Weighting Board Layout](image)

Manufacturing

Due to small pad size and inaccessible 2mm x 2mm digital step attenuator ground pad, the board cannot be assembled using a soldering iron. Instead, a Heller reflow oven is used. A five-step process is used to attach the digital step attenuator to the PCB:
1. Apply solder paste to pads
2. Reflow solder paste
3. Apply “tacky” paste flux (Kester TSF-6850) to the pads [3]
4. Place digital step attenuator onto pads
5. Reflow board

“Tacky” flux allows for attenuator positioning, while preventing attenuator movement when transferring the board from bench to reflow oven. Solder paste cannot be used because component movements could create solder bridges between pins.

The reflow oven houses five heating zones, with heating elements above and below the conveyer belt, except for zone 1 (one overhead element). The reflow oven’s internal component layout is shown in Figure 25. The reflow oven consists of 5 zones, each zone referring to a top/bottom heating element pair. Reflow “recipes” specify temperatures for each heating zone, conveyer belt speed, and exhaust setting in cubic feet per minute (CFM). The reflow “recipe” used for the lead-free solder paste is shown in Table 4.

![Figure 25 Reflow Oven, Internal Component Layout](image)

**Table 4 Reflow parameters used for the Heller oven**

<table>
<thead>
<tr>
<th></th>
<th>Zone 1</th>
<th>Zone 2</th>
<th>Zone 3</th>
<th>Zone 4</th>
<th>Zone 5</th>
<th>Belt</th>
<th>Exhaust</th>
</tr>
</thead>
<tbody>
<tr>
<td>Top (°C)</td>
<td>165</td>
<td>150</td>
<td>185</td>
<td>220</td>
<td>260</td>
<td>30 cm/min</td>
<td>95 CFM</td>
</tr>
</tbody>
</table>
The completed weighting board is shown in Figure 26.

![Assembled Weighting Board](image)

**Figure 26 Assembled Weighting Board**

The weighting board assembly travels through 3 stages in the reflow oven. In the first stage, the board is preheated, which allows the copper, board and components to safely reach high temperatures. As the temperature increases, the board enters the flux activation stage, where the flux is given time to clean the contacts. In the third stage, the solder refloows, creating an electrical and physical bond between components and the board.
Characterization

An initial set of three boards was tested using 16 combinations of phase shift and attenuator settings. Bias and attenuation settings are shown in Table 5.

<table>
<thead>
<tr>
<th>Test Case</th>
<th>Bias (V)</th>
<th>Attenuation Bits</th>
<th>Attenuation (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.14</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>3</td>
<td>5</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>4</td>
<td>7</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>5</td>
<td>9</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>6</td>
<td>11</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>7</td>
<td>13</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>8</td>
<td>15</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>9</td>
<td>0</td>
<td>00001</td>
<td>8.0</td>
</tr>
<tr>
<td>10</td>
<td>0</td>
<td>00010</td>
<td>4.0</td>
</tr>
<tr>
<td>11</td>
<td>0</td>
<td>00100</td>
<td>2.0</td>
</tr>
<tr>
<td>12</td>
<td>0</td>
<td>01000</td>
<td>1.0</td>
</tr>
<tr>
<td>13</td>
<td>0</td>
<td>10000</td>
<td>0.5</td>
</tr>
<tr>
<td>14</td>
<td>0</td>
<td>00000</td>
<td>0.0</td>
</tr>
<tr>
<td>15</td>
<td>15</td>
<td>11111</td>
<td>15.5</td>
</tr>
<tr>
<td>16</td>
<td>0</td>
<td>11111</td>
<td>15.5</td>
</tr>
</tbody>
</table>

Figure 27-Figure 32 show test results for Board 2. Parameters of interest for all boards are shown in Table 6, Summarized Results.
Figure 27  Weighting Board, Forward Attenuation

Figure 28  Weighting Board, Reverse Attenuation
Figure 29  Weighting Board, Reverse Phase Delay

Figure 30  Weighting Board, Forward Phase Delay
Figure 31  Weighting Board, Input VSWR

Figure 32  Weighting Board, Forward and Reverse Phase Delay Comparison
**Summarized Results**

Table 6 summarizes the operating characteristics for each board. Worst-case performance values are displayed.

Table 6  Summarized Results of Key Measurements from all 8 Boards (2.45 GHz)

| Board | Forward IL (dB) | 8dB Atten Setting (dB) | Difference in Atten (dB) | Max VSWRin | Max Phase Shift (degrees) | Min Phase Shift (degrees) | | Forward/Reverse Phase Discrepancy | (degrees) (7V Bias) |
|-------|----------------|------------------------|--------------------------|------------|--------------------------|--------------------------|--------------------------|--------------------------|
| 1     | 8.03           | 18.29                  | 10.26                    | 2.74       | 291.50                   | 49.40                    | 11.84                    |
| 2     | 6.60           | 15.17                  | 8.57                     | 1.68       | 303.67                   | 100.31                   | 3.60                     |
| 3     | 4.70           | 14.90                  | 10.20                    | 1.91       | 300.28                   | 68.53                    | 10.14                    |
| 4     | 4.42           | 13.54                  | 9.12                     | 2.35       | 299.93                   | 59.40                    | 12.01                    |
| 5     | 4.58           | 13.96                  | 9.38                     | 2.38       | 301.55                   | 66.89                    | 10.83                    |
| 6     | 9.65           | 20.79                  | 11.14                    | 5.05       | 294.24                   | 43.26                    | 0.67                     |
| 7     | 7.68           | 16.23                  | 8.55                     | 1.89       | 300.65                   | 67.85                    | 9.22                     |
| 8     | 8.22           | 16.53                  | 8.31                     | 1.54       | 325.18                   | 109.13                   | 8.21                     |

By Cascaded Component Spec: 4.50 12.50 8.00 1.50 Min+201

Measured Max: 9.65 20.79 11.14 5.05 325.18 109.13 12.01

Measured Min: 4.42 13.54 8.31 1.54 291.50 43.26 0.67

Measured Delta: 5.23 7.25 2.83 3.51 33.68 65.87 11.34

Measured Avg: 6.73 16.18 9.44 2.44 302.13 70.60 8.31
Justification for Varied Results

Board 6 exhibits unacceptable performance. The 9.7dB IL is caused by a manufacturing defect. Excess flux caused attenuator misalignment during flux activation prior to solder reflow. To correct the problem, the attenuator was removed, repositioned using fresh flux, and reflowed again. No additional solder paste was added to minimize component damage caused by additional heating and cooling cycles. An insufficient amount of remaining solder, after part removal, created intermittent solder joints. Therefore, board 6 cannot be used in the final array. Each board’s IL corresponds to manufacturing process changes. Boards 1 and 2 have insufficient solder under the attenuator; it was difficult to evenly apply the solder paste. With practice, solder paste application improved and the IL decreased to between 4dB and 5dB. Boards 7 and 8 were manufactured on a different brand of FR-4, which increased IL to 7.68dB and 8.22dB, respectively.

Solution for Board 6

Since board 6 is unreliable, the system is reduced to a 7-element array. This reduces the overall design efficiency by 1/8 and requires a matched load at the omitted phase shifter board location.
V. **Antenna Element and Antenna Mount Assembly**

**Antenna Element**

Nearson model 614 vertically polarized omnidirectional antennas are selected as the radiating elements. Model 614 is a multiband antenna with gain specified in Table 7. Other key features include: 50 Ω impedance, VSWR<2.0 over the specified frequency range, and SMA connectivity.

<table>
<thead>
<tr>
<th>Frequency</th>
<th>Gain</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.45 GHz</td>
<td>4.0 dBi</td>
</tr>
<tr>
<td>4.90 GHz</td>
<td>5.0 dBi</td>
</tr>
<tr>
<td>5.25 GHz</td>
<td>5.0 dBi</td>
</tr>
<tr>
<td>5.75 GHz</td>
<td>4.5 dBi</td>
</tr>
</tbody>
</table>

**Antenna Mount**

The antenna mount is composed of 3/4" plywood: see Figure 33. Mounting holes are friction fitted to the array elements and are spaced λ/2 (2.42 in.) apart. Figure 34 shows the fully assembled phased array. In this image the array is positioned backwards to measure the rear 180° of the radiation pattern span without interference from the positioner.

![Figure 33 Mechanical Drawing, Antenna Mount](image)
Figure 34 Fully Assembled Phased Array in Anechoic Chamber
VI. Calculating Complex Element Weights

Matlab code calculates the complex element weights required for arbitrary array factor patterns. The Matlab script (m-file) accepts the number of array elements, interelement spacing in wavelengths, and the number of desired signal sources and interference sources and corresponding incident angles. The m-file outputs attenuation (dB) and phase shift (deg) requirements for each element to achieve the desired radiation pattern and a normalized AF plot.

Figure 35 Matlab Script Block Diagram

Complex Element Weights

From antenna theory [3], the AF equation for a linearly spaced phased array takes the form:

\[ 1 + e^{j(\beta d \sin \theta + \delta)} + e^{j2(\beta d \sin \theta + \delta)} + \ldots + e^{j(N-1)(\beta d \sin \theta + \delta)} \]  

(1)

where \( \delta \) is phase shift (radians) between elements, and \( d \) is the interelement spacing. The AF terms are placed in a vertical array, called the array vector,

\[ \overline{a}(\theta) = [1 \quad e^{j(\beta d \sin \theta + \delta)} \quad \ldots \quad e^{j(N-1)(\beta d \sin \theta + \delta)}]^T \]  

(2)

An array of optimized weights that provides the desired AF is defined as

\[ w^N = [w_1 \quad w_2 \quad \ldots] \]  

(3)
A system of multiple array requires the following relations:

Condition 1: \( y_{\text{signal}} = w_1 e^{-j(\beta d \sin \theta_{\text{sig}})} + w_2 + w_3 e^{j(\beta d \sin \theta_{\text{sig}})} = 1 \)

Condition 2: \( y_{\text{noise}_1} = w_1 e^{-j(\beta d \sin \theta_{\text{noise}_1})} + w_2 + w_3 e^{j(\beta d \sin \theta_{\text{noise}_1})} = 0 \)

Condition 3: \( y_{\text{noise}_2} = w_1 e^{-j(\beta d \sin \theta_{\text{noise}_2})} + w_2 + w_3 e^{j(\beta d \sin \theta_{\text{noise}_2})} = 0 \)

Recast in matrix form,

\[ \begin{bmatrix} \bar{w} \end{bmatrix}^H \cdot \bar{A} = \bar{u}^T \quad (4) \]

where \( \bar{A} \) is a matrix of array vectors, defined in equation (2) in the form
\[ \bar{A} = [\bar{a}_0 \quad \bar{a}_1 \quad \bar{a}_2] \]
and \( \bar{u} = [1 \quad 0 \quad 0]^T \) with 1’s for desired signals and 0’s for interference sources. \( \bar{w}^H \) is determined using

\[ \bar{w}^H = \bar{u}^T \cdot \bar{A}^{-1} \quad (5) \]

For cases where \( A \) is not invertible (NxN matrix with N-array elements and N-arriving signals), the Godara method is used [4]. In this method, simulated noise, \( \sigma_n^2 \), is added to allow matrix inversion.

\[ \bar{w}^H = \bar{u}^T \cdot \bar{A}^{-H} (\bar{A} \cdot \bar{A}^H + \sigma_n^2 \bar{I})^{-1} \quad (6) \]

Where \( \bar{I} \) represents the identity matrix and the superscript \( H \) signifies the complex conjugate transpose of the matrix.

**Array Factor Plot**

The array factor is found using \( y = \bar{w}^H \ast e^{-j(2\pi n \sin \theta)} \), where \( n \) is a vector of integer values between 0 and (N-1) and -180 < \( \theta < 180 \). The m-file then plots \( \frac{y(\theta)}{y_{\max}} \).
VII. Array Characterization

Single Element Characterization

The measured array radiation pattern is the product of the element pattern and array factor. Measured element patterns are displayed in Figure 36-Figure 39. Figure 36 contains the measured co-pol H-plane element pattern, which is combined with the array factor to produce the total radiation patterns in the next section. The single antenna element has 4.12dBi average gain, 0.38dB less than the 4.5dBi specification. The measured co-pol H-plane radiation pattern has a relatively constant pattern (standard deviation of 1.95dB), as expected from a dipole radiator.
Figure 37 Cross-Pol E-Plane Radiation Pattern, Single Antenna Element

Figure 38 Cross-Pol H-Plane Radiation Pattern, Single Antenna Element
Test Cases

Table 8 shows desired signal and interferer (maximums and nulls) incident angles for each test case. Test case 1 is designed to demonstrate a narrow mainbeam, test case 2 demonstrates multiple mainlobes, and test case 3 shows that arbitrary angle placement and non-alternating maximums and nulls are possible.

*Table 8  Phased Array Test Cases*

<table>
<thead>
<tr>
<th>Test Case</th>
<th>Maximum Angles (degrees)</th>
<th>Null Angles (degrees)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>-25, 25</td>
</tr>
<tr>
<td>2</td>
<td>-90, 0, 90</td>
<td>-45, 45</td>
</tr>
<tr>
<td>3</td>
<td>0, 45</td>
<td>-45, -65</td>
</tr>
</tbody>
</table>
**Test Case 1**

The first test case includes a maximum at $0^\circ$ and nulls at $\pm25^\circ$. $w_{\text{Bar}H}$ is a vector containing complex valued (there are no imaginary components in this test case) weights required for Test Case 1. These weights are converted to attenuation(dB) and phase shift(deg) as shown in Figure 40.

\[
\begin{align*}
\text{wBarH} & = \\
& \begin{array}{ccccccc}
0.1113 & 0.0955 & 0.1770 & 0.2320 & 0.1770 & 0.0955 & 0.1113 \\
\end{array}
\end{align*}
\]

For element 1, Mag: -6.38 dB  Phase: 0 degrees  
For element 2, Mag: -7.71 dB  Phase: 0 degrees  
For element 3, Mag: -2.35 dB  Phase: 0 degrees  
For element 4, Mag: 0.00 dB  Phase: 0 degrees  
For element 5, Mag: -2.35 dB  Phase: 0 degrees  
For element 6, Mag: -7.71 dB  Phase: 0 degrees  
For element 7, Mag: -6.38 dB  Phase: 0 degrees

Figure 40 Complex Valued Weights, Test Case 1

The measured AF in Figure 42 is compared to the simulated array factor (AF) shown in Figure 41. The measured radiation pattern (dBi) is shown in Figure 43. The SNR between $0^\circ$ and $25^\circ$ increased by 44.5dB and the SNR between $0^\circ$ and $25^\circ$ increased by 11.34dB.
Figure 41  Simulated Normalized Array Factor, Test Case 1

Figure 42  Measured Normalized Array Factor, Test Case 1
Test Case 2

The second test case specifies maximums at 0°, ±90° and nulls at ±45°.

Complex weighting coefficients required for this configuration are shown in Figure 44.

\[ \text{wBarH} = \begin{bmatrix} 0.2138 & 0.0199 & 0.2861 & -0.0398 & 0.2861 & 0.0199 & 0.2138 \end{bmatrix} \]

For element 1, Mag: -2.53 dB Phase: 0 degrees
For element 2, Mag: -23.16 dB Phase: 0 degrees
For element 3, Mag: 0.00 dB Phase: 0 degrees
For element 4, Mag: -17.14 dB Phase: 180 degrees
For element 5, Mag: 0.00 dB Phase: 0 degrees
For element 6, Mag: -23.16 dB Phase: 0 degrees
For element 7, Mag: -2.53 dB Phase: 0 degrees
This test case requires attenuation levels beyond the maximum 15.5dB provided by the digital step attenuator. The boards with highest IL (1 and 8) are used for elements requiring the greatest attenuation (2 and 6); boards with lowest IL (4 and 5) are used for elements requiring lower attenuation (3 and 5). The digital step attenuators add significant phase shift to elements with relatively high attenuation. The additional phase shift must also be added to other elements through the phase shifter. The additional phase requirement on element 4 exceeds the weighting board’s 180° maximum phase shift. Because channel 4 requires 17dB attenuation, the radiating element is replaced by a matched load. Actual weighting values were used to resimulate the normalized AF shown in Figure 46. The aforementioned deviations resulted in a gain decrease for the ±90° mainlobes as well as the sidelobes and a 3° increase in the 45° null position. The simulated AF for the ideal configuration, shown in Figure 45, is compared to the measured AF in Figure 47. The measured radiation pattern (dBi) for Test Case 2 is shown in Figure 48. SNR improvements are shown in Table 9. The maximum SNR improvement (19.14dB) is between the signal at 0° and the interferer at -45°. Minimum SNR improvement (7.27dB) is between the signal at 90° and the interferer at 45°.

**Table 9 SNR Improvements, Test Case 2**

<table>
<thead>
<tr>
<th>Signal Angle</th>
<th>Interferer Angle</th>
<th>SNR (dB)</th>
<th>Signal Angle</th>
<th>Interferer Angle</th>
<th>SNR (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0°</td>
<td>45°</td>
<td>16.13</td>
<td>90°</td>
<td>-45°</td>
<td>10.28</td>
</tr>
<tr>
<td>0°</td>
<td>-45°</td>
<td>19.14</td>
<td>-90°</td>
<td>45°</td>
<td>7.46</td>
</tr>
<tr>
<td>90°</td>
<td>45°</td>
<td>7.27</td>
<td>-90°</td>
<td>-45°</td>
<td>10.47</td>
</tr>
</tbody>
</table>
Figure 45  Simulated Normalized Array Factor, Test Case 2

Figure 46  Simulated Normalized Array Factor Using Actual Weighting Values
Figure 47  Measured Normalized Array Factor, Test Case 2

Figure 48  Measured Radiation Pattern (dBi), Test Case 2
**Test Case 3**

The third test case includes maximums at 0°, 45° and nulls at -45°, -65°.

Weighting coefficients required for this configuration are shown in Figure 49.

\[
\text{wBarH} = \\
0.0692 + 0.0977i, \quad 0.0809 - 0.1046i, \quad 0.3494 - 0.0403i, \\
0.0244 + 0.1267i, \quad 0.1505 - 0.1842i, \quad 0.3090 + 0.0675i, \\
0.0163 + 0.0372i
\]

For element 1, Mag: -9.37 dB Phase: 106.9 degrees
For element 2, Mag: -8.50 dB Phase: 0.0 degrees
For element 3, Mag: 0.00 dB Phase: 45.7 degrees
For element 4, Mag: -8.71 dB Phase: 131.3 degrees
For element 5, Mag: -3.40 dB Phase: 1.5 degrees
For element 6, Mag: -0.92 dB Phase: 64.6 degrees
For element 7, Mag: -18.75 dB Phase: 118.6 degrees

Since attenuation levels required for element 7 could not be attained, the radiator was omitted and replaced with a matched load. Actual weighting values were used to resimulate the normalized AF shown in Figure 51. The simulated AF for the ideal configuration, shown in Figure 50, is compared to the measured AF in Figure 52. To show radiation pattern variation over the channel bandwidth, measurements are taken at 2.426GHz, 2.437GHz, and 2.448GHz (Figure 53), the low, center, and high frequencies for channel 6 of the 802.11g specification. SNR improvements achieved by the array in this configuration are shown in Table 10. Maximum SNR improvement (16.47dB) at the center frequency is between the signal at 45° and the interferer at -45°. Minimum SNR improvement (9.24dB) is between the signal at 0° and the interferer at -65°. Over the entire channel, the maximum SNR variation is 2dB, indicating that the radiation pattern is relatively consistent.
Table 10 SNR Improvement Measurements, Test Case 3

<table>
<thead>
<tr>
<th>Frequency:</th>
<th>2.437 GHz</th>
<th>Frequency:</th>
<th>2.426 GHz</th>
<th>Frequency:</th>
<th>2.448 GHz</th>
</tr>
</thead>
<tbody>
<tr>
<td>Signal Angle (degrees)</td>
<td>Interferer Angle (degrees)</td>
<td>SNR(dB)</td>
<td>Signal Angle (degrees)</td>
<td>Interferer Angle (degrees)</td>
<td>SNR(dB)</td>
</tr>
<tr>
<td>0</td>
<td>-45</td>
<td>13.8</td>
<td>0</td>
<td>-45</td>
<td>13.1</td>
</tr>
<tr>
<td>0</td>
<td>-65</td>
<td>9.2</td>
<td>0</td>
<td>-65</td>
<td>10.8</td>
</tr>
<tr>
<td>45</td>
<td>-45</td>
<td>16.5</td>
<td>45</td>
<td>-45</td>
<td>16.2</td>
</tr>
<tr>
<td>45</td>
<td>-65</td>
<td>11.9</td>
<td>45</td>
<td>-65</td>
<td>13.9</td>
</tr>
</tbody>
</table>

Figure 50  Simulated Normalized Array Factor, Test Case 3

Figure 51  Simulated Normalized Array Factor Using Actual Weighting Values
Figure 52  Measured Normalized Array Factor, Test Case 3

Figure 53  Measured Radiation Patterns (dBi), Test Case 3

VSWR
Test case 3 phased array VSWR is shown in Figure 54.

**Test Case 3 VSWR**

![Test Case 3 VSWR](image)

**Figure 54 VSWR, Test Case 3**

**Half Power Beamwidth**

In test case 1, nulls are placed within 25° of a maximum. The mainbeam HPBW in Test Case 1 is $21 \pm 2^\circ$. Other HPBWs are shown in Table 11. HPBW measurements are accurate to within $2^\circ$, the measurement resolution. Deviations from simulated HPBWs are less than $9^\circ$ (Test Case 3: 45° mainlobe).

<table>
<thead>
<tr>
<th>Test Case</th>
<th>Simulated HPBW</th>
<th>Measured HPBW</th>
</tr>
</thead>
<tbody>
<tr>
<td>Case1- 0°Mainlobe</td>
<td>17°</td>
<td>$21 \pm 2^\circ$</td>
</tr>
<tr>
<td>Case2- 0°Mainlobe</td>
<td>14°</td>
<td>$13 \pm 2^\circ$</td>
</tr>
<tr>
<td>Case2- 90°Mainlobe</td>
<td>58°</td>
<td>$51 \pm 2^\circ$</td>
</tr>
<tr>
<td>Case3- 45°Mainlobe</td>
<td>28°</td>
<td>$19 \pm 2^\circ$</td>
</tr>
<tr>
<td>Case3- 0°Mainlobe</td>
<td>18°</td>
<td>$17 \pm 2^\circ$</td>
</tr>
</tbody>
</table>
Home Router Lab Network Test

A Broadcom wireless N router is configured with all router radios disabled except wireless G, set to channel 6. With no antennas attached, the signal from the router is -90dBm, as measured by an internal Broadcom BCM43xx chipset at 20 ft. Windows XP cannot associate with the router at this signal level.

The phased array is connected to the router. The signal at 25ft is greater than -50dBm. The laptop is able to connect to and browse the router configuration pages, showing full link functionality.
VIII. Sample Lab

Introduction

This lab involves a 7-element linear phased antenna array and a comparison between theory and actual performance.

Prelab

1. Using the Godara method [5], write Matlab code to calculate element complex weighting values (phase shift and attenuation) required to produce a desired array factor
   a. Code must calculate weighting values for any number of elements and any inter-element spacing (in λ)
   b. Code must accept any number of pattern maximums and/or null angles as input (up to the number of elements)
   c. Hint: Solving for 4 complex weights with 7 elements requires the addition of simulated noise to yield invertible matrices.

2. Find element weighting values for a phased array that produce maximums at 0º and 45º and nulls at -45º and -65º
   a. N=number of elements=7
   b. d=inter-element spacing=0.5 (λ)
   c. f=2.437 (GHz)

3. Use Matlab to plot the array factor using the weighting values found in step 2

Turn in:

1. Matlab code
2. Array Factor plot
3. Weighting value list
4. Bring an extra copy to the lab.

Procedure

1. Set two separate DC voltage supplies to 3V and 15V
   a. 3V current limit: 40mA
   b. 15V current limit: 5mA
2. Connect supplies to array (locations labeled)
3. Adjust weighting parameters for each channel to coincide with prelab results.
   a. The forward and reverse characteristics of phase shifters are different, which S-parameter should be used?
   b. Note: the phased array is the chamber receive antenna.
c. Include measurements of all coax cables and power splitters. Terminate any unused ports. Four 50 Ω terminators are required.
d. Find an appropriate zero reference point for phase shift and attenuation created by the array. Find realizable weighting values for the array using your prelab.

Note: Attenuators also produce phase shift, and phase shifters also exhibit attenuation. Allow overhead in the settings. To achieve prelab settings, several adjustment iterations may be required.

4. Place the phased array inside the anechoic chamber. Reconnect the power supplies (outside the chamber) to the array using the provided power cable.
5. Replace the transmit antenna with the 2.4GHz standard gain horn antenna.
6. Measure the radiation pattern at 2.437 GHz
7. Include the following measurements
   a. Cable loss to each antenna
   b. Effective antenna distance
   c. Horn antenna gain (obtain data sheet)

Report

1. Gain at 0° and 45°?
2. If noise sources exist at incident angles of -45° and -65° on the H-plane, what SNR does the array provide as opposed to an isotropic radiator?
3. Include 360° radiation patterns.
   a. Array radiation pattern (dBi)
      i. Hint: Use Friis Transmission formula.
   b. Single element radiation pattern (dBi)
   c. Normalized AF
4. Compare radiation pattern and AF with theory
5. Measure array VSWR
IX. Conclusion

Beam steering provides a fixed direction SIR greater than 10dB in seven of the nine signal to interference ratios calculated across the three test cases. A 10dB increase corresponds to a 10% signal quality increase as measured (and defined) by the router. Fixed null phased arrays are ideal for installations with fixed location interferers causing degradation to wireless system performance. Mobile applications require “smart” phased arrays able to detect desired signals and interferers and dynamically update the array factor. Unfortunately, system cost is prohibitive; hundreds of dollars per element. FPGAs can directly synthesize array signals with required attenuation and phase delay. As high speed FPGAs decrease in cost, direct synthesis may become a viable option.
X. Further Development

An exciting new application of array beam forming allows direct synthesis of array signals using high-speed FPGAs. Direct synthesis produces phase shifted, attenuated signals without the need for phase shifter or attenuators. Direct synthesis increases the overall efficiency and system gain. Although inhibitably expensive ($500/channel), a prototype board could be used for future senior/thesis projects.

The phased array discussed in this thesis utilizes passive potentiometers to set phase shifter bias. The array would benefit from an active biasing system, because as phase shift is increased, the current drawn by the phase shifters also increases. This makes it difficult to use passive potentiometers to set the bias. Also, the DIP switches for attenuator settings could be replaced by a digital control system.
XI. Bibliography


## Appendices

### A. Materials

Table 12 shows the materials and associated costs used for the project.

#### Table 12 Bill of Materials

<table>
<thead>
<tr>
<th>Part</th>
<th>Model Number/Desc</th>
<th>Ordered</th>
<th>Supplier</th>
<th>Unit Price</th>
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<tbody>
<tr>
<td>Phase Shifter</td>
<td>JSPHS-2484+</td>
<td>8</td>
<td>MiniCircuits</td>
<td>$32.95</td>
</tr>
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<td>Antenna</td>
<td>Nearson 614</td>
<td>8</td>
<td>Donation</td>
<td>$0.00</td>
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<tr>
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<td>Donation</td>
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<td>Attenuator</td>
<td>DAT-15R5-PP+</td>
<td>10</td>
<td>MiniCircuits</td>
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<td>Digikey</td>
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<td>SZWholesale</td>
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<td>$3.46</td>
</tr>
<tr>
<td>Potentiometers</td>
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<td>Digikey</td>
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<tr>
<td>SMA Resistors (4.7k)</td>
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<td>Donation</td>
<td>$0.00</td>
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<td>Zephytronics</td>
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</tr>
<tr>
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<td>ZT-5-022-5-2pack</td>
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</tr>
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<td>Tacky Flux</td>
<td>TSF-6850-30G</td>
<td>1</td>
<td>EIS</td>
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<tr>
<td>SMA male to SMA male adapters</td>
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<td>8</td>
<td>Universal Radio</td>
<td>$2.29</td>
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<tr>
<td>FR-4 Copper Clad Laminate</td>
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<td>2</td>
<td>Assorted</td>
<td>n/a</td>
</tr>
<tr>
<td>SMA female to SMA female adapters</td>
<td>SMA-F to SMA-F</td>
<td>8</td>
<td>Donation</td>
<td>$0.00</td>
</tr>
</tbody>
</table>
B. Matlab Simulation Code

The following code calculates required complex weights for each element to produce an array factor with chosen maximums and nulls and plots the array factor.

```matlab
clear all, close all;
N=7; %number of elements
d=1/2; %spacing in wavelengths

%% Sample Test Case
if (0) %Select Test Case: 1-Selected, 0-Unselected
    totalSignals=8; %Total number of signals
    numOfInterferers=4; %How many of the Above Signals are Interferers?
    %Incident Angles (degrees)
    Sig1=0 * (2*pi/360);
    Sig2=0 * (2*pi/360);
    Sig3=0 * (2*pi/360);
    Sig4=0 * (2*pi/360);
    Noise1=25 * (2*pi/360);
    Noise2=35 * (2*pi/360);
    Noise3=45 * (2*pi/360);
    Noise4=-45 * (2*pi/360);
    %Setup Arrays for Solution
    desiredWeights= zeros(1,totalSignals);
    for i=1:(totalSignals-numOfInterferers)
        desiredWeights(i)=1;
    end
    n=0:(N-1);
    a0= [ exp(-j*(2-n)*pi*sin(Sig1)) ]';
    a1= [ exp(-j*(2-n)*pi*sin(Sig2)) ]';
    a2= [ exp(-j*(2-n)*pi*sin(Sig3)) ]';
    a3= [ exp(-j*(2-n)*pi*sin(Sig4)) ]';
    a4= [ exp(-j*(2-n)*pi*sin(Noise1)) ]';
    a5= [ exp(-j*(2-n)*pi*sin(Noise2)) ]';
    a6= [ exp(-j*(2-n)*pi*sin(Noise3)) ]';
    a7= [ exp(-j*(2-n)*pi*sin(Noise4)) ]';
    ABar= [a0 a1 a2 a3 a4 a5 a6 a7];
end

%% Test Case 1
if (1)
    totalSignals=3;
    numOfInterferers=2;
    %Incident Angles (degrees)
    Sig1= 0 * (2*pi/360);
    Noise1= -25 * (2*pi/360);
    Noise2= 25 * (2*pi/360);
    desiredWeights= zeros(1,totalSignals);
    for i=1:(totalSignals-numOfInterferers)
        desiredWeights(i)=1;
    end
    n=0:(N-1);
    a0= [ exp(-j*(2-n)*pi*sin(Sig1)) ]';
    a1= [ exp(-j*(2-n)*pi*sin(Noise1)) ]';
    a2= [ exp(-j*(2-n)*pi*sin(Noise2)) ]';
    ABar= [a0 a1 a2];
end

%% Test Case 2
```

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if (0)
  totalSignals=5;
  numOfInterferers=2;
end

% Incident Angles (degrees)
Sig1=0 * (2*pi/360);
Sig2=90 * (2*pi/360);
Sig3=-90 * (2*pi/360);
Noise1=45 * (2*pi/360);
Noise2=-45 * (2*pi/360);

desiredWeights= zeros(1,totalSignals);
for i=1:(totalSignals-numOfInterferers)
  desiredWeights(i)=1;
end

n=0:(N-1);
a0= [ exp(-j*(2-n)*pi*sin(Sig1)) ]';
a1= [ exp(-j*(2-n)*pi*sin(Sig2)) ]';
a2= [ exp(-j*(2-n)*pi*sin(Sig3)) ]';
a3= [ exp(-j*(2-n)*pi*sin(Noise1)) ]';
a4= [ exp(-j*(2-n)*pi*sin(Noise2)) ]';
ABar=[a0 a1 a2 a3 a4];
end

%% Test Case 3
if (0)
  totalSignals=4;
  numOfInterferers=2;
end

% Incident Angles (degrees)
Sig1=0 * (2*pi/360);
Sig2=45 * (2*pi/360);
Noise1=-45 * (2*pi/360);
Noise2=-65 * (2*pi/360);

desiredWeights= zeros(1,totalSignals);
for i=1:(totalSignals-numOfInterferers)
  desiredWeights(i)=1;
end

n=0:(N-1);
a0= [ exp(-j*(2-n)*pi*sin(Sig1)) ]';
a1= [ exp(-j*(2-n)*pi*sin(Sig2)) ]';
a2= [ exp(-j*(2-n)*pi*sin(Noise1)) ]';
a3= [ exp(-j*(2-n)*pi*sin(Noise2)) ]';
ABar=[a0 a1 a2];
end

%% Calculations
sigmaNoise=.001; % noise variance
IBar=eye(length(ABar)); % Create and identity matrix the size of ABar
wBarH=desiredWeights*ABar'*inv(ABar*ABar'+sigmaNoise*IBar) % Godara method
% Convert to dB and Phase
for i = 1:N
  mag(i)= 20*log10(abs(wBarH(i)));
  phase(i)= angle(wBarH(i)) * 360/(2*pi);
end

minAtten=max(mag);
minPhase=min(phase);
for i = 1:N
  mag(i)=mag(i)-minAtten; % Normalize Attenuation to 0dB
  phase(i)=phase(i)-minPhase; % Normalize Phase Shift to 0 degrees
  disp(['For element ' num2str(i) ... % Print Results
    ', Mag: ' num2str(mag(i)) ' dB  Phase:' num2str(phase(i)) ' degrees'])
end

%% Plot AF
theta=pi:.01:pi;
degrees=theta*180/pi;
for j=1:length(theta)
    th=theta(j);
    aForElement=[exp(-1j*(2-n)*pi*sin(th)) ]';
    y(j)=wBarH*aForElement;
end
figure;
plot(degrees,abs(y)/max(abs(y)),'k')
axis([-180 180 0 1])
xlabel('\theta')
ylabel('|AF(\theta)|')