Microcontroller Design: Control Electronics

for

Rantec’s 28VDC Input 3-U Power Supply

By

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Senior Project

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# TABLE OF CONTENTS

<table>
<thead>
<tr>
<th>Section</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>Acknowledgements</td>
<td>vi</td>
</tr>
<tr>
<td>Abstract</td>
<td>vii</td>
</tr>
<tr>
<td>I. Introduction</td>
<td>1</td>
</tr>
<tr>
<td>II. Requirements and Specifications</td>
<td>2</td>
</tr>
<tr>
<td>III. Electrical Design</td>
<td>5</td>
</tr>
<tr>
<td>IV. Firmware Design</td>
<td>11</td>
</tr>
<tr>
<td>V. Graphical User Interface</td>
<td>19</td>
</tr>
<tr>
<td>VI. Test Plans</td>
<td>22</td>
</tr>
<tr>
<td>VII. Development and Construction</td>
<td>23</td>
</tr>
<tr>
<td>VIII. Integration and Test Results</td>
<td>24</td>
</tr>
<tr>
<td>IX. Problems Encountered</td>
<td>32</td>
</tr>
<tr>
<td>X. Conclusion</td>
<td>34</td>
</tr>
<tr>
<td>XI. References</td>
<td>35</td>
</tr>
<tr>
<td>Appendices</td>
<td></td>
</tr>
<tr>
<td>A. Senior Project Analysis</td>
<td>37</td>
</tr>
<tr>
<td>B. Project Planning</td>
<td>40</td>
</tr>
<tr>
<td>C. Parts and Cost Estimate</td>
<td>47</td>
</tr>
<tr>
<td>D. Source Code</td>
<td>50</td>
</tr>
<tr>
<td>E. PC Board Layout</td>
<td>75</td>
</tr>
</tbody>
</table>
## LIST OF TABLES AND FIGURES

<table>
<thead>
<tr>
<th>Table</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>I. Requirements Specifications for the Control Electronics</td>
<td>2</td>
</tr>
<tr>
<td>II. Output Voltage Control</td>
<td>4</td>
</tr>
<tr>
<td>III. Level 0 Functionality</td>
<td>6</td>
</tr>
<tr>
<td>IV. Recommended Resistor Values to Scale Down the Power Supply Voltages</td>
<td>8</td>
</tr>
<tr>
<td>V. I²C Analog-to-Digital Converter Address</td>
<td>8</td>
</tr>
<tr>
<td>VI. Microcontroller’s I²C Address Select</td>
<td>9</td>
</tr>
<tr>
<td>VII. Microcontroller’s Transmit Register Assignment</td>
<td>11</td>
</tr>
<tr>
<td>VIII. Microcontroller’s Transmit Register Assignment (continued)</td>
<td>11</td>
</tr>
<tr>
<td>IX. Microcontroller’s Transmit Register Description</td>
<td>11</td>
</tr>
<tr>
<td>X. Microcontroller’s Transmit Register Description (continued)</td>
<td>11</td>
</tr>
<tr>
<td>XI. /ENABLE and /INHIBIT Test Results</td>
<td>26</td>
</tr>
<tr>
<td>XII. Parts List</td>
<td>47</td>
</tr>
<tr>
<td>XIII. Labor Hours</td>
<td>48</td>
</tr>
<tr>
<td>XIV. Material Cost</td>
<td>49</td>
</tr>
<tr>
<td>XV. Labor &amp; Material Cost</td>
<td>49</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Figure</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Turn-On Sequence</td>
<td>3</td>
</tr>
<tr>
<td>2. Level 0 Control Electronics Block Diagram</td>
<td>5</td>
</tr>
<tr>
<td>3. Resistor Divider for ADC Input</td>
<td>8</td>
</tr>
<tr>
<td>4. Level 1 Circuit Diagram</td>
<td>10</td>
</tr>
<tr>
<td>5. System State Diagram</td>
<td>13</td>
</tr>
<tr>
<td>6. Main Program Flow Chart</td>
<td>14</td>
</tr>
<tr>
<td>7. Meas_Volt Function Flow Chart</td>
<td>16</td>
</tr>
<tr>
<td>8. Meas_Temperature Function Flow Chart</td>
<td>17</td>
</tr>
<tr>
<td>9. Two-Wire Master Function Flow Chart</td>
<td>18</td>
</tr>
</tbody>
</table>
10. Control Electronics Front Panel........................................................................................................19
11. Control Electronics Block Diagram..................................................................................................21
12. Board with 7 ADCs and I²C Isolators .................................................................................................23
13. Board with Temperature Sensor ..........................................................................................................23
14. Control Electronics Bare Board........................................................................................................23
15. Partially Built Board ............................................................................................................................23
16. Internal Two-Wire Interface Communication ....................................................................................23
17. Measured Output Voltages ..................................................................................................................24
18. Measured DC_Bus & Input Voltage ......................................................................................................24
19. Overvoltage Condition..........................................................................................................................25
20. Measured Temperature .........................................................................................................................25
21. Over temperature .................................................................................................................................25
22. +5V and /SYSRESET Waveforms ......................................................................................................26
23. /SYSRESET and /FAIL Status Signals ..............................................................................................26
24. Enable Turn-On Sequence ...................................................................................................................27
25. LabVIEW I²C Device Driver ...............................................................................................................28
26. NI-8451 Two-Wire Interface Waveforms..........................................................................................29
27. TWI Master-Slave Device Testing .......................................................................................................30
28. TWI Waveforms Master – Maser Device Writing Data to the Microcontroller ..................................30
29. TWI Waveforms – Master Device Reading Data from the Microcontroller ......................................31
30. Final Hardware, Firmware, and Software Integration ........................................................................31
31. Senior Project Expo ..............................................................................................................................31
32. Schematic and Photograph of the Missing Capacitor .........................................................................33
33. Original Gantt Chart for the Senior Project—Pane 1 .........................................................................40
34. Original Gantt Chart for the Senior Project—Pane 2 .........................................................................41
35. Original Gantt Chart for the Senior Project—Pane 3 .........................................................................42
36. Original Gantt Chart for the Senior Project—Pane 4 .........................................................................43
37. Actual Gantt Chart for the Senior Project—Pane 1 ...........................................................................44
38. Actual Gantt Chart for the Senior Project—Pane 2 ...........................................................................45
39. Actual Gantt Chart for the Senior Project—Pane 3.................................................................46
40. Control Electronics PC Board Layout.....................................................................................75
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Abstract

The Control Electronics is a real-time data acquisition and control system based on Atmel’s AVR microcontroller. The Control Electronics will be embedded into Rantec’s 3U power supply to monitor and report power supply data [1]. The power supply data monitored includes power supply output voltages, input voltage, DC bus voltage, and temperature. The microcontroller continuously monitors DC voltages through seven $I^2C$ compatible analog-to-digital converters via a custom Two-Wire interface. An analog temperature sensor measures power supply data and feeds it as an analog voltage to the microcontroller’s built-in analog to digital interface. The microcontroller stores the acquired power supply data into a buffer for easy processing and to maximize its performance. The Control Electronics reports the power supply data to an external Two-Wire master device via the microcontroller’s built-in Two-Wire interface, configured as slave. This project describes the development and implementation of the Control Electronics data acquisition and control system.
I. Introduction

Rantec is an engineering company that designs and manufactures a variety of ruggedized switching power supplies (DC-DC converters). Over the last four years, Rantec has supplied various power supplies with prognostic capabilities to several of its customers. The Control Electronics project attempts to embed a microcontroller into Rantec’s 3U VME power supply to monitor and report power supply data. Rantec recommended Atmel’s AVR 8-bit microcontroller [2] to be part of The Control Electronics project instead of a Complex Programmable Logic Device (CPLD) or a Field Programmable Gate Array (FPGA) for data acquisition and system control, due to its flexibility for timing requirements and its low cost. Also, due to Rantec’s experience in programming in C-language and in LabVIEW development environment, this project used the CodeVision AVR along with Atmel’s Studio C-Compiler to develop the firmware and program the microcontroller. LabVIEW development environment was used to design the Graphical User Interface (GUI) for the user to view and analyze the power supply data.

The Control Electronics features include power supply real-time data acquisition, power supply overvoltage and overtemperature protection. The Control Electronics’ fundamental components include one microcontroller, seven \( \text{I}^2\text{C} \) analog-to-digital converters, and one temperature sensor. Rantec selected Atmel’s ATmega 164P/364P microcontroller, National Semiconductor’s ADC121C021 \( \text{I}^2\text{C} \)-compatible 12-bit analog-to-digital converter, and Texas Instruments’ LM94021 analog temperature sensor for the data acquisition system.

This report discusses the electrical, firmware, and software design of the Control Electronics. The electrical design consists selecting and integrating commercial off-the-shelf components (COTS) into a printed circuit. The printed circuit board contains a Joint Test Action Group interface, also known as a JTAG interface. The JTAG interface provides easy access for programming the microcontroller and for on-chip debugging. Section III discusses the electrical design in greater detail.

The firmware design includes developing and implementing C-Code drivers for data gathering and external communication. Specifically, Rantec requested the development of a Two-Wire Interface Master Bit Bang driver for the communication between the microcontroller and seven \( \text{I}^2\text{C} \) analog to digital converters. This driver implements the internal Two-Wire communication protocol by bit banging two microcontroller GPIO pins, configured as TWI_SDA and TWI_SCL signals [5]. This approach was chosen due to the limited microcontroller Two-Wire interface resources. Section IV explains the development of C-Code drivers in detail.

The software design describes the development and testing of a Graphical User Interface (GUI). This software application contains graphical icons and visual indicators to allow the user to interact with the Control Electronics.
II. Requirements and Specifications

Background
Rantec’s 3U power supply is a 28VDC input DC-DC Converter with 5 DC output voltages. The DC output voltages are: +3.3V, +5V, +12V, +3.3V_AUX, and -12V_AUX. The 3U power supply has two control signals, /ENABLE and /INHIBIT, and three status signals, /SYSRESET, /FAIL, and INPUT_LOW. The goal of this project is to design and embed a microcontroller-based system into Rantec’s 3U power supply.

Electrical Performance
Table I contains the Control Electronics requirements and specifications. These requirements and specifications derive directly from Rantec’s document SD35798A.

<table>
<thead>
<tr>
<th>Marketing Requirements</th>
<th>Engineering Specifications</th>
<th>Justification</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>The Control Electronics monitors the output voltages, input voltage, and intermediate bus voltage of the 3U power supply.</td>
<td>This feature provides real time output voltage data.</td>
</tr>
<tr>
<td>2</td>
<td>The output voltage readback accuracy is ±1%. The voltage resolution is 100mV.</td>
<td>Voltage readback accuracy of ±1% is typical in the power supply industry.</td>
</tr>
<tr>
<td>3</td>
<td>The Control Electronics monitors the temperature of the 3U power supply.</td>
<td>The temperature of the power supply is monitored to prevent a catastrophic failure.</td>
</tr>
<tr>
<td>4</td>
<td>The temperature readback accuracy is ±3°C.</td>
<td>The temperature readback accuracy</td>
</tr>
<tr>
<td>5</td>
<td>The Control Electronics reports power supply voltage and temperature data to the Master Unit via the I2C interface bus.</td>
<td>The Master Unit could be a computer or the next assembly such as an aircraft or a vehicle.</td>
</tr>
<tr>
<td>6</td>
<td>All output voltages are enabled/disabled individually by the microcontroller.</td>
<td>The outputs are enabled individually to reduce input inrush current.</td>
</tr>
<tr>
<td>3,4,6,7</td>
<td>All outputs are protected against an over temperature condition. Should the chassis temperature of the power supply exceed +85°C (+5°C / -0°C), all of the outputs are turned off by the microcontroller.</td>
<td>The temperature of the power supply is monitored and if the temperature exceeds +85°C, then the outputs are turned off. The overtemperature feature prevents a catastrophic failure.</td>
</tr>
<tr>
<td>1,2,6,8</td>
<td>The Control Electronics reports an overvoltage condition to an external I2C device if the output voltage is between 110-125% of the rated output. The output shall be turned off by the microcontroller if an over voltage condition occurs.</td>
<td>The overvoltage protection feature prevents the output voltage from reaching a dangerous level.</td>
</tr>
<tr>
<td>9</td>
<td>The /ENABLE and /INHIBIT control signals determine which output is turned on. See Table II.</td>
<td>These control signals allow the user to turn on or off the power supply output voltages without interrupting the input voltage.</td>
</tr>
</tbody>
</table>
The **/SYSRESET** signal is asserted high 200mS after the 5V Output is within specification following turn-on.

The **/SYSRESET** status signals allows the user to visually identify a fault with the +5V output voltage. The timing requirement required by the customer.

The **/FAIL** signal is asserted open collector only when all of the outputs are nominally within +/-10% of their output voltage rating.

The **/FAIL** indicates that the power supply is operating in a normal condition.

The operating temperature range is -55°C to +85°C.

The Control electronics must operate at extreme temperatures.

The storage temperature range is -55°C to +125°C.

The Control electronics must survive extreme temperatures.

The labor and parts cost not exceed $9,000

The budget is based on the estimated labor and material cost.

The Two-Wire Interface Bit Bang driver Is compatible with I2C devices.

Marketing Requirements

1. Output voltage monitor
2. Output voltage accuracy and resolution
3. Temperature monitor
4. Temperature readback accuracy
5. External communication interface
6. Output voltages enabled/disabled
7. Thermal shutdown protection
8. Over voltage protection
9. /ENABLE & /INHIBIT control inputs
10. /SYSRESET status signal
11. /FAIL status signal
12. Extreme operating temperature
13. Extreme Storage Temperature
14. Low Cost
15. Two-Wire Interface Bit Bang Driver

The requirements and specifications table format derives from [3], Chapter 3

**Turn-on Sequencing**

Upon initial application of the input voltage, all of the outputs are sequenced enabled by the microcontroller. See Figure 1 for the turn-on sequencing.

![Figure 1: Turn-On Sequence](image-url)
Table II shows the control inputs that enable the power supply outputs.

<table>
<thead>
<tr>
<th>Control Input</th>
<th>Power Supply Outputs</th>
</tr>
</thead>
<tbody>
<tr>
<td>/ENABLE</td>
<td>/INHIBIT</td>
</tr>
<tr>
<td>High</td>
<td>High</td>
</tr>
<tr>
<td></td>
<td>Off</td>
</tr>
<tr>
<td>High</td>
<td>Low</td>
</tr>
<tr>
<td></td>
<td>Off</td>
</tr>
<tr>
<td>Low</td>
<td>High</td>
</tr>
<tr>
<td></td>
<td>On</td>
</tr>
<tr>
<td>Low</td>
<td>Low</td>
</tr>
<tr>
<td></td>
<td>On</td>
</tr>
</tbody>
</table>

**TABLE II: OUTPUT VOLTAGE CONTROL**

**EMI, Operating temperature, Operating Lifetime, Weight, Dimensions, and Packaging**

Electromagnetic Interference (EMI), operating temperature, operating lifetime, weight, dimensions, and packaging is addressed at the power supply level.
III. Electrical Design

Level 0 Block Diagram
Figure 2 shows the Level 0 block diagram for the Control Electronics. The inputs are power supply temperature, power supply input and output voltages, and control inputs. The outputs are the power supply outputs status signals and the control outputs. The communication interface is the I²C interface.

Figure 2: Level 0 Control Electronics Block Diagram
Table III describes the Control Electronics Level 0 Functionality.

**TABLE III: LEVEL 0 FUNCTIONALITY**

<table>
<thead>
<tr>
<th>Module</th>
<th>Control Electronics</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Inputs</strong></td>
<td>TEMPERATURE - temperature of the unit /ENABLE - Control signal to enable the power supply /INHIBIT - Control signal to enable the power supply INPUT VOLTAGE - Input voltage INTERMEDIATE BUS VOLTAGE - Bus voltage +3.3V - Output voltage / 0 to 20A +5V - Output voltage / 0 to 20A +12V - Output voltage / 0 to 5A -12V_AUX - Output voltage / 0 to 0.5A +3.3V_AUX - Output voltage / 0 to 4A I2C ADDRESS SELECT - used to set the Control Electronics I2C address</td>
</tr>
<tr>
<td><strong>Outputs</strong></td>
<td>+3.3V_ENABLE - 0 to 5VDC signal +5V_ENABLE - 0 to 5VDC signal +12V_ENABLE - 0 to 5VDC signal +3.3V_AUX_ENABLE - 0 to 5VDC signal -12V_AUX_ENABLE - 0 to 5VDC signal INPUT_LOW - Open-collector TTL signal /SYSRESET - Open-collector TTL signal /FAIL - Open-collector TTL signal</td>
</tr>
<tr>
<td><strong>Communication Interface</strong></td>
<td>I2C compatible Serial Interface.</td>
</tr>
</tbody>
</table>

**Inputs**
The Control Electronics monitors the power supply temperature. The /ENABLE and the /INHIBIT are Transistor-Transistor Logic (TTL) control inputs that determine which outputs are enabled by the Control Electronics; refer to table II for information on the output voltage control. The power supply input voltage, intermediate bus voltage, +3.3V, +5V, +12V, +3.3V_AUX, and -12V_AUX output voltages go into the Control Electronics for data analysis.

**Outputs**
The control outputs are +3.3V_ENABLE, =5V_ENABLE, +12_ENABLE, +3.3V_AUX, -12V_AUX. These signals enable and disable the power supply output voltages. The status signals indicate the power supply status. The /INPUT_LOW signal indicates that the power supply input voltage is below the threshold voltage. The /SYSRESET signal indicates that the +5V output voltage is within ± 1% of the nominal voltage. The /FAIL signal indicates that all output voltages are within ±10% of their nominal voltage.
**Communication interface**
The Control Electronics uses the I²C interface and operates in slave mode only. The I²C has a 7-bit hardware address also known as the slave address. The slave address is configured by the I²C ADDRESS SELECT signals and the default slave address is 0x27.

**Level 1 Circuit Diagram**
A myriad of microcontrollers, I²C compatible analog-to-digital converters, and temperature sensors are available for the Control Electronics project. Rantec’s Director of Test-Engineering and Software Development selected all the components for this project.

Figure 4 shows the level 1 circuit diagram. The Parts Lists is found in Appendix C and the PB board layout is found in Appendix E. The Control Electronics project is an embedded system that includes one ATmega164P/324P microcontroller, seven I²C-compatible analog-to-digital converters (ADCs), one analog temperature sensor, JTAG interface, and an LED. The microcontroller performs the following functions:

**Voltage Monitor**
The Controls Electronics configures the microcontroller’s PD0 and PD1 pins for Two-Wire Interface communication. PD0 is the SCL signal and PD1 is the SDA signal. Pull up resistors are necessary for the Two-Wire Interface to operate correctly. The maximum value of the pull-resistors is a function of the bus capacitance and the SDA/SCL maximum rise time. The following formula determines the maximum value for the pull-up resistors [4].

Given parameters [5];

\[
\begin{align*}
\text{Maximum rise time (from datasheet)} & \quad t_r := 1000 \times 10^{-9} \text{s} \\
\text{Input pin capacitance (from datasheet)} & \quad C_{\text{in}} := 30 \text{pF} \\
\text{Number of devices connected to the Control Electronics I2C bus} & \quad N_{\text{dev}} := 7 \\
\text{Bus capacitance} & \quad C_b := C_{\text{in}} \cdot N_{\text{dev}} \\
\text{Maximum pull-up resistor value} & \quad R_{P_{\text{max}}} = \frac{t_r}{0.8473C_b} \quad R_{P_{\text{max}}} = 5.62\Omega 
\end{align*}
\]

For safety margin, a 2kΩ pull-up resistor values were chosen for the internal Two-Wire Interface.
The microcontroller uses this internal I²C interface to communicate with seven I²C single-channel analog-to-digital converters (ADC121C021CIMM) to retrieve power supply voltage. The I²C analog-to-digital converters continuously monitor the power supply input and output voltages. The measured voltage is fed to the microcontroller. The voltage is formatted to a digital value and stored in the TX_Reg_Data array, see tables 7-10 for more information on the TX_Reg_Data array.

The input voltage to the analog-to-digital converters cannot exceed the reference voltage; therefore, resistor dividers are used to scale down the power supply voltage. Figure3 shows the recommended resistor values to scale down the power supply voltage.

![Figure 3: Resistor Divider for ADC Input](image)

| TABLE IV: RECOMMENDED RESISTOR VALUES TO SCALE DOWN THE POWER SUPPLY VOLTAGES |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|
| DC Voltage     | +3.3V           | +5V            | 12V            | +3.3_AUX       | -12V_AUX       | DC Bus         | 28V            |
| R1 in Ω        | 1.0E+3          | 8.91E+03       | 20.0E+3        | 1.0E+3         | 20.0E+3        | 30.1E+3        | 150.3E+3       |
| R2 in Ω        | 10.0E+3         | 9.8E+3         | 7.87E+03       | 10.0E+3        | 7.87E+03       | 3.7E+3         | 14.8E+3        |

Since all seven I²C analog-to-digital converters share the same Two-Wire interface bus, each device has a unique I²C address. Table V describes the analog-to-digital converter I²C address.

| TABLE V: I²C ANALOG-TO-DIGITAL CONVERTER ADDRESS |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|
| I²C Device     | +3.3V           | +5V            | 12V            | +3.3_AUX       | -12V_AUX       | DC Bus         | 28V            |
| I²C Hex Address| 0x50            | 0x51           | 0x52           | 0x54           | 0x55           | 0x56           | 0x58           |

**Overvoltage Protection**

The I²C analog-to-digital converters contain an Alert pin that is asserted when the power supply output voltage exceeds the programmed threshold [4]. The Alert pins are routed to port B of the microcontroller, see Figure 3. The microcontroller immediately disables the output that has the overvoltage condition. The output is re-enabled by sending the command 0x0A to the controller via the external I²C bus or by recycling input power.
**Control Inputs**
The /ENABLE and /INHIBIT are control inputs that are fed PB5 and PB6 of the microcontroller. These signals indicate the microcontroller which power supply outputs to enable during initial power-up; refer to table II to determine which states enable the power supply output voltages.

**Control Outputs**
The /SYSRESET, /FAIL, and INPU_LOW are output signals from the microcontroller. PA5 pin of the microcontroller indicates the /SYSRESET state. The microcontroller asserts the /SYSRESET when the +5V output is within 1% of the nominal voltage. PA6 pin of the microcontroller indicates the /FAIL state. The microcontroller asserts the /FAIL when all output voltages are within 10% of their nominal voltage. PC6 pin of the microcontroller indicates the /INPUT_LOW state. The microcontroller asserts the /INPUT_LOW signal when the power supply input voltage falls below 12.5VDC.

**Temperature Monitor**
The analog temperature sensor (LM94021) measures the temperature of the power supply. The temperature sensor output voltage is inversely proportional to the measured power supply temperature [6]. The sensor voltage is fed to the analog-to-digital converter, port 7, of the microcontroller and is converted to a digital value and stored in the Temp_Data array [5].

**Overtemperature**
The microcontroller disables all outputs if the power supply temperature exceeds 85°C

**I^2C Communication Interface**
The Control Electronics uses the microcontroller’s built-in Two-Wire interface that is compatible with Philips I^2C interface for external serial communication [3]. The Control Electronics monitors and stores power supply data in the TX_Reg_Data array. An external I^2C device can access this data by sending the microcontrollers I^2C address followed by a read command. The microcontroller’s I^2C address is set by the I^2C_GA0 and I^2C_GA1 input signals. The default address is 0x27. Table VI describes how to set the microcontroller’s I^2C address.

<table>
<thead>
<tr>
<th>I^2C_GA1</th>
<th>I^2C_GA0</th>
<th>I^2C Hex Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>LOW</td>
<td>LOW</td>
<td>0x30</td>
</tr>
<tr>
<td>LOW</td>
<td>HIGH</td>
<td>0x29</td>
</tr>
<tr>
<td>HIGH</td>
<td>LOW</td>
<td>0x28</td>
</tr>
<tr>
<td>HIGH</td>
<td>HIGH</td>
<td>0x27</td>
</tr>
</tbody>
</table>

**JTAG Interface**
The Control Electronics uses the JTAG interface to program the microcontroller and perform on-chip real time debugging.

**Status LED**
The Control Electronics toggles an LED every 350ms to indicate that the microcontroller is cycling thought the program. The LED staying On or Off all the time indicates that the microcontroller got stuck in a while loop.
Figure 4: Level 1 Circuit Diagram
IV. Firmware Design

This project used CodeVisionAVR C compiler to develop the firmware, per Rantec’s Director of Test Engineering and Software Development recommendation.

Serial communication protocol

The Control Electronics project uses the Phillips I²C serial communication protocol to communicate with an external I²C master device, via the microcontroller’s built-in I²C interface. To access power supply data from the Control Electronics, the I²C master device sends a read command to the ATMega microcontroller and the microcontroller returns up to 22 bytes of data. It’s the I²C master’s responsibility to indicate the number of bytes to expect from the microcontroller in one read. For more information on the I²C communication protocol, refer to Phillips UM10204 I²C-bus specification and user manual.

Tables VII through X describes the power supply data stored in the microcontroller’s transmit registers.

<table>
<thead>
<tr>
<th>TABLE VII: MICROCONTROLLER’S TRANSMIT REGISTER ASSIGNMENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte Number</td>
</tr>
<tr>
<td>Register Description</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TABLE VIII: MICROCONTROLLER’S TRANSMIT REGISTER ASSIGNMENT (CONTINUED)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte Number</td>
</tr>
<tr>
<td>Register Description</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TABLE IX: MICROCONTROLLER TRANSmit REGISTER DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte</td>
</tr>
<tr>
<td>-----</td>
</tr>
<tr>
<td>1-0</td>
</tr>
<tr>
<td>3-2</td>
</tr>
<tr>
<td>5-4</td>
</tr>
<tr>
<td>7-6</td>
</tr>
<tr>
<td>9-8</td>
</tr>
<tr>
<td>11-10</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>TABLE X: MICROCONTROLLER TRANSmit REGISTER DESCRIPTION (CONTINUED)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Byte</td>
</tr>
<tr>
<td>------</td>
</tr>
<tr>
<td>13-12</td>
</tr>
<tr>
<td>15-14</td>
</tr>
<tr>
<td>16</td>
</tr>
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<td>17</td>
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<td>18</td>
</tr>
<tr>
<td>19</td>
</tr>
<tr>
<td>20</td>
</tr>
<tr>
<td>21</td>
</tr>
</tbody>
</table>
System State Diagram
Figure 5 shows the microcontroller’s System State Diagram. This diagram describes the behavior of the ATMega microcontroller which includes the main loop and four interrupt routines developed in this project [7]. The interrupt routines include: The Timer0_Interrupt, Timer2_Interrupt, TWI_Interrupt, and OVP_Interrupt routine. The Timer0_Interrupt updates power supply input and output voltage data every 317mS. The Timer2_Interrupt maintains the system clock and updates temperature data every 750mS. The TWI Interrupt waits for the START signal from the external Two-Wire Interface master device to send or receive data(9). The OVP_Interrupt triggers when there is a power supply output overvoltage condition.

![System State Diagram](image)

Figure 5: System State Diagram
Main Program

Figure 6 shows the main program flow chart.

Figure 6: Main Program Flow Chart
The Main program initializes the microcontroller’s ports A through D, Timer 0 & Timer 2, the Pin Change interrupts, analog-to-digital converter port 7, and the Two-wire interface. The Two-wire interface 7-bit slave address is set to 0x27. Next, the program enables the global interrupts. After that, the program initializes the I2C 12-bit analog-to-digital converters (ADC121C021). Afterwards, the program monitors the /ENABLE and the /INHIBIT control inputs and enables certain output voltages; refer to table II to determine which control input states enable/disable the power supply output voltages.

After the hardware and software initialization, the program enters the main loop. The following steps describe functions that are executed main loop:

First, the program toggles the Program Status LED. The LED blinks every 250mS and provides a visual aid to the user indicating that the program is cycling through the main loop successfully. The LED not blinking that reveals that the program got stuck in a loop and the user must recycle the input power to reset the program.

Second, the program runs the Meas_Volt function to retrieve 14 bytes of voltage data from the seven I2C 12-bit analog-to-digital converters and stores the data in the TX_Reg_Data array; refer to table VII for more information on the stored voltage data. See figure 6 for more information on the Meas_Volt function.

Third, the program monitors the RX_Reg_Data(3). The 0x0A value in this register enables all power supply output voltages and a value of 0x0C disables all power supply output voltages.

Fourth, the program executes the Meas_Temperature to retrieve 2 bytes of voltage data. The voltage data is compared to a value in look-up table and translated to a temperature. The formatted temperature value is stored in the TX_Reg_Data array; refer to table VIII for more information on the stored temperature data.

Fifth, the program analyzes the input voltage data stored in TX_Reg_Data(12) & TX_Reg_Data(13) and updates the TX_Reg_Data(19) register. If the input voltage is below 12.5V, the program updates the TX_Reg_Data(19) register with the value 0x0D. Otherwise, the program sets the TX_Reg_Data(19) register to 0x00.

Sixth, the program analyzes the +5V output voltage data stored in TX_Reg_Data(2) & TX_Reg_Data(3) and updates the TX_Reg_Data(20) register. If the +5V output voltage is within 4.95V and 5.05V, the program updates the TX_Reg_Data(20) register with the value 0x0A. Otherwise, the program sets the TX_Reg_Data(20) register to 0x00.

Last, the program analyzes the output voltage data stored in TX_Reg_Data(0) through TX_Reg_Data(11) and updates the TX_Reg_Data(21) register. If all output voltages are within 10% of their nominal voltage, the program updates the TX_Reg_Data(21) register with the value 0x0B. Otherwise, the program sets the TX_Reg_Data(21) register to 0x00.

After this step, the program returns to the beginning of the main loop.
C-Code functions

Meas_Volt Function
Figure 7 shows the Meas_Volt function flow chart. This function retrieves voltage from the i²C analog-to-digital converters. The microcontroller initiates a conversation with the ADC121C021 and waits for the device to acknowledge. If the device acknowledges and the data is valid, the program multipliers the voltage data from the i²C device by a scaling factor [8]. Figure 6 describes the scaling factors for the power supply output voltages. The formatted voltage data is stored in the TX_Reg_Data [0] through TX_Reg_Data [11] registers.

<table>
<thead>
<tr>
<th>Index</th>
<th>Signal Name</th>
<th>ADC Address</th>
<th>Scale Factor</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>+3.3V</td>
<td>0x50</td>
<td>0.10</td>
</tr>
<tr>
<td>1</td>
<td>+5V</td>
<td>0x51</td>
<td>0.1913846</td>
</tr>
<tr>
<td>2</td>
<td>+12V</td>
<td>0x52</td>
<td>0.10</td>
</tr>
<tr>
<td>3</td>
<td>+3.3V_AUX</td>
<td>0x54</td>
<td>0.10</td>
</tr>
<tr>
<td>4</td>
<td>-12V_AUX</td>
<td>0x55</td>
<td>0.10</td>
</tr>
<tr>
<td>5</td>
<td>DC_Bus</td>
<td>0x58</td>
<td>1.1134861</td>
</tr>
<tr>
<td>6</td>
<td>Vin</td>
<td>0x58</td>
<td>1.1134861</td>
</tr>
</tbody>
</table>

The data measured (adcdata) is multiplied by a scaling factor (ScaleFact) and is stored in the voltdata variable for \( i = 0; \ i < 7; \ i++ \).

Figure 7: Meas_Volt Function Flow Chart
**Meas_Temperature Function**

Figure 8 shows the Meas_Temperature function flow chart. The microcontroller starts the conversation with the LM94021 temperature sensor through its analog-to-digital port 7. After the conversion is complete and if there is no timeout, the voltage data is compared to a value in a lookup table and converted to a temperature value. The formatted temperature data is stored in TX_Reg_Data(14) & TX_Reg_Data(15) registers.

![Figure 8: Meas_Temperature Function Flow Chart](image-url)
**TWI_Master Function**

Figure 9 shows the TWI_Master function flow chart. This function implements the Two-wire interface communication protocol by the bit banging technique and is compatible with the I²C protocol. The bit banging method was chosen due to its low cost implementation that utilizes software instead of hardware to handle the timing and synchronization requirements for serial communication [10], [11], [12].

![Two-Wire Interface Bit-Bang C-Driver Flow Chart](image-url)
The function configures the microcontroller’s GPIO pins, PD0 and PD1, for Two-Wire serial communication, which is compatible with the I²C interface. PD0 is the SCL signal and PD1 is the SDA. The master device (the microcontroller) sends the start condition followed the slave 7-bit address and the Read or Write command to the I²C device. The microcontroller waits for the slave device to acknowledge. If the slave device acknowledges, then the microcontroller sends or receives n-bytes of data. If the slave device does not acknowledge, a timeout occurs and the program exits this function. The TWI_Master function supports up to 100 KHz clock frequency. Almost any two microcontroller GPIO pins can be configured for Two-Wire interface communication. For more information on the I²C communication protocol, refer to Phillips UM10204 I²C-bus specification and user manual.

V. Graphical User Interface

A Graphical user interface is required to retrieve the Control Electronics data. National Instruments LabVIEW development environment was chosen to develop the Graphical User Interface due to its flexibility to integrate hardware and software. Figure 10 shows the Control Electronics Graphical User Interface.
**Graphical User Interface Features**

*Auto Scan*
The user clicks the enable control to automatically retrieve data from the microcontroller. The default scan interval is 750 milli-seconds and can be updated by the user.

*Enable Outputs*
The user commands the Control Electronics to enable and disable the power supply outputs by clicking the ON or OFF buttons.

*Output Voltage Data*
Power supply output voltage data is displayed as well as the enable/disable and overvoltage protection status.

*Input Voltage Data*
Power supply input voltage is displayed as well as the INPUT_LOW status.

*Status Signals*
The /SYSRESET signal indicates when the power supply +5V output is within ±1% of its nominal voltage. The /FAIL signals indicates when all power supply outputs are within ±10% of their nominal voltage.

*Power Supply Temperature*
The power supply temperature is displayed as well as the overtemperature status.
Figure 11 shows the Control Electronics Block Diagram. This diagram shows the Graphical User Interface source code. The code is executed from left to right. First, the variables are initialized. Then the program enters the main loop. In the main loop, the program waits for user input. When the user selects the auto scan feature, the program requests data from the Control Electronics at a user defined scanned interval.

![Control Electronics Block Diagram](image)

Figure 11: Control Electronics Block Diagram
VI. Test Plans

The following was used to complete this senior project.

**Software**
- LabVIEW 2010-2012 student version development environment
- Atmel Studio 5.1 & 6.1 C-Compiler
- CodeVision AVR V3.05, V3.06, and V3.07 C-Compiler
- MS Office Visio student version

**Hardware**
- Atmel STK600 development board
- JTAG ICE II debugger
- NI-8451 USB-I^2^C interface
VII. Development and Construction

Printed Circuit Board construction

Figure 12 shows seven ADC121C021 I^2C analog-to-digital converters and three I^2C isolators soldered to a prototype. A 10-pin header was soldered to clip test leads and allow easy debugging. Figure 13 shows the LM94021 temperature sensor soldered to PC board. These two boards were used to develop and the initial firmware.

Prototype I

Figure 12: Board with 7 ADCs and I2C Isolators

Prototype II

Figure 14: Control Electronics Bare Board

Figure 15: Partially Built board
VIII. Integration and Test Results

Output Voltage Monitor and Resolution
Figure 16 shows the Internal Two-Wire interface communication. The microcontroller uses the Two-Wire interface bit bang driver to monitor the power supply input and output voltages every 12.8mS. Figure 17 shows the measured output voltages. Figure 18 shows the measured DC_Bus and Input voltage. The measured voltage resolution is 10 milli-volts and is well within the required 100 milli-volts.

Figure 16: Internal Two-Wire Interface Communication

Figure 17: Measured Output Voltages

Figure 18: Measured DC_Bus & Input Voltage
Overvoltage Protection
Figure 19 shows the overvoltage condition. The Control Electronics reports an overvoltage condition if any of the power supply output voltages exceed 125% of the rated output. The microcontroller disables the power supply output that causes an overvoltage condition.

Temperature Monitor and Over Temperature
The Control Electronics monitors temperature. The temperature readback accuracy will be verified and the system level. Figure 20 shows the measured temperature. Figure 21 shows the Control Electronics reporting an over temperature condition. The control Electronics disables all of the power supply output voltages if the power supply temperature exceeds +85 °C. The operating and storage temperature will be verified at the system level.
/ENABLE and /INHIBIT

Table XI describes the /ENABLE and /INHIBIT test results. The Control Electronics allows the user to control the logic states of the /ENABLE and /INHIBIT signals to turn ON or OFF the power supply output voltages.

<table>
<thead>
<tr>
<th>Control Input</th>
<th>Power Supply Outputs</th>
<th>Test Results</th>
</tr>
</thead>
<tbody>
<tr>
<td>/ENABLE</td>
<td>/INHIBIT</td>
<td>3.3V_AUX</td>
</tr>
<tr>
<td>High</td>
<td>High</td>
<td>Off</td>
</tr>
<tr>
<td>High</td>
<td>Low</td>
<td>Off</td>
</tr>
<tr>
<td>Low</td>
<td>High</td>
<td>On</td>
</tr>
<tr>
<td>Low</td>
<td>Low</td>
<td>On</td>
</tr>
</tbody>
</table>

/SYSRESET and /FAIL Status Signals

Figure 22 shows the +5V and /SYSRESET waveforms. The Control Electronics asserts the /SYSRESET signal 200mS after the +5V output is within ±1% of the nominal voltage. The Control Electronics asserts the /FAIL signal when all of the power supply output voltages are within ±10% of their output voltage rating. Figure 23 shows the Control Electronics asserting the /SYSRESET and /FAIL signals.
Turn-On/Enable Sequence

Figure 24 shows the Enable turn-on sequence. The active low signals enable the power supply outputs and will be calibrated at the system level to satisfy the turn-on specification from figure 1.

Figure 24: Enable Turn-On Sequence
External Master Two-Wire Interface
A National Instruments Ni-8451 I²C/SPI device emulates a master TWI that communicates with the Control Electronics.

The drivers were developed using the LabVIEW Student Edition development environment. Figure 25 shows the LabVIEW Front Panel and Block Diagram I²C device driver.

Figure 25: LabVIEW I²C Device Driver
Figure 26 shows the Two-Wire Interface Waveforms (SCL and SDA). The NI-8451 $I^2$C/SPI device generates the SCL and SDA waveforms.

This test verifies that the LabVIEW $I^2$C drivers command the NI-8451 device to generate a 100 kHz Clock (SCL) and send the 7-bit address with R/W. The MSB is sent first via the SDA line. The ninth cycle is where the Two-Wire interface device pulls SDA low to acknowledge that the data was received successfully. Figure 26 shows that the SDA signal is high during the ninth cycle, since the Two-Wire interface device is not connected to the Two-Wire Interface bus; therefore, a Not Acknowledge is expected.
Master and Slave Two-Wire Interface Integration
Figure 27 shows a picture of the initial Two-wire master/Slave setup. The photograph demonstrates the hardware, firmware, and software integration. The computer communicates with the microcontroller via the NI-USB to $I^2C$ device. The scope captures the SDA and SCL waveforms.

![Figure 27: TWI Master-Slave Device Testing](image)

Figure 28 shows the TWI Waveforms – Master device writing data to the microcontroller. The picture demonstrates that the microcontroller acknowledges that the data was successfully received on the ninth cycle.

![Figure 28: TWI Waveforms – Master Device Writing Data to the Microcontroller](image)
Figure 29 shows the TWI Waveforms – Master device reading data from the microcontroller. Per the I\(^2\)C protocol, the TWI master does not acknowledge after the last data byte is received.

![TWI Waveforms](image)

**Figure 29: TWI Waveforms – Master Device Reading Data from the Microcontroller**

Figure 30 demonstrates the final hardware, firmware, and software integration. Figure 31 shows a photograph of the senior project demonstration at the Cal Poly Senior Project Expo.

![Final Hardware, Firmware, and Software Integration](image)

**Figure 30: Final Hardware, Firmware, and Software Integration**

![Senior Project Expo](image)

**Figure 31: Senior Project Expo**
IX. Problems Encountered

1. TWI Master clock
   While implementing the master TWI protocol communication via the bit banging method, it was discovered that due to hardware limitations, the TWI CLOCK (SCL) of 400kHz could not be achieved. The TWI clock was decreased to 100 kHz.

2. Master TWI device
   Originally, the Aardvark device was selected as the master device to communicate with the ATMega164P microcontroller. However, the LabVIEW drivers could not be installed on a Dell laptop with Windows 7 64-bit operating system. After extensive literature search, it was discovered that the device manufacturer, Total Phase, only supports 32-bit operating systems. An alternative choice had to be made. The following options were available:
   - A. Use existing TWI code and operate the ATmega as a master and slave TWI device
   - B. Borrow a USB to I^2C/SPI device from the customer
   - C. Ask the customer to purchase a USB to I^2C/SPI device (spend approximately $423).

   The customer resolved the problem by purchasing a USB-8451 I^2C/SPI device from National Instruments.

3. Microcontroller External Interrupts
   When implementing the Pin Change Interrupts on Port B of the ATMega164P microcontroller, some unknown event continuously triggered the PCINT1 routine. Extensive debugging showed that Port Bit1 (PCINT9) continuously triggered the PCINT1 routine. It turns out that when the CKOUT fuse is programmed, PBB1’s alternate function is to output the system clock, regardless of PORTB settings. Somehow, the CKOUT fuse had been inadvertently programmed. The problem was fixed by disabling the CKOUT fuse.

4. TWI Bit Banging Source Code
   There bit banging application note contains errors and due to limited knowledge on C-programming, it took over 8 hours to realize that it had errors.

5. TWI Slave Source Code
   There was not much information available on the Two-Wire Interface source code. Many online application notes were pieced together to develop the Two-Wire Interface for this project.

6. JTAG ICE II Debugger
   When performing on-chip debugging for the first time, the JTAG ICE II debugger did not function correctly. The Atmel Studio development environment failed to recognize the debugger device ID. The help feature provided by Atmel provided little information in setting up the JTAG ICE II debugger. The AVR freaks website had many threads regarding the debugger but none of offered a solution to the problem.
7. **PC Board Construction**
The temperature reported by the microcontroller fluctuated from 25°C to 50°C. This was due to the missing filter capacitor (C10) on the output of the temperature sensor. Figure 32 shows the schematic and photograph of the missing capacitor.

8. **/SYSRESET Testing**
The /SYSRESET timing requirement did not work the first time. This was due to missing code to assert this signal. Code was added to assert the /SYSRESET signal when the +5V is within ± 1%. The initial test revealed that the /SYSRESET signal stayed logic high all the time. This was due to not initializing and resetting signal when required.

After updating the source code to initialize and reset the signal when required, the /SYSRESET signal was asserted 400mS after the trigger source instead of 200mS. This was due to the program’s slow sample rate of 350mS that monitor the +5V output voltage. To fix this problem, the Nyquist-Shannon sampling theorem was implemented and the sample rate was changed to 12.8mS. The delay between the +5V being within ± 1% was updated to 122mS and the /SYSRESET assertion time met the requirement. Figure 22, shows the /SYSRESET signal meeting the assertion time.

9. **/FAIL Testing**
The /FAIL status signal did not function during the initial testing. This was due to missing code to assert the signal. Code was added to assert the /FAIL signal when all of the power supply outputs are within ±10 of their rated output. Figure 26 shows the Control Electronics asserting the /FAIL signal.

10. **Turn-On Sequence**
The turn-On sequence from figure 1 cannot be verified at this level. The turn-on sequence will be verified at the system level, when the Control Electronics is embedded into Rantec’s power supply.
X. Conclusion

Though the project has not been embedded into Rantec’s 3U power supply, the Control Electronics met most of its standalone specifications and performance. All C-Code drivers have gone through extensive testing and proven to be reliable. There was one drawback with the Two-Wire Interface master bit-bang driver. This driver did not meet the Two-Wire clock speed requirement of 400 kHz. It was discovered that the hardware limits the clock speed to 100 kHz. For future projects, the clock speed can be increased by replacing the 16MHz crystal oscillator with a higher frequency oscillator.

The Two-Wire Interface slave driver clock speed is determined by the master device and based on the microcontroller’s datasheet; it’s capable of operating up to 400 kHz. This operating speed was not verified, since the master device (National Instruments NI-8451 USB to I2C adapter) has a maximum clock speed of 250 kHz; nevertheless, it’s possible for this diver to operate at 400 kHz.

The printed circuit board designed for this project had no electrical flaws or manufacturing defects. The only thing that could be improved is the silkscreen. For example, U$1 could be replaced with U1.

This project successfully integrated hardware and software for the Control Electronics design. The Two-Wire interface master and slave drivers are compatible with Philip’s I2C serial communication protocol, and the printed circuit board can be used for future projects.
XI. References


Appendix A. Senior Project Analysis

Project Title: Microcontroller Design: Control Electronics for Rantec’s 28VDC Input 3-U Power Supply

Student's Name: Leo Hernandez
Student's Signature: 
Advisor's Name: Dr. Braun
Advisor's Initials: 
Date: December 6, 2012

• Summary of Functional Requirements
The Control Electronics monitors the output voltages, input voltage, intermediate bus voltage, and the temperature of the 3U Power Supply. The control electronics reports this data via the I^2C Interface Bus to the outside world. The Control Electronics greatly reduces troubleshooting time. Refer to Table I for more information on the requirements specifications for the Control Electronics.

• Primary Constraints
  ➢ Limited time to build and test the project. The customer may need the Firmware by June of 2013 to meet their Qualification schedule.
  ➢ Limited knowledge of C-programming.
  ➢ Lack of resources to design the printed circuit board and limited knowledge of printed circuit board houses that can build a reliable prototype at a low cost.

• Economic
After the project ends, the design will be embedded into Rantec’s power supply for Design and Verification Testing. After the power supply meets the Design and Verification parameters, the power supply goes into a Qualification stage at the customers facility and then into full production.

If manufactured on a commercial basis
  ➢ The Non Re-occurring cost is $8,224.
  ➢ Approximately 50 units sold per year.
  ➢ $400 to build and test each device. A minimum of 20 pieces are required to cover the manufacturing equipment setup cost.
  ➢ $1000 per unit.
  ➢ The estimated profit is $25,000 per year.

• Environmental
The Control Electronics contains semiconductor devices. These semiconductor devices are manufactured on wafers containing natural elements such as silicon, germanium, gallium or arsenic. Chemicals involved in the manufacturing process of semiconductors, such as arsenic and cadmium are carcinogens, and pose a severe health risk if not handled properly. Great care should be taken so that these toxic components don't pollute our environment.

• Manufacturability
The Control Electronics design could be built on a single printed circuit board. Printed circuit boards provide noise immunity and greatly reduce the manufacturing cost when the design is produced in
mass quantities. Should the design go into full production, the product will be manufactured per IPC-JSTD-001 Soldering Standard.

- **Sustainability**
The printed circuit board for this project is highly maintainable and requires low maintenance. Components chosen for this project such as resistors, capacitors, and the microprocessor come in standard packages and therefore are easily replaceable. Dust can accumulate over electronics after a long period of time. The dust can be wiped off with a small brush using isopropyl alcohol. The printed circuit board is recyclable; after the project’s life cycle, the user should take printed circuit board to a recycling center where valuable items such as gold and copper elements can be re-used.

- **Ethical**
The Control Electronics contains semiconductor devices. These semiconductor devices are manufactured on wafers containing natural elements such as silicon, germanium, gallium or arsenic. These devices are manufactured in semiconductor fabrication plants located in countries where rules and regulations are not as strict as in the United States. The primary reason that semiconductor parts are built in third world countries is the extremely low labor cost.

- **Health and Safety**
This product operates at relatively low voltages (3.3-28 VDC), posing no major safety hazard to the user. The user however, shall be careful not to touch any electronic component while the electrical power is applied. Once fully assembled, the Control Electronics shall have a protective cover to reduce electrical hazards. The assemblers assembling the Control Electronics project shall avoid breathing the solder fumes. An air ionizer fan can be used blow the solder fumes away from the assembler.
• **Social and political**

The Control Electronics is integrated into Rantec’s 3U DC-DC converter. This project directly impacts Rantec Power Systems Inc. by expanding their custom DC-DC converter product line and could potentially attract new customers. This U.S. made product provides job opportunities to people living in the San Luis Obispo County.

• **Development**

Since this project integrated hardware, firmware and software, several skills were learned in this project. As an electrical engineering student, it was very challenging programming in C, specially, developing firmware in two different development environments, Atmel Studio and CodeVision AVR. Atmel studio is free software; however, the customer recommended using CodeVision AVR, a $220 C-compiler. CodeVision AVR is great C-compiler, however, it has a different user interface and it was not possible to simply copy the code from the Atmel Studio development environment. A requisite for the Control Electronics project was to develop a Two-Wire Interface master bit bang driver; this demanded over 30 hours of extensive reading of the I²C serial communication protocol. Another requisite required the development of the Two-Wire Interface slave driver; it took over 20 hours of research to develop this driver, since there is not much information available online. At the completion of this project, the skills learned include, microcontroller design, C-programming, on-board chip debugging, printed circuit board layout techniques, and in-depth knowledge of the I²C serial communication protocol.
Appendix B. Project Planning

The Gantt chart shows the original senior project schedule.

Figure 33: Original Gantt Chart for the Senior Project—Pane 1
<table>
<thead>
<tr>
<th>ID</th>
<th>Task Name</th>
<th>Duration</th>
<th>Start</th>
<th>Finish</th>
<th>Pred</th>
</tr>
</thead>
<tbody>
<tr>
<td>22</td>
<td>Submit Literature Search</td>
<td>0 days</td>
<td>Mon 2/4/13</td>
<td>Mon 2/4/13</td>
<td></td>
</tr>
<tr>
<td>23</td>
<td>Functional Diagram</td>
<td>20 days</td>
<td>Mon 1/21/13</td>
<td>Fri 1/25/13</td>
<td></td>
</tr>
<tr>
<td>24</td>
<td>Create Functional Diagram</td>
<td>0 days</td>
<td>Mon 1/21/13</td>
<td>Thu 1/31/13</td>
<td></td>
</tr>
<tr>
<td>25</td>
<td>Functional Diagram Review with Customer</td>
<td>0 days</td>
<td>Fri 2/1/13</td>
<td>Fri 2/1/13</td>
<td></td>
</tr>
<tr>
<td>26</td>
<td>Update Functional Diagram</td>
<td>0 days</td>
<td>Fri 2/5/13</td>
<td>Fri 2/15/13</td>
<td></td>
</tr>
<tr>
<td>27</td>
<td>Firmware Design/Development</td>
<td>69 days</td>
<td>Mon 3/18/13</td>
<td>Thu 6/20/13</td>
<td></td>
</tr>
<tr>
<td>28</td>
<td>TWI Master Code Software Design: First Iteration</td>
<td>3 days</td>
<td>Fri 4/12/13</td>
<td>Tue 4/16/13</td>
<td></td>
</tr>
<tr>
<td>29</td>
<td>Design TWI Master Flow Chart</td>
<td>1 day</td>
<td>Fri 4/12/13</td>
<td>Fri 4/12/13</td>
<td></td>
</tr>
<tr>
<td>30</td>
<td>Write TWI Master C-code (Bit Banging Method)</td>
<td>1 day</td>
<td>Mon 4/15/13</td>
<td>Mon 4/15/13</td>
<td></td>
</tr>
<tr>
<td>31</td>
<td>Test TWI Master C-code</td>
<td>1 day</td>
<td>Tue 4/16/13</td>
<td>Tue 4/16/13</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td>TWI Master Code Software Design: Second Iteration</td>
<td>3 days</td>
<td>Fri 4/19/13</td>
<td>Tue 4/23/13</td>
<td></td>
</tr>
<tr>
<td>33</td>
<td>Update TWI Master C-code (Bit Banging Method)</td>
<td>2 days</td>
<td>Fri 4/19/13</td>
<td>Mon 4/22/13</td>
<td></td>
</tr>
<tr>
<td>34</td>
<td>Test TWI Master C-code</td>
<td>1 day</td>
<td>Tue 4/25/13</td>
<td>Tue 4/25/13</td>
<td></td>
</tr>
<tr>
<td>35</td>
<td>TWI Master Code Software Design: Third Iteration</td>
<td>17 days</td>
<td>Wed 4/24/13</td>
<td>Thu 5/16/13</td>
<td></td>
</tr>
<tr>
<td>36</td>
<td>Update TWI Master C-code (Bit Banging Method)</td>
<td>0 days</td>
<td>Wed 4/24/13</td>
<td>Thu 4/25/13</td>
<td></td>
</tr>
<tr>
<td>37</td>
<td>Review Software</td>
<td>1 day</td>
<td>Tue 5/14/13</td>
<td>Tue 5/14/13</td>
<td></td>
</tr>
<tr>
<td>38</td>
<td>Update / Test TWI Master C-code</td>
<td>2 days</td>
<td>Wed 5/15/13</td>
<td>Thu 5/16/13</td>
<td></td>
</tr>
<tr>
<td>39</td>
<td>TWI Slave Code Software Design: First Iteration</td>
<td>5 days</td>
<td>Mon 5/20/13</td>
<td>Fri 5/24/13</td>
<td></td>
</tr>
<tr>
<td>40</td>
<td>Design TWI Flow Chart</td>
<td>1 day</td>
<td>Mon 5/20/13</td>
<td>Mon 5/20/13</td>
<td></td>
</tr>
<tr>
<td>41</td>
<td>Write TWI slave C-code</td>
<td>3 days</td>
<td>Tue 5/21/13</td>
<td>Tue 5/25/13</td>
<td></td>
</tr>
<tr>
<td>42</td>
<td>Test TWI Master code</td>
<td>1 day</td>
<td>Fri 5/24/13</td>
<td>Fri 5/24/13</td>
<td></td>
</tr>
<tr>
<td>43</td>
<td>TWI Slave Code Software Design: Second Iteration</td>
<td>4 days</td>
<td>Mon 5/27/13</td>
<td>Thu 5/30/13</td>
<td></td>
</tr>
<tr>
<td>44</td>
<td>Update TWI slave C-code</td>
<td>2 days</td>
<td>Mon 5/27/13</td>
<td>Tue 5/29/13</td>
<td></td>
</tr>
</tbody>
</table>

**Figure 34: Gantt Chart for the Senior Project Pane—2**
Figure 35: Gantt Chart for the Senior Project—Pane 3
Figure 36: Gantt Chart for the Senior Project—Pane 4
The Gantt chart shows the actual senior project schedule.

![Gantt Chart Image]

**Figure 37: Actual Gantt Chart for the Senior Project—Pane 1**
Figure 38: Actual Gantt Chart for the Senior Project—Pane 2
<table>
<thead>
<tr>
<th>ID</th>
<th>Task Name</th>
<th>Duration</th>
<th>Start</th>
<th>Finish</th>
<th>Pred</th>
</tr>
</thead>
<tbody>
<tr>
<td>54</td>
<td>Electrical Design</td>
<td>4.8 days</td>
<td>Fri 4/12/13</td>
<td>Mon 6/24/13</td>
<td></td>
</tr>
<tr>
<td>55</td>
<td>Electrical Design Review</td>
<td>1 day</td>
<td>Tue 6/25/13</td>
<td>Tue 8/25/13</td>
<td>54</td>
</tr>
<tr>
<td>56</td>
<td>Update Electrical Design</td>
<td>2 days</td>
<td>Tue 6/25/13</td>
<td>Wed 6/26/13</td>
<td></td>
</tr>
<tr>
<td>57</td>
<td><strong>Build Prototype Circuit: First Iteration</strong></td>
<td>20 days</td>
<td>Fri 4/5/13</td>
<td>Thu 5/2/13</td>
<td></td>
</tr>
<tr>
<td>58</td>
<td>Order Parts</td>
<td>6 days</td>
<td>Fri 4/5/13</td>
<td>Fri 4/12/13</td>
<td></td>
</tr>
<tr>
<td>59</td>
<td>Build Prototype Circuit</td>
<td>10 days</td>
<td>Thu 4/18/13</td>
<td>Wed 5/1/13</td>
<td>58</td>
</tr>
<tr>
<td>60</td>
<td>Inspect Prototype Circuit</td>
<td>1 day</td>
<td>Thu 5/2/13</td>
<td>Thu 5/2/13</td>
<td></td>
</tr>
<tr>
<td>61</td>
<td><strong>Integration: First Iteration</strong></td>
<td>12 days</td>
<td>Thu 5/2/13</td>
<td>Fri 5/3/13</td>
<td>59</td>
</tr>
<tr>
<td>62</td>
<td>Test/Integrate Hardware &amp; software</td>
<td>6 days</td>
<td>Thu 8/29/13</td>
<td>Thu 9/5/13</td>
<td>62</td>
</tr>
<tr>
<td>63</td>
<td>Update Software</td>
<td>6 days</td>
<td>Fri 9/6/13</td>
<td>Fri 9/13/13</td>
<td></td>
</tr>
<tr>
<td>64</td>
<td><strong>Build Prototype Circuit: Second Iteration</strong></td>
<td>22 days</td>
<td>Fri 10/4/13</td>
<td>Mon 11/4/13</td>
<td></td>
</tr>
<tr>
<td>65</td>
<td>Order Parts</td>
<td>20 days</td>
<td>Fri 10/4/13</td>
<td>Thu 10/31/13</td>
<td></td>
</tr>
<tr>
<td>66</td>
<td>Build Prototype Circuit</td>
<td>3 days</td>
<td>Thu 10/31/13</td>
<td>Mon 11/4/13</td>
<td></td>
</tr>
<tr>
<td>67</td>
<td><strong>Integration: Third Iteration</strong></td>
<td>10 days</td>
<td>Mon 11/4/13</td>
<td>Fri 11/15/13</td>
<td></td>
</tr>
<tr>
<td>68</td>
<td>Test/Integrate Hardware &amp; software</td>
<td>9 days</td>
<td>Mon 11/4/13</td>
<td>Thu 11/14/13</td>
<td></td>
</tr>
<tr>
<td>69</td>
<td>Update Software</td>
<td>1 day</td>
<td>Fri 11/15/13</td>
<td>Fri 11/15/13</td>
<td>68</td>
</tr>
<tr>
<td>70</td>
<td><strong>Integration: Third Iteration</strong></td>
<td>15 days</td>
<td>Sun 11/17/13</td>
<td>Fri 12/6/13</td>
<td></td>
</tr>
<tr>
<td>71</td>
<td>Test/Integrate Hardware &amp; software</td>
<td>7 days</td>
<td>Sun 11/17/13</td>
<td>Tue 12/20/13</td>
<td>69</td>
</tr>
<tr>
<td>72</td>
<td>Final Demonstration</td>
<td>8 days</td>
<td>Wed 11/27/13</td>
<td>Fri 12/13/13</td>
<td></td>
</tr>
<tr>
<td>73</td>
<td><strong>Project Report</strong></td>
<td>226 days</td>
<td>Thu 1/31/13</td>
<td>Thu 12/12/13</td>
<td></td>
</tr>
<tr>
<td>74</td>
<td>Write Project Report</td>
<td>1 day</td>
<td>Sun 12/15/13</td>
<td>Sun 12/15/13</td>
<td>76</td>
</tr>
<tr>
<td>75</td>
<td>Review Project Report</td>
<td>1 day</td>
<td>Sat 12/14/13</td>
<td>Sat 12/14/13</td>
<td>74</td>
</tr>
</tbody>
</table>

**Figure 39: Actual Gantt Chart for the Senior Project—Pane 3**
## Parts List and Cost Estimate

Table XII shows the Control Electronics Parts List.

<table>
<thead>
<tr>
<th>Description</th>
<th>QTY</th>
<th>Reference Designator</th>
<th>Value</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitor</td>
<td>1</td>
<td>C12</td>
<td>.01uF</td>
<td>0603</td>
</tr>
<tr>
<td>Capacitor</td>
<td>2</td>
<td>C1, C2</td>
<td>20pF</td>
<td>0603</td>
</tr>
<tr>
<td>Capacitor</td>
<td>22</td>
<td>C3, C4, C5, C6, C7, C8, C9, C10, C11, C13, C14, C15, C17, C19, C21, C23, C25, C27, C29, C30, C32, C33, C34</td>
<td>.1uF</td>
<td>0603</td>
</tr>
<tr>
<td>Capacitor</td>
<td>8</td>
<td>C16, C18, C20, C22, C24, C26, C28, C31</td>
<td>4.7uF</td>
<td>0603</td>
</tr>
<tr>
<td>Resistor</td>
<td>9</td>
<td>R4, R13, R14, R15, R16, R17, R18, R17</td>
<td>40.2</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>1</td>
<td>R20</td>
<td>200</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>1</td>
<td>R40</td>
<td>95.3</td>
<td>0603</td>
</tr>
<tr>
<td>Resistor</td>
<td>1</td>
<td>R44</td>
<td>1.87k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>1</td>
<td>R34</td>
<td>13k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>1</td>
<td>R41</td>
<td>9.76k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>1</td>
<td>R45</td>
<td>5.23k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>4</td>
<td>R1, R8, R9, R10, R11</td>
<td>10k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>2</td>
<td>R2, R3</td>
<td>2k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td>18</td>
<td>R6, R7, R21, R23, R24, R29, R31, R32, R33, R35, R40, R41, R42, R43, R44, R45, R46</td>
<td>1K</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td></td>
<td>R22</td>
<td>28k</td>
<td>1206</td>
</tr>
<tr>
<td>Resistor</td>
<td></td>
<td>R5</td>
<td>10</td>
<td>1206</td>
</tr>
<tr>
<td>AVR Microcontroller</td>
<td>1</td>
<td>U1</td>
<td>ATMega324P</td>
<td>TQPF</td>
</tr>
<tr>
<td>Temp Sensor</td>
<td>2</td>
<td>U$1, U$2</td>
<td>LM94021</td>
<td>SOT23</td>
</tr>
<tr>
<td>1-channel ADC</td>
<td>7</td>
<td>IC2, IC4, IC6, IC8, IC10, IC12, IC14</td>
<td>ADC121C021CIMM</td>
<td>8VSSOP</td>
</tr>
<tr>
<td>4.096V Ref Zener</td>
<td>8</td>
<td>IC3, IC5, IC7, IC9, IC11, IC13, IC15, IC17</td>
<td>LM4050</td>
<td>SOT23</td>
</tr>
<tr>
<td>10P Double Row Header</td>
<td>11</td>
<td>SV1-SV11</td>
<td>10 Pins</td>
<td>Thru hole</td>
</tr>
<tr>
<td>16MHz Crystal Oscillator</td>
<td>1</td>
<td>Xtal</td>
<td>ECS-160-20-30B-DU</td>
<td>SMT</td>
</tr>
<tr>
<td>Resistor</td>
<td>9</td>
<td>R25, R26, R27, R28, R36, R37</td>
<td>3.74k</td>
<td>0805</td>
</tr>
<tr>
<td>Diode</td>
<td>1</td>
<td>D2</td>
<td>1N4148</td>
<td>Thru hole</td>
</tr>
<tr>
<td>LED</td>
<td>1</td>
<td>LED2</td>
<td>Green LED</td>
<td>Thru hole</td>
</tr>
</tbody>
</table>
Tables XIII through XV describe the material and labor cost for this project.

<table>
<thead>
<tr>
<th></th>
<th>Estimated</th>
<th>Actual</th>
</tr>
</thead>
<tbody>
<tr>
<td>Proposal</td>
<td>2</td>
<td>4</td>
</tr>
<tr>
<td>Development Plan</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>Requirements</td>
<td>24</td>
<td>24</td>
</tr>
<tr>
<td>Presentation</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>Specifications</td>
<td>18</td>
<td>18</td>
</tr>
<tr>
<td>PDR</td>
<td>5</td>
<td>5</td>
</tr>
<tr>
<td>Flow Charts</td>
<td>32</td>
<td>40</td>
</tr>
<tr>
<td>Block Diagram</td>
<td>20</td>
<td>25</td>
</tr>
<tr>
<td>CDR</td>
<td>10</td>
<td>10</td>
</tr>
<tr>
<td>Firmware Development</td>
<td>80</td>
<td>123</td>
</tr>
<tr>
<td>Build Prototype circuit</td>
<td>24</td>
<td>24</td>
</tr>
<tr>
<td>Test Prototypes</td>
<td>40</td>
<td>40</td>
</tr>
<tr>
<td>Hardware &amp; Software integration</td>
<td>40</td>
<td>60</td>
</tr>
<tr>
<td>Maintain</td>
<td>10</td>
<td>2</td>
</tr>
</tbody>
</table>

**Total Labor Hours**

<table>
<thead>
<tr>
<th></th>
<th>Estimated</th>
<th>Actual</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>308</td>
<td>378</td>
</tr>
</tbody>
</table>
### TABLE XIII: MATERIAL COST

<table>
<thead>
<tr>
<th>Material/Software</th>
<th>Estimated Cost</th>
<th>Actual Cost</th>
</tr>
</thead>
<tbody>
<tr>
<td>Development Kit</td>
<td>$300.00</td>
<td>$300.00</td>
</tr>
<tr>
<td>Software Development Environment</td>
<td>$300.00</td>
<td>$300.00</td>
</tr>
<tr>
<td>Aardvark I2C/SPI Host Adapter</td>
<td>$275.00</td>
<td>$0.00</td>
</tr>
<tr>
<td>NI-8451 I^2C/SPI device</td>
<td>$0.00</td>
<td>$473.00</td>
</tr>
<tr>
<td>Microprocessor</td>
<td>$2.00</td>
<td>$2.00</td>
</tr>
<tr>
<td>ADCs</td>
<td>$5.00</td>
<td>$6.00</td>
</tr>
<tr>
<td>Crystal</td>
<td>$2.00</td>
<td>$0.00</td>
</tr>
<tr>
<td>SM Resistors</td>
<td>$10.00</td>
<td>$5.00</td>
</tr>
<tr>
<td>SM Capacitors</td>
<td>$5.00</td>
<td>$5.00</td>
</tr>
<tr>
<td>Prototype Board</td>
<td>$100.00</td>
<td>$516.00</td>
</tr>
<tr>
<td><strong>Total Material Cost</strong></td>
<td><strong>$1000.00</strong></td>
<td><strong>$1559.00</strong></td>
</tr>
</tbody>
</table>

### TABLE XIV: LABOR & MATERIAL COST

<table>
<thead>
<tr>
<th>Component</th>
<th>Estimated</th>
<th>Actual</th>
</tr>
</thead>
<tbody>
<tr>
<td>Labor Cost</td>
<td>Estimated: 308 hours @ $25/hour</td>
<td>$7,700.00</td>
</tr>
<tr>
<td></td>
<td>Actual: 378 hours @ $25/hour</td>
<td></td>
</tr>
<tr>
<td>Material Cost</td>
<td>Material Cost</td>
<td>$992.00</td>
</tr>
<tr>
<td><strong>Total Cost</strong></td>
<td><strong>$8,224.00</strong></td>
<td><strong>$11,009.00</strong></td>
</tr>
</tbody>
</table>
Appendix E. Source code

Header File

/***************************************************************************/
VME.h
Two Wire Interface Header
Created by Leo H. for Rantec's 3U power supply
4/19/2012
/***************************************************************************/

/*****************************************************************************/
Global Variables
****************************************************************************/

// Global timers
unsigned int T0_Outp_Enable; // LED timer
unsigned int ADC_Timer2; // ADC Timer
unsigned int Temp_Timer1; // Temp Timer
unsigned int Timer2; // ADC Timer

// Global TWI variables
unsigned int TWI_Status; // Reset TWI hardware
unsigned int TWI_uC_Address;

//unsigned int Reg_Data;
char Reg_Data[22];
char TX_Reg_Data[22];
char PortA_Status;
char OVP_Status; // Port B
char SysReset;
char FAIL;

unsigned int OVP_Limit[7];
unsigned int OVP_Hysteresis[7];
char Volt_ADC_Addr[7];
float Volt_Scl_Fact[7];

/*****************************************************************************/
Measurement Data Variables
****************************************************************************/

int Temp_Data; // Temperature measurement data
/**********Temperature Sensor ADC Lookup Table**********/
// Vref = 4.096V
unsigned int TEMP_TABLE[244] = {
    832, 829, 827, 824, 822, 819, 817, 814, 811, 808, 805, 803, 800, 797, 793, 790, 787, 784, 780, 777, 774, 771, 761, 764, 761, 758, 754, 751, 748, 745, 741, 738, 
    628, 625, 622, 618, 615, 612, 608, 605, 602, 598, 595, 591, 588, 585, 581, 578, 575, 571, 568, 565, 561, 558, 554, 551, 548, 544, 541, 537, 534, 531, 527, 524, 
    297, 293, 290, 286, 283, 279, 275, 272, 268, 265, 261, 258, 254, 250, 247, 243, 240, 236, 232, 229, 225, 222, 218, 215, 211, 207, 204, 200, 197, 193, 189, 186, 
    182, 178, 175, 171, 168, 164, 160, 157, 153, 149, 146, 142, 138, 135, 132, 128, 124, 121, 117, 114, 110, 107, 103, 99, 96, 92, 89, 85, 81, 78, 74, 71, 
    67, 63, 60, 56, 53, 49, 45, 42, 38, 35, 31, 28, 24, 20, 17, 13, 10, 6, 2, 0};

/****************************Function Prototypes**********/
/
Retrieve Temperature Data
*/
void Retrieve_Temp_Data(void);
/
Sequence enable outputs
*/
void Seq_Enable(void);
/
Enable outputs
*/
void Output_Enable (char outno, char cmd);

/****************************TWI Master Function Prototypes***/
/
Retrieve ADC Voltage Data
*/
char Retrieve_ADC_Volt_Data(char num);
/
Measure ADC voltage data
*/
char Meas_Volt(char mindex, char *p_volt);
/
Measure unit temperature
*/
int Meas_Temperature(void);
/*
Send TWI START, address, & read/write

char Send_TWI_Start_Addr_RW(char slv_addr, char rw);

/*
**** Send TWI Master Write *****
*/
char Write_TWI_Master(char slv_addr, char *p_data, char dlength);

/*
**** Send TWI Master Read *****
*/
char Read_TWI_Master( char slv_addr, char *p_data, char dlength);

/*
**** Initialize TWI ADcs *****
*/
void Initialize_TWI_ADCs(void);

/************TWI Bit Banging Function Prototypes********/

/*
**** Send i2c byte *****
*/
int SendByte(char byte);

/*
**** ReceiveI2C byte *****
*/
char ReceiveByte(char lastbyte);

/*
**** Send I2C start signal *****
*/
void Send_Start_bit(void);

/*
**** Write I2C SCL bit *****
*/
void Write_SCL(unsigned char state);

/*
**** Write I2C SDA bit *****
*/
void Write_SDA(unsigned char state);

/*
**** Send I2C stop signal *****
*/
void Send_Stop_bit(void);
Main Program

/*******************************************************
Project: Control Electronics
Version: 1.0
Date: 4/19/2013
Author: Leo Hernandez
Company: Rantec Power Systems Inc. & Cal Poly State University, San Luis Obispo
Comments: This is a microcontroller designs project for Rantec Power Systems Inc.

Chip type: ATmega324P
Program type: Application
AVR Core Clock frequency: 16.000000 MHz
Memory model: Small
External RAM size: 0
Data Stack size: 512
/***************************************************************************/
#include <mega324.h>
#include <delay.h>
#include <stdlib.h>
#include <math.h>
#include <iobits.h>
#include "VME.h"
#define TW_SR_SLA_ACK 0x60 // SLA+W has been received; ACK has been returned
#define TW_SR_DATA_ACK 0x80 // Previously addressed with own SLA+W; data has been received; ACK has been returned
#define TW_SR_ARB_LOST_SLA_ACK 0x68 // Arbitration lost in SLA+R/W as Master; own SLA+W has been received; ACK has been returned
#define TW_SR_STOP 0xA0 // STOP condition or repeated START condition has been received while still addressed as Slave
#define TW_SR_DATA_NACK 0x88 // Previously addressed with own SLA+W; data has been received; NOT ACK has been returned
#define TW_ST_SLA_ACK 0xA8 // Own SLA+R has been received; ACK has been returned
#define TW_ST_DATA_ACK 0xB8 // Data byte in TWDR has been transmitted; ACK has been received
#define TW_ST_LAST_DATA 0xC8 // Last data byte in TWDR has been transmitted (TWEA = “0”); ACK has been received
#define TW_ST_DATA_NACK 0xC0 // Data byte in TWDR has been transmitted; NOT ACK has been received
#define TW_BUS_ERROR 0x00 // Bus error due to an illegal START or STOP condition

ISR TIMER0_OVF_vect
ISR TIMER2_OVF_vect
/*************************************************************************************
ISR TIMER0_OVF_vect
/*******************************************************
interrupt [TIM0_OVF] void timer0_ovf_isr(void)
{
 T0_Outp_Enable++;
 Temp_Timer1++;
} //

/****************************
ISR TIMER2_OVF_vect
// Overflow every 15.872uS
interrupt [TIM2_OVF] void timer2_ovf_isr(void)
{
interrupt [TIM2_OVF] void timer2_ovf_isr(void)
{
    ADC_Timer2++;
    Timer2++;
}

ISR TWI_vect
interrupt [TWI] void i2c_slave_isr(void)
{
    static unsigned char t_index = 0;

    TWI_Status = (TWSR & 0xF8); // Mask TWPS1, & TWPS0

    switch(TWI_Status)
    {
        case TW_SR_SLA_ACK: // 0x60: SLA+W has been received; ACK has been returned
            t_index = 0;
            break;

        case TW_SR_DATA_ACK: // 0x80: Previously addressed with own SLA+W; data has been
            Reg_Data[t_index] = TWDR;
            t_index++;
            break;

        case TW_SR_ARB_LOST_SLA_ACK: // 0x68: Arbitration lost in SLA+R/W as Master; own
            SLA+W has been received; ACK has been returned
            break;

        case TW_SR_STOP: // 0xA0: A STOP condition or repeated START condition has been
            received while still addressed as Slave
            break;

        case TW_SR_DATA_NACK: // 0x88: Previously addressed with own SLA+W; data has been
            received; NOT ACK has been returned
            break;

        /**************************Error Recovery**************************/
        case TW_SR_DATA_NACK: // 0x88: Previously addressed with own SLA+W; data has been
            received; NOT ACK has been returned
            break;

        /**************************SlaveTransmitter**************************/
        case TW_ST_SLA_ACK: // 0xA8: Own SLA+R has been received; ACK has been returned
            t_index = 0;
            TWDR = TX_Reg_Data[t_index]; // Load data in the TWI register
            t_index++;
            break;

        case TW_ST_DATA_ACK: // 0xB8: Data byte in TWDR has been transmitted; ACK has
            been received
TWDR = TX_Reg_Data[t_index]; // Load data in the TWI register
t_index++;
break;

case TW_ST_LAST_DATA: // 0xC8: Last data byte in TWDR has been transmitted (TWEA = “0”); ACK has been received
break;

case TW_ST_DATA_NACK: // 0xC0: Data byte in TWDR has been transmitted; NOT ACK has been received
break;

/******************************TWI Slave Bus Error***********************************/
case TW_BUS_ERROR: // 0x00: Bus error due to an illegal START or STOP condition
break;

default:
TWCR |= (1<<TWINT); // Clear TWI interrupt flag

{ //Nbytes = bytes;
TWCR |= (1<<TWINT); // Clear TWI interrupt flag
}

ISR(PCINT1_vect)
************************************************************************************
乒
PortB B4 B3 B2 B1 B0
outno 4 3 2 1 0
-12V_AUX +3.3V_AUX +12V +5V +3.3V

 adhesive

Pin change 8-15 interrupt service routine
interrupt [PC_INT1] void pin_change_isr1(void)
{
// PORTA |= (1<<PORTA6) // /FAIL indicates an output voltage is outside the +-10%
tolerance
char i, portb_data;
char ovdata = 0x00;

portb_data = PINB; // Get port data
ovdata = ~portb_data & 0x1F; // 1 = OVP condition

if (ovdata)
{
OVP_Status = ovdata;

for (i = 0; i < 5; i++)
{
if ( ovdata & (1<<i) )
{
Output_Enable(i,2); // overvoltage condition occurred - shutdown output
}
if ( i == 1 )
{
SysReset = 0x00; // Reset SYsreset Signal
TX_Reg_Data[20] = SysReset; // Reset /Sysreset

FAIL = 0x00;
TX_Reg_Data[21] = FAIL;

PCIFR = 0x02; // Clear interrupt flag
//PCIFR = (1<<PCIF1); // Clear interrupt flag

void main(void)
{
    int i;
    char portd_data;
    char vdc_enable = 0x03; // initialize /ENABLE & /INHIBIT to logic high
    char outno;

    T0_Outp_Enable = 0x00;
    ADC_Timer2 = 0x00;
    Temp_Timer1 = 0x00;
    Timer2 = 0x00;
    PortA_Status = 0x00;
    OVP_Status = 0x00;
    SysReset = 0x00;
    FAIL = 0x00;

    // Crystal Oscillator division factor: 1
    #pragma optsize-
    CLKPR=(1<<CLKPCE);
    CLKPR=(0<<CLKPCE) | (0<<CLKPS3) | (0<<CLKPS2) | (0<<CLKPS1) | (0<<CLKPS0);
    #ifdef _OPTIMIZE_SIZE_
    #pragma optsize+
    #endif

    Port A Initialization
    /***************************************************************************/
    /*
    PA4= /-12V_AUX_ENABLE, PA3= /+3.3V_AUX_ENABLE, PA2= /+12V_ENABLE, PA1= /+5V_ENABLE, PA0=
    /+3.3V_ENABLE,
    PA5 = /SYSRESET
    PA6 = /FAIL
    PA7 ADC7 input
    */
    DDRA = 0x7F; // PA0-PA7 set as output pins
PORTA = 0x7F; // All outputs disabled

Port B Initialization
*******************************************************************************
PB4 = /-12V_AUX_OVP, PB3 = /+3.3V_AUX_OVP, PB2 = /+12V_OVP, PB1 = /+5V_OVP, PB0 = /+3.3V_OVP,
PB5 = /ENABLE
PB6 = /INHIBIT
PB7 Not used (configured as input)
*/

DDRB = 0x00; // Configure as input
PORTB = 0xFF; // Enable pull-up resistors

Port C Initialization
*******************************************************************************
JATAG port
*/

DDRC = 0xC0;
PORTC = 0xC0; // (JTAG port) Configured as input

Port D Initialization
*******************************************************************************
DDR D = 0x03; // PD0 & PD1 set as output pins (SCL & SDA)
PORTD = 0x00; // All outputs disabled
PORTD |= (1<<PORTD3) | (1<<PORTD2); // Enable pull-up resistors

Timer/Counter 0 initialization
*******************************************************************************
TCCR0A = 0x00;
TCNT0 = 0; // Initialize counter
OCR0A = 0x00;
OCR0B = 0x00;
TIFR0 |= (1<<TOV0); // clear overflow flag

/* For 16MHz clock:
B2-0 settings are:
Setting Frequency Period Timeout (256 count)
0 = no clock
1 = ClkI0 16MHz 62nS 15.872uS
2 = ClkI0/8 2MHz 500nS 128uS
3 = ClkI0/32 500kHz 2.00uS 512uS
4 = ClkI0/64 250kHz 4.00uS 1.024mS
5 = ClkI0/128 125kHz 8.00uS 2.048mS
6 = ClkI0/256 62.5kHz 16.00uS 4.096mS
7 = ClkI0/1024 15.625kHz 64.00uS 16.384mS*/
TCCR0B = 0x01; // set CLK/1 = 62ns
/***************************************************************************************************************************************************/
/*
Timer/Counter 2 initialization
**************************************************************************************************************************************************/

Clock source: System Clock
Mode: Normal top=FFh
OC2A output: Disconnected
OC2B output: Disconnected
*/
ASSR = 0x00; // Set asynchronous settings to 0.
TCCR2A = 0x00;
TCNT2 = 0x00;
OCR2A = 0x00;
OCR2B = 0x00;
/*
TCCR2B B2-0 (CS02-CS00) select the timer clock
For 16MHz clock:
B2-0 settings are:

<table>
<thead>
<tr>
<th>Setting</th>
<th>Frequency</th>
<th>Period</th>
<th>Timeout (256 count)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>no clock</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>ClkIO</td>
<td>16MHz</td>
<td>0.062uS</td>
</tr>
<tr>
<td>2</td>
<td>ClkIO/8</td>
<td>2MHz</td>
<td>.500uS</td>
</tr>
<tr>
<td>3</td>
<td>ClkIO/32</td>
<td>500kHz</td>
<td>2.00uS</td>
</tr>
<tr>
<td>4</td>
<td>ClkIO/64</td>
<td>250kHz</td>
<td>4.00uS</td>
</tr>
<tr>
<td>5</td>
<td>ClkIO/128</td>
<td>125kHz</td>
<td>8.00uS</td>
</tr>
<tr>
<td>6</td>
<td>ClkIO/256</td>
<td>62.5kHz</td>
<td>16.00uS</td>
</tr>
<tr>
<td>7</td>
<td>ClkIO/1024</td>
<td>15.625kHz</td>
<td>64.00uS</td>
</tr>
</tbody>
</table>
*/
TCCR2B = 0x02; //set clock/8 128uS timeout

/*****************************************************************************
ADC Initialization
*****************************************************************************

DIDR0 = 0x7F;
ADMUX = 0x07;
    //ADMUX = 0x47; // To debug
ADCSRA = 0x87; // ADC enabled, frequency = 125kH
/*******************USART0 Initialization*********************************************/
UCSR0B = 0x00; // USART0 disabled

/********************USART1 Initialization*********************************************/
UCSR1B = 0x00; // USART1 disabled

/********************SPI Bus Initialization*********************************************/
SPCR = 0x00; // SPI disabled

/********************Analog Comparator Initialization***********************************/
ACSR = 0x80;
ADCSRB = 0x00;
DIDR1 = 0x00;

/********************Two Wire Interface Bus Slave Initialization*************************/
portd_data = (PIND & 0x0C); // Monitor PD3 & PD2 to determine TWI slave address
//i2c_add = ~portd_data & 0xC0;
switch(portd_data)
{
    case 0x00:
        TWI_uC_Address = 0x30;
        break;
    case 0x04:
        TWI_uC_Address = 0x29;
        break;
    case 0x08:
        TWI_uC_Address = 0x28;
        break;
    case 0x0C:
        TWI_uC_Address = 0x27;
        break;
    default:
        TWI_uC_Address = 0x02;

}

TWAR = (TWI_uC_Address << 1 | 0); // Shift microcontroller address to the left,
ignore general call address
TWDR = 0xFF; // Release SDA pin
TWCR = (1<<TWINT) | (1<<TWEN) | (1<<TWEA) | (1<<TWIE); // Clear TWINT, enable TWI hardware, enable ACK own address, enable interrupt

/*******************
OVP_Limit, OVP_Hysteresis, & voltage scaling factor
***************************************************/

OVP_Limit[0] = 0xE7B; // OVP = 3.707V (+3.3V)
OVP_Limit[1] = 0xCC1; // +OVP = 6.25V (+5V)
OVP_Limit[2] = 0xE7B; // OVP = 3.707V (+12V)
OVP_Limit[3] = 0xE7B; // OVP = 3.707V (+3.3V_AUX)
OVP_Limit[4] = 0xE7B; // OVP = -3.707V (-12V_AUX)
OVP_Limit[5] = 0xF45; // OVP = 3.707 (DC_Bus)
OVP_Limit[6] = 0xF45; // OVP = 3.707V (V28Vin)

OVP_Hysteresis[0] = 0x38D; // +3.3V
OVP_Hysteresis[1] = 0x28D; // +5V
OVP_Hysteresis[2] = 0x289; // +12V
OVP_Hysteresis[3] = 0x38D; // +3.3V_AUX
OVP_Hysteresis[4] = 0x289; // -12V_AUX
OVP_Hysteresis[5] = 0x38D; // (DC_Bus)
OVP_Hysteresis[6] = 0x28D; // (V28Vin)

Volt_ADC_Addr[0] = 0x50; // +3.3V
Volt_ADC_Addr[1] = 0x51; // +5V
Volt_ADC_Addr[2] = 0x52; // +12V
Volt_ADC_Addr[3] = 0x54; // +3.3V_AUX
Volt_ADC_Addr[4] = 0x55; // -12V_AUX
Volt_ADC_Addr[5] = 0x56; // (DC_Bus)
Volt_ADC_Addr[6] = 0x58; // (V28Vin)

Volt_Scl_Fact[0] = 0.10; // +3.3V
Volt_Scl_Fact[1] = 0.1913846; // +5V
Volt_Scl_Fact[2] = 0.10; // +12V
Volt_Scl_Fact[3] = 0.10; //+3.3V_AUX
Volt_Scl_Fact[4] = 0.10; // -12V_AUX
Volt_Scl_Fact[5] = 0.064; // (DC_Bus)
Volt_Scl_Fact[6] = 1.1134861; // (V28Vin)

// Global enable interrupts
#asm("sei")

/**********************************
Initializing Program for the user
***********************************/

Initialize_TWI_ADCs();

for ( i = 0; i < 27; i++ )
{
    PORTC ^= (1<<PORTC7);
    delay_ms(PORTC);
}

/**************************
Control Inputs
***************************/

vdc_enable = (PINB & 0x60); // Monitor PB5 & PB6
if (vdc_enable == 0x00)
{
    Output_Enable(3,1); // Enable 3.3V_AUX
    OVP_Status = 0x00;
    for (i = 0; i < 3; i++)
    {
        Output_Enable(i,2); // Disable outputs: +3.3VDC, +5VDC, +12VDC
    }
    Output_Enable(4,2); // Disable -12V_AUX
}
else if(vdc_enable == 0x40) // Enable all outputs
{
    Seq_Enable();
    OVP_Status = 0x00;
}
else
{
    for (i = 0; i < 5; i++)
    {
        Output_Enable(i,2); // Disable all outputs
    }
}

/***********************************************************************************/
/*                                Main Loop                                       */
/***************************************************************
***********/
while (1)
{
    TX_Reg_Data[17] = PortA_Status; // Output Enable status
    delay_us(1); // This delay is used to debug only one IF Statement inside the
    while loop
    TX_Reg_Data[18] = OVP_Status; // Output Enable status
    delay_us(1); // This delay is used to debug only one IF Statement inside the
    while loop
    if ( Timer2 >= 1953 ) // Get ADC data every 250mS // WAS 1953
    {
        PORTC ^= (1<<PORTC7);
        Timer2 = 0x00;
    }
}

/***********************************************************************************/
/* Get ADC voltage data **************************************************************/

if ( ADC_Timer2 >= 100 ) // Get ADC data every 250mS
{
    outno = 0x00;
    for (i = 0; i < 7; i++)
    {

Retrieve_ADC_Volt_Data(outno); // Get voltage data from TWI device, returns true if data is valid
outno++;
}

ADC_Timer2 = 0; // reset Start_Timer variable
} // End TWI monitor

/**************************** Turn On/OFF outputs *******************************************/

if (Reg_Data[3] == 0x0A)
{
    OVP_Status = 0x00; // Clear OVP condition
    Seq_Enable(); // Enable Outputs
    delay_ms(250); // To debug
    Reg_Data[3] = 0x00; // Update register data to detect OV condition
}

if(Reg_Data[3] == 0x0C)
{
    for (i = 0; i < 5; i++)
    {
        OVP_Status = 0x00; // Clear OVP condition
        Output_Enable(i, 2); // Disable outputs
        delay_ms(200); // To debug
    }
    Reg_Data[3] = 0x00; // Update to detect other conditions
}

/**************************** Get temperature data *************************************/

if (Temp_Timer1 > 20000) // every 317.4mS
{
    Retrieve_Temp_Data();
}

if (Temp_Data > 850) // 85 degrees C
{
    TX_Reg_Data[16] = 0x0F; // Indicate an overtemp condition
    for (i = 0; i < 5; i++)
    {
        // Overtemp occurred - shutdown outputs
        Output_Enable(i, 2); // Disable outputs
        delay_ms(100); // To debug
    }
}

if (Temp_Data < 840) // 84 degrees C
{
    TX_Reg_Data[16] = 0x00; // Indicate a normal temp condition
}

/**************************** Input Low **********************************************/

if (TX_Reg_Data[12] <= 0x04 ) // MSB
{
    if ( TX_Reg_Data[13] < 0xE2 ) // If input is < 12.5 then indicate an UV condition

{  
  TX_Reg_Data[19] = 0x0D;
  for (i = 0; i < 5; i++)
  {
    OVP_Status = 0x00;  // Clear OVP condition  
    Output_Enable(i,2);  // Disable outputs
  }

  SysReset = 0x00;
  TX_Reg_Data[20] = SysReset;  // Assert /SYSRESET
  FAIL = 0x00;
  TX_Reg_Data[21] = FAIL;
}

} 
else
{
  TX_Reg_Data[19] = 0x00;  // Indicate an normal VIN condition
}

/***********************************************************/
// if the +5V is within regulation, the /SYSRESET signal is asserted
if ( (TX_Reg_Data[2] >= 0x01) ) // MSB 
{
  if ( (TX_Reg_Data[3] > 0xEF) && (SysReset == 0) ) // If input is > 4.95
    then assert /Sysreset
    { 
      SysReset = 0xA;
      delay_ms(200);
      TX_Reg_Data[20] = SysReset;  // Assert /SYSRESET
    }

  if ( TX_Reg_Data[3] < 0xEA)
  { 
    SysReset = 0x00;
    TX_Reg_Data[20] = SysReset;  // Assert /SYSRESET
  }
}

} 
if ( (TX_Reg_Data[2] < 0x01) ) // MSB
{
  SysReset = 0x00;
  TX_Reg_Data[20] = SysReset;  // Assert /SYSRESET
}

} 

/**************************************************************/
if ( ( (TX_Reg_Data[1] > 0x29) ) && (TX_Reg_Data[0] >= 0x01) ) // +3.3V MSB
if ( ( (TX_Reg_Data[3] > 0xC2) ) && (TX_Reg_Data[2] >= 0x01) ) // +5V MSB
if ( ( (TX_Reg_Data[5] > 0x29) ) && (TX_Reg_Data[4] >= 0x01) ) // +12V MSB
if ( ( (TX_Reg_Data[7] > 0x29) ) && (TX_Reg_Data[6] >= 0x01) ) // +3.3V_AUX MSB
if ( ( (TX_Reg_Data[9] > 0x29) ) && (TX_Reg_Data[8] >= 0x01) ) // -12V_AUX MSB
{  
  FAIL = 0x0B;
}
TX_Reg_Data[21] = FAIL;
    }
    if ( (TX_Reg_Data[0] < 0x01) || (TX_Reg_Data[2] < 0x01) || (TX_Reg_Data[4] < 0x01) || (TX_Reg_Data[6] < 0x01) || (TX_Reg_Data[8] < 0x01) )
    {
        FAIL = 0x00;
        TX_Reg_Data[21] = FAIL;
    }
    } // End while loop

} // End main program

/*
===================================================================
End Main Program
===================================================================
*/
/**********************************************************************************
Retrieve_ADC_Volt_Data
**********************************************************************************/
/
Retrieve_ADC_Volt_Data
num = ADC number
0 1 2 3 4 5 6
+3.3V +5V +12V +3.3V_AUX -12V_AUX DC_Bus Vin 
cmd = 0x01, 0x02, 0x03, 0x04, 0x04 0x05 0x06
*/

c char Retrieve_ADC_Volt_Data(char num)
{
    char vdata[16];
    char data_valid = 0, i;
    char reg_ind;
    reg_ind = num*2;
    if (num < 0x07)
    {
        //It's a valid command
        data_valid = Meas_Volt(num, vdata);
        if (data_valid)
        {
            for (i = 0; i < 2; i++)
            {
                TX_Reg_Data[reg_ind + i] = vdata[i];
            }
        }
    }
    return data_valid;
} // End Process_Status_Cmd

/**********************************************************************************
Initialize_TWI_ADCs
**********************************************************************************/
/
Initialize all the TWI Analog-to-Digital Converters for output voltage measurements
Configure single channel ADCs
0 1 2 3 4 5 6
+3.3V +5V +12V +3.3V_AUX -12V_AUX DC_Bus Vin
*/

void Initialize_TWI_ADCs(void)
{
    int i;
    char regdata[3];
    for (i=0; i < 7; i++)
    {

}
// Write OVP voltage to High Lim reg 0x04
regdata[0] = 0x04; // Reg 0x04
regdata[1] = (char)((OVP_Limit[i] >> 8) & 0x0F); // Shift bits to right, 8 places. Load MSB bytes
regdata[2] = (char)(OVP_Limit[i] & 0xFF); // Load LSB bytes
Write_TWI_Master(Volt_ADC_Addr[i],regdata,3); // Write to High Lim reg

// Write OVP Hysteresis to Alert Hyst reg 0x05
regdata[0] = 0x05; // Reg 0x05
regdata[1] = (char)((OVP_Hysteresis[i] >> 8) & 0x0F); // Shift bits to right, 8 places. Load MSB bytes
regdata[2] = (char)(OVP_Hysteresis[i] & 0xFF); // Load LSB bytes
Write_TWI_Master(Volt_ADC_Addr[i],regdata,3); // Write to Alert Hyst reg

// Set configuration register data
// Write to config reg 0x02
regdata[0] = 0x02; // Reg 0x02
regdata[1] = 0x24; // Configure auto-conversion 32uS, Alert pin enable, active low
Write_TWI_Master(Volt_ADC_Addr[i],regdata,2); // Write to config reg

// Reset over range bit in the Alert Status Register
regdata[0] = 0x01;
regdata[1] = 0x02;
Write_TWI_Master(Volt_ADC_Addr[i],regdata,2);

// Write to conv result reg 0x00
regdata[0] = 0x00; // Reg 0x00
Write_TWI_Master(Volt_ADC_Addr[i],regdata,1);
}

} // Initialize_TWI_ADCs

/***************************************************************************/
Meas_Volt
/***************************************************************************/
/*
Initialize all the TWI Analog-to-Digital Converters for output voltage measurements
Measure voltage 0 1 2 3 4 5 6
mindex = meas index
-3.3V +5V +12V +3.3V_AUX -12V_AUX DC_Bus Vin
p_Volt = two byte voltage data
p_mvalue = measured voltage
Returns data valid
*/

char Meas_Volt(char mindex, char *p_volt)
{
  // char nbytes = 0;
  char data_valid = 0, p_data[2];
  unsigned int adcdatas;
  //int voltdatas;
  int voltdatas;
  float mvalue, fpart, ipart;
  data_valid = Read_TWI_Master(Volt_ADC_Addr[mindex], p_data, 2);
  if (data_valid)
// Convert 2 byte adc data to integer
adcdata = ((unsigned int)(p_data[0] & 0xF)) << 8;  //may need to write 0xF
adcdata |= (unsigned int)p_data[1];

// Scale voltage data
mvalue = (float) adcdata * Volt_Scl_Fact[mindex];

// Return 2 bytes
fpart = modf(mvalue, &ipart);
voltdata = (int) ipart;
if (fpart >= 0.5) voltdata ++;  // Round up
p_volt[0] = (char)(voltdata >> 8);
p_volt[1] = (char)voltdata;

return data_valid;

} // Meas_Volt

/************************************************************
Write_TWI_Master
************************************************************/
/*
Write to TWI device via the master TWI interface
slv_addr = TWI slave address
*p_data = data byte array
dlength = data length
returns 1 if data is successfully transmitted to the TWI device
*/

char Write_TWI_Master(char slv_addr, char *p_data, char dlength)
{
    char data_valid = 0, i=0;

    /*Send Start and Address+Write to TWI device*/
data_valid = Send_TWI_Start_Addr_RW(slv_addr, 0);
    /* If TWI device acknowledges address, then send data*/

    if (data_valid)
    {
        do
        { ...
        } while ((++i < dlength) && data_valid);  //loop if not last byte and ACK received

        Send_Stop_bit();  //Send STOP condition to TWI

        return data_valid;  //Return true if data is successfully transmitted
    }

} // End Write_TWI_Master
Read_TW_I_Master
**********************************************************************************
Read_TW_I_Master
**********************************************************************************

Read from TWI device via the master TWI interface
slv_addr = TWI slave address
*p_data = receive data byte array
dlength = data length
Returns 1 if data is successfully received from the TWI device
*/

char Read_TW_I_Master( char slv_addr, char *p_data, char dlength)
{
    char data_valid = 0, i = 0;
    /*Send Start and Address+Write to TWI device*/
    data_valid = Send_TW_I_Start_Addr_RW(slv_addr, 1);
    if( data_valid )
    {
        // Receive data bytes
        do
        {
            if (i == (dlength - 1)) // Check to see if last byte
            {
                p_data[i] = Receive.Byte(1); //send NACK before STOP condition
            }
            else
            {
                p_data[i] = Receive.Byte(0); //Receive byte and send ACK
            }
        }
        while ( ++i < dlength ); //loop if not last byte and no error occurred
        Send_Stop_bit(); //Terminate communication
    return data_valid;
    } // End Read_TW_I_Master

Send_TW_I_Start_Addr_RW
**********************************************************************************
Send_TW_I_Start_Addr_RW
**********************************************************************************

Send Start condition and slave address + Read/Write* to TWI device
slv_addr = TWI slave address
rw = 1=Read, 0=write
returns 1 if TWI ACK, returns 0 if TWI NACK
*/

char Send_TW_I_Start_Addr_RW(char slv_addr, char rw)
{
    char ack = 0;
    char retry = 1;
    //char retry = 0;
do {
    Send_Start_bit(); //Send START condition
    ack = Send_Byte(slv_addr << 1 | rw); // Send slave address + read/write, returns
    true if TWI device ACK
}
} while (!ack && (retry-- == 1)); //Send repeated start if not acknowledge
return ack;
} // End Send_TWI_Start_Addr_RW

/**
 * Sequence_Enable
 * @brief Enables the outputs for the specified ports
 *
 * - B0: +3.3V_AUX
 * - B1: +12V
 * - B2: +5V
 * - B3: +3.3V
 * - B4: -12V_AUX
 *
 * @param outno The output number to enable
 * @param cmd The command to perform: 0x01 to disable, 0x02 to enable
 */
void Seq_Enable(void)
{
    Output_Enable(3,1); // Enable +3.3V_AUX
    delay_ms(41);
    Output_Enable(0,1); // Enable +3.3V
    delay_ms(4);
    Output_Enable(1,1); // Enable +5V
    delay_ms(4);
    Output_Enable(2,1); // Enable +12V
    Output_Enable(4,1); // Enable +12V_AUX
}
} // End Seq_Enable

/**
 * Output_Enable
 * @brief Enables or disables the specified outputs
 *
 * @param outno The output number to enable or disable
 * @param cmd The command to perform: 0x01 to disable, 0x02 to enable
 */
void Output_Enable(char outno, char cmd)
{
    if (cmd == 0x01)
    {
        PORTA &= ~(1<<outno); // Enable Output
        PortA_Status |= (1<<outno);
    }

    else if (cmd == 0x02)
    {
        PORTA |= (1<<outno); // Disable Output
        PortA_Status &= ~(1<<outno);
    }
}
Retrieve_Temp_Data
**********************************************************************************
void Retrieve_Temp_Data(void)
{
    char t_index = 0;
    Temp_Data = Meas_Temperature();

    // Two Temperature bytes
    TX_Reg_Data[t_index + 14] = (char)(Temp_Data >> 8); // High byte
    TX_Reg_Data[t_index + 15] = (char)(Temp_Data & 0xFF); // Low byte
}

Meas_Temperature
**********************************************************************************/*
int Meas_Temperature()
{
    unsigned int adcdat;
    char i, hibyte, lowbyte;
    float tval, tval1, tval2, fpart, ipart;
    int tempval;

    ADMUX = 0x07;
    ADCSRA |= (1<<ADSC); // Start conversion
    while ( (ADCSRA & (1<<ADIF)) == 0 ); // Wait for conversation to complete
    lowbyte = ADCL; // Retrieve low byte
    hibyte = ADCH; // Retrieve high byte
    adcdat = ((unsigned int)hibyte) << 8; // Add high byte
    adcdat|=(unsigned int)(lowbyte); // Add low byte
    ADCSRA |= (1<<ADIF); // Clear interrupt flag

    // Get Temperature
    i=0;
    while (adcdat < TEMP_TABLE[i++]);
    i--;
    tval = (float)i -55; // (add -55 degC offset)

    if ( (adcdat > TEMP_TABLE[i]) && (i > 0) )
    {
        // Value is between table values - interpolate value
        tval1 = (float)(TEMP_TABLE[i]);
        tval2 = (float)(TEMP_TABLE[i-1]);
        tval = (((float)adcdat - tval1) / (tval2 - tval1));
        tval *= 10;
        fpart = modf(tval, &ipart);
        tempval = (int)ipart;
    }
//Round
if (fpart >= .5)
tempval++;  // Round up

}  
else
{
    tval *= 10;  //
tempval = (int) tval;
}
return tempval;  //Integer value with LSB = 0.1C

/*
/XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX I2C Bit Bang Drivers XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX/
*/

/***********************************************************************************
Send_Byte
***********************************************************************************/

int Send_Byte(char byte)
{
    char i;
    char ack = 0;
    int timeout = 0;

    for(i = 0; i < 8; i++)
    {
        Write_SDA(byte & 0x80);  // Send bit, MSB first
        byte = (byte << 1);  // shift bits to the left
        Write_SCL(1);
        Write_SCL(0);
    }

    /*The master generates the clock pulse for ACKs, wait for ACK from slave device */
    // Release SDA pin, wait for ACKs
    Write_SDA(1);
    Write_SCL(1);
    ack = PIND.1;

    while ( ack == 1 && ++timeout < 10 )
    {
        ack = PIND.1;
    }

    Write_SCL(0);
    return ~ack;
Receive_Byte
@PostMapping{*
Receive one byte of data from the TWI slave device, MSB first.
lastbyte = 1, signals the TWI slave device that the read is over
*}/
char Receive_Byte(char lastbyte)
{
    int i;
    char SDA_bit;
    char result = 0;
    
    Write_SDA(1);
    for(i = 7; i >= 0; i--)
    {
        // Receive each bit at a time, MSB first
        Write_SCL(1);
        SDA_bit = PIND.1; // read SDA pin
        result |= SDA_bit << (i); //Shift left
        Write_SCL(0);
    }
    // Send ACK
    /* // If last byte, then send Master sends NACK. Else, Master sends ACK.
        (Immediately preceding a STOP condition, this bit must be a NACK) */
    Write_SDA(lastbyte);
    Write_SCL(1);
    Write_SCL(0);
    Write_SDA(1); // Set SDA to input, release SDA pin
    return(result);
} // End Receive_Byte

Send_Start_bit
PostMapping{*
Send Start Bit
A start condition occurs when SDA transitions from high to Low while the SCL is high.
*}/
void Send_Start_bit(void)
{
    Write_SDA(1); // Set SDA high
    Write_SCL(1); // To debug on STK600
    // Set_SCL_High(); // if timeout after 35uS occurs, then return TWI error, else
    return no error
    Write_SDA(0);
    Write_SCL(0);
} // End Send_Start_bit
/*************************************************************************************/
void Write_SDA(unsigned char state)
{
    if (state)
    {
        DDRD.1 = 0; // configure PDB1 as high-z input
    }
    else
    {
        DDRD.1 = 1; // configure PORTDB1 as output, PORTD.1 = 0; // Set SDA pin low
    }
}
// End Write_SDA

/*********************************************/
void Write_SCL(unsigned char state)
{
    if (state)
    {
        DDRD.0 = 0; // Configure PORTDB0 as input & high Z
    }
    else
    {
        DDRD.0 = 1; // Configure PORTDB0 as output, PORTD.0 = 0; // Set SCL pin low
    }
}
// End Write_SCL

/*********************************************/
void Send_Stop_bit(void)
{
    // A stop condition occurs when SDA transitions low to high while the SCL is high.
    // Assumes SCL is low at the beginning
}
{ Write_SDA(0); // Set SDA low
  Write_SCL(1); // Set SCL high
  #asm
    nop
    nop
    nop
    nop
  #endasm

  // Release SDA
  Write_SDA(1); // Set SDA high

} // End Send_Stop_bit
Appendix E. PC Board Layout

Figure 40 shows the Control Electronics PC board layout.

Figure 40: Control Electronics PC board layout