

# Work in Progress - Undergraduate Research in Power Electronics – The Super Barrier Rectifier Diode Project

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**Abstract** –This paper presents an undergraduate student research project which involved a new power diode called the Super Barrier Rectifier (SBR) diode. Several projects incorporating SBR diodes in dc-dc converters performed by undergraduate students will be described. Challenges in conducting these projects along with results will also be discussed.

*Index Terms* – Undergraduate research.

## INTRODUCTION

Power Electronics has recently experienced rapid growth due mainly to advances in semiconductor technology [1] as evidenced by the expansion of power semiconductor markets into areas such as automotive electronics, renewable energy, and portable electronics [2]. As a result, there has been an increasing demand for power electronic engineers. At Cal Poly, the demand has been noticeable by the rising number of companies that participated in campus career fair looking to hire graduating seniors with power electronic background. In turn, enrollment in power electronic courses at Cal Poly has increased in the past few years [3] and consequently so has the number of senior design projects in power electronics.

To better accommodate students wanting to conduct senior projects in power electronics, real-world design ideas were sought from local power semiconductor companies. One such company was APD Semiconductor who offered an undergraduate research involving a new type of power diode called the Super Barrier Rectifier (SBR) diode. The research was aimed to investigate the benefits of SBR in commonly used dc-dc converters. To meet this objective, 9 senior projects were conducted involving 15 undergraduate students.

## THE SBR RESEARCH PROJECT

The two families of diodes widely used for power electronic applications are the pn-junction and Schottky diodes. Unfortunately, both diodes still possess physical limitations that preclude their use in certain applications. For example, pn-junction diodes are reliable at high voltage applications, but they are relatively slow. Hence, pn-junction diodes are not suitable for fast switching applications. On the other hand, Schottky diodes are fast but they are not available at high

voltages. Table 1 lists typical trade-off comparison between the two diodes [4]:

TABLE 1  
Performance comparison of pn and schottky

	Pn-junction	Schottky
Forward Voltage	High	Low
Reverse Leakage Current	Low	High
Breakdown Voltage Range	High	Medium
Surge Reliability	High	Low
Thermal Immunity	High	Low
Switching speed	Low	High

As shown in Table 1, important operating characteristics are complimentary between the pn-junction diode and the Schottky diode. SBR diodes attempt to bridge this gap by uniquely capturing the benefits provided by each of these traditional devices and integrate them into a single component [5]. Other benefits of SBR which became the focus of the research project are their very low on-state losses and low leakage currents.

The SBR research project presented in this paper entailed the implementation and performance study of SBR diodes as compared with Schottky diodes in commonly used dc-dc converters. Several important circuit parameters that were investigated include overall converter's efficiency, power loss analysis due to forward conduction drop and leakage, and switching losses. Impact of thermal impedance to circuit's performance and effect of changing switching frequency to converter's efficiency were also studied. The following lists 9 dc-dc converters that were planned for the SBR research:

- Flyback Converter 3.3 V at 50 W
- Synchronous Flyback Converter 3.3 V at 50 W
- Synchronous Flyback Converter 15 V at 50 W
- Buck Converter 3.3 or 5 V at 1 W
- Buck Converter 3.3 V at 30 W
- Synchronous Buck Converter 3.3 V at 30 W
- Buck Converter 12 V at 30 W
- Boost Converter 12 V at 15 W
- Off-line Flyback Power Supply 15 V at 100 W

OBSTACLES AND FUTURE WORK

The research project spanned for two quarters and each project was formally administered under senior project design course. Therefore, as part of the course requirements, students involved in the research project had to submit weekly status progress. By the end of the second quarter, besides submitting their final reports, the students had to demo their hardware projects to faculty advisor and representatives from APD semiconductor.

RESULTS

9 dc-dc converters were designed, built and tested for the SBR undergraduate research. Most of the converters built were fully operational while some were partly functioning. Figure 1 shows one converter that was successfully built for this research project. Figure 2 illustrates an example of the result obtained from circuit shown in Figure 1 in which SBR improved converter's overall efficiency of the converter. Overall, based on the results obtained and with the exception in a few cases, it can be concluded that SBR diodes did provide potential benefits over Schottky diodes. Hence, SBR diodes offer great potential in future power electronic applications.

There were several obstacles encountered during the course of the research activities. The first was equipment limitation. Although most tasks in this research could be performed using equipment currently available in the power electronics lab at Cal Poly; however, some measurements unfortunately could not be undertaken. An example was the lack of temperature chamber causing the inability to conduct below room temperature testing and measurements. The lack of sophisticated temperature measuring equipment also posed difficulty in conducting stable and accurate temperature measurements. Coordinating all 9 projects was also a challenge since 15 students were involved. It was very difficult to find a common time where all students involved could meet and report their progresses. To overcome this problem, it was decided that the students had to submit end of week status report via email. Another issue was the cost associated with building the dc-dc converters since APD Semiconductor only supported the diodes needed for the research project.

Following the final demonstrations of the 9 projects before the representatives and engineers from APD Semiconductor, discussion on conducting more projects to further investigate SBR's performance in other power electronic applications took place.

CONCLUSIONS

This paper describes the undergraduate research performed at Cal Poly to investigate the performance of newly developed Super Barrier Rectifier diode. Students designed and built dc-dc converters to test the performance of SBR diodes as compared to Schottky diodes. Overall, this research project was a success. More importantly, the research project provided the hands-on experience valuable for the students to better prepare them for the rapidly growing power semiconductor industry. Future collaboration with APD Semiconductor to further investigate SBR performance in many other power electronic applications was planned.

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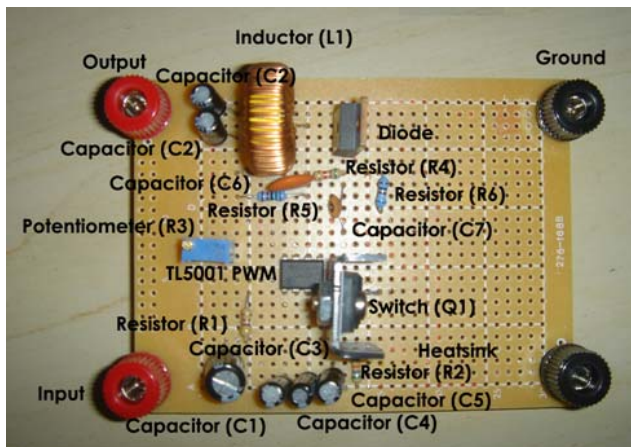


FIGURE 1  
A 30 W Buck Converter

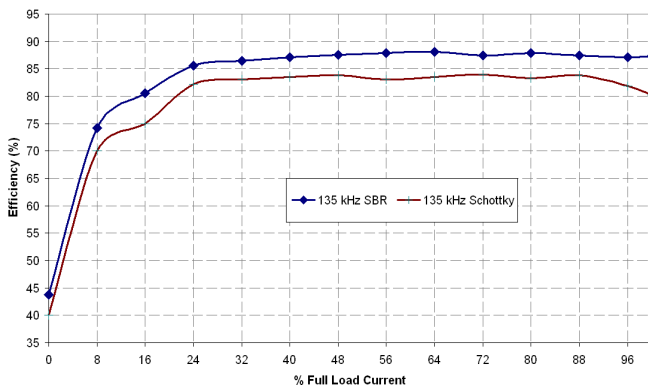


FIGURE 2  
Overall efficiency using SBR vs. Schottky