Chromium OS on Freescale i.MX6q

Author: Pushpal Sidhu (psidhu@calpoly.edu)
Adviser: Lynne Slivovsky (lslivovs@calpoly.edu)

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Chapter 1

Project Charter

This chapter will discuss the undertaking of this project, its’ clients, goals, objectives, and analyze the risks involved. Using this information, this chapter will attempt to detail out the duration of the project through the use of a timeline with stretch goals.

1.1 Project Overview

I will attempt to port Chromium OS to the Gateworks© Ventana GW5400 Single Board Computer with a Freescale i.MX6q processor with basic support of the board peripherals and will attempt several stretch goals to enable specific peripherals on the Ventana GW5400. After I determine risks in Section 1.7, criteria in Section 2.2, and related works and other research in Section 3.2, I will use the knowledge gained at CalPoly and outside sources to allow Chromium OS to be installed by any user following the set of instructions I will have written up at the end of this project.

1.2 Client and Community Partners

Gateworks© will serve as the beneficiary of this project. The company was founded locally on the central coast with an emphasis on embedded software and firmware develop-
ment. The company then began embedded systems hardware and incorporated in 1998 [1]. This project will allow for the company to increase their repertoire of supported operating systems and thereby allowing for greater marketability of their products. In this sense, the undertaking of this project will directly benefit the Sales and Marketing group within Gateworks®, especially since no platform currently exists with a Freescale i.MX6q running Chromium OS.

1.3 Stakeholders

Table 1.1 will detail out the stakeholders involved with this project.

<table>
<thead>
<tr>
<th>Stakeholder</th>
<th>Reasons</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gateworks®</td>
<td>The company would like to get involved with operating systems that are easy for users to use.</td>
</tr>
<tr>
<td>Engineering Group at Gateworks®</td>
<td>This group within Gateworks® would like to see this proof of concept become a reality to verify the ARM support within Chromium OS</td>
</tr>
<tr>
<td>Tim Harvey</td>
<td>Principal Software Engineer at Gateworks®. Tim maintains current BSP’s for Gateworks®</td>
</tr>
<tr>
<td>Ron Eisworth</td>
<td>Executive Vice President of Sales and Marketing with a vested interest in the marketability of Chromium OS</td>
</tr>
</tbody>
</table>

Table 1.1: This table denotes this project’s stakeholders

1.4 Framed Insights and Opportunities

This section will outline the current contact I’ve had with the client and will then state the necessity of this project.

1.4.1 Communication

I was first approached to undertake this project by Tim Harvey (see Table 1.1 for more information on Tim Harvey). After further review, I deemed it a worthwhile project. Since then, Ron Eisworth, Executive Vice President of Sales and Marketing at Gateworks®,
also approached me to gear this project towards the usability of Chromium OS on Gateworks® Ventana family line. After discussions with all of the stakeholders, we agreed that the Ventana GW5400 would serve as the main focus of this project, with the rest of the Ventana Family coming after it, well after the end of this project.

1.4.2 Need Statement

Porting Chromium OS to a Freescale i.MX6q will further my inclination to contribute to the open source community and help shape my future endeavors.

1.5 Goals and Objectives

This section will lay out the goals and objectives of this project. Section 1.7 will then speak towards this projects’ risk plan surrounding these goals and objectives.

1.5.1 Goals

The main goal of this project is to get Chromium OS running on a Ventana GW5400 with basic support, including HDMI Video Out and Ethernet networking. Any other peripheral support will serve as separate stretch goals of this project. For specific stretch goals, please see Figure 1.1.

1.5.2 Objectives

Table 1.2 will outline the main objectives of this project.
### Objective

<table>
<thead>
<tr>
<th>Objective</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Create a Recreateable Build Environment</td>
<td>Create a system that will cross-compile ARM code for the Ventana GW5400.</td>
</tr>
<tr>
<td>Create a Basic Board Port</td>
<td>Create a port of Chromium OS to the Ventana GW5400 with video out, Ethernet, and serial communication working.</td>
</tr>
<tr>
<td>Create a Board Support Package</td>
<td>Create a port of Chromium OS to the Ventana GW5400 with the Freescale i.MX6q processor. This also includes configuring Chromium OS to understand all the attached peripherals of the Ventana GW5400 so they may be utilized.</td>
</tr>
</tbody>
</table>

Table 1.2: This table denotes this projects’ objectives

### 1.6 Outcomes and Deliverables

Table 1.3 will outline the outcomes and deliverables for this project to the client.

<table>
<thead>
<tr>
<th>What</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Documentation</td>
<td>Any and all documentation on how to recreate this project.</td>
</tr>
<tr>
<td>Source Code</td>
<td>Any and all source code written and obtained, including any driver support required for any peripheral on the intended target.</td>
</tr>
</tbody>
</table>

Table 1.3: This table denotes this projects’ outcomes and deliverables

### 1.7 Project Risk Analysis

This section will describe risks involved with this project as well as any risk response strategies.

#### 1.7.1 Risks

Table 1.4 and 1.5 shows the failure mode and effects analysis of this project.
### Failure Mode and Effects Analysis

**FMEA Date (Orig):** June 12, 2014  
**Type:** Project

<table>
<thead>
<tr>
<th>Process Function</th>
<th>Potential Failure Mode</th>
<th>Potential Effect(s)</th>
<th>Severity</th>
<th>Potential Cause(s)</th>
<th>Occurrence</th>
<th>Detect</th>
<th>RPN</th>
<th>Recommended Action(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Simple Board Port Goal</td>
<td>Never gets completed</td>
<td>Project doesn’t complete</td>
<td>10</td>
<td>Binaries not available</td>
<td>3</td>
<td>1</td>
<td>30</td>
<td>Keep working with required parties</td>
</tr>
<tr>
<td>Technical Documents</td>
<td>Do not get completed</td>
<td>Project becomes difficult to recreate</td>
<td>3</td>
<td>Laziness</td>
<td>2</td>
<td>1</td>
<td>6</td>
<td>Overcome</td>
</tr>
<tr>
<td>Software Version Control</td>
<td>Agile Development not Utilized</td>
<td>Recreation becomes difficult</td>
<td>5</td>
<td>Laziness</td>
<td>2</td>
<td>2</td>
<td>20</td>
<td>Overcome</td>
</tr>
<tr>
<td>Chromium OS Compatibility</td>
<td>Chromium OS not compatible with Freescale Vivante Driver</td>
<td>3D Video Acceleration is not viable</td>
<td>5</td>
<td>Compatibility not present in Chromium OS</td>
<td>3</td>
<td>1</td>
<td>15</td>
<td>Write hooks into Freescale Vivante Driver</td>
</tr>
<tr>
<td>Video Input Drivers</td>
<td>Unavailable</td>
<td>Cannot create full-featured BSP</td>
<td>3</td>
<td>Drivers not written</td>
<td>4</td>
<td>2</td>
<td>24</td>
<td>Write Video Input Driver</td>
</tr>
</tbody>
</table>

Table 1.4: This is the FMEA for Project Occurrences
1.8 Estimated Development Time

Figure 1.1 shows an outline of the timeline I will be following for the duration of this project:

![Timeline Diagram](image-url)
Notice task ID’s 7-17 with the off color. This color represents this project’s stretch goals, while task ID’s 1-6 represent immediate goals of this project.
Chapter 2

Formal Product Definition

This chapter will formally define the product in marketing and engineering terminology. Thus, it will allow for constraints to be created in order to logically go through with this project.

2.1 Requirements

The following sections will breakdown both the marketing and engineering requirements for this project.

2.1.1 Marketing

The following are the marketing requirements of this project:

- The project should be repeatable in its’ entirety
- Chromium OS should have wireless Internet capabilities
- Chromium OS should have video out
- Chromium OS should have touchscreen abilities
2.1.2 Engineering

The following are engineering requirements of this project:

- Functionality
  - Ethernet on eth0
    - Gives network capabilities
  - HDMI Video Out
    - Requirement of Chromium OS
  - Analog Audio Out
    - Requirement of Chromium OS
  - Analog Audio In
    - Grants “MIC” options

- Performance
  - The Ventana GW5400 will utilize the Vivante Driver in the Freescale i.MX6q to allow full 3D Video Acceleration in OpenGL ES
    - This will allow for the full user experience Chromium OS aims to achieve
  - Gigabit speeds will be enabled on both eth0 and eth1 ports
    - eth0 is directly connected to the Freescale i.MX6q, with limiting speed at 480mbit while eth1 is connected to a gigabit switch at 1000mbit speeds
  - The GPS will get a lock on a minimum of 3 satellites within 1 minute with a reasonable antenna and outside location
  - Analog audio input and output streams shall be performed in real-time

- Economic
  - The Ventana GW5400 will come out of pocket of Gateworks©
    - Since Gateworks© both manufactures and sells the Ventana GW5400, they are willing to give up a product to further this project.

- Energy
• The Ventana GW5400 shall be powered either through PoE or directly through a DC power supply as specified by Gateworks©

- Legal
  • Any source code written shall conform to GNUv2 or MIT licensing
    ◦ Part of the requirement for Chromium OS
  • All source code shall conform to Chromium OS’s coding style
    ◦ Part of the Requirement for Chromium OS
  • I will not divulge any secrets of the Gateworks© Corporation without expressed written consent
    ◦ Part of our non-written NDA

- Maintainability
  • Any maintenance shall continue under the engineering group within Gateworks©

- Operational
  • Chromium OS shall not change or destroy the hardware it is running on
    ◦ Any hardware drivers won’t overdrive components on the Ventana GW5400

- Political
  • At the conclusion of this project, I will pass on all design information to my client, Gateworks© while still maintaining a public form
  • Any software will comply with the Digital Millennium Copyright Act
    ◦ Prohibits dissemination of the device intending to circumvent measures

- Social and Cultural
  • System defaults to the English
    ◦ Gateworks© is an American Company as well as most of the Chromium OS developers

- Usability
  • Binaries will be available on Gateworks© servers
    ◦ This will allow for easy installation of Chromium OS for any users
2.1.3 Constraints

The following list shows some of my constraints:

- I have a budget for the price of a Ventana GW5400 + any other hardware given to me by Gateworks®
- I do not have access to the source code for the Vivante driver for the Freescale i.MX6q
- My build system has an AMD Athlon II X3 Triple-Core Processor with 8GB RAM
- The OS will be installed on a MMC card and therefore read/write speeds are slower than NAND Flash
  - Note that there mSata SSD might be able to replace MMC if I can acquire a card
- The Ventana GW5400 specifications are as follows
  - Freescale i.MX6 1GHz Quad Core Processor
  - 512MB DDR3 RAM

2.2 Criteria

Table 2.1 shows the criteria I’ve chosen for this project.

<table>
<thead>
<tr>
<th>Criteria</th>
<th>Parameters</th>
<th>Standard</th>
</tr>
</thead>
<tbody>
<tr>
<td>Build Time</td>
<td>Minutes</td>
<td>Lower is Better</td>
</tr>
<tr>
<td>Flash Time</td>
<td>Seconds</td>
<td>Lower is Better</td>
</tr>
<tr>
<td>Video Rendering</td>
<td>Minutes</td>
<td>Lower is Better</td>
</tr>
<tr>
<td>Network interface speed</td>
<td>Mbits/s</td>
<td>High is Better</td>
</tr>
<tr>
<td>File System Type</td>
<td>UBIFFS/jffs2</td>
<td>UBIFFS is preferred by the client</td>
</tr>
</tbody>
</table>

Table 2.1: This table shows my criteria for this project
Chapter 3

Related Work and Projects

This chapter shows all research found relating to this project, with the hopes of furthering my understanding of this project and any attempts made beforehand.

3.1 The Chromium Projects

The official channels for Chromium OS lay out their guidelines for creating an official board port and how to properly sync the code to their repositories [2]. The first step in this is to setup a development machine [3] which states the minimum system requirements are as follows:

- Ubuntu Linux (version 12.04)
- An x86_64-bit system
- 4GB RAM

In addition to the above requirements, there are some software requirements that are listed in [3] in the TOC-Prerequisites section.

After getting a development machine setup, a developer creating a board port creates a “board overlay” (the wording used by the Chromium OS development team) that will sync with their file management scheme. There is also the option of creating a private board
overlay that will not upload to the public repository until the developer is ready to publish the release of the board overlay.

There still exists empirical data gathering to determine the shortest method of developing the kernel and Chromium OS. Once this phase is done, I might be able to edit the [2] webpage to further their explanations on this subject matter.

3.2 Other Related Works

Nothing exists as of today. This is mainly due to the fact that this is the first project to try and port Chromium OS to Freescale i.MX6q. This may change in future versions of this document.
Chapter 4

System Integration and Testing

This chapter will discuss my methodology on what aspects of my project to test. It will then go over display the results and have a writeup to analyze them.

4.1 Test Plan

The following functionality will be tested for:

- Power Consumption
- eth0/1
- USB EHCI/OTG
- Mini-PCIE (6)
  - Testing with standard mini-pcie ath9k radios
- User LED
- Accelerated Video Out

4.2 Test Results

The following are the results:

- Power Consumption
• Passed - Less than 6W
  – eth0/1
    • Passed eth0 - Greater than 100mbps
    • Passed eth1 - Greater than 100mbps
  – USB EHCI/OTG
    • EHCI - Passed - USB hub detected by OS
    • OTG - Failed - Could not get into device mode
  – Mini-PCIE (6)
    • Freescale i.MX6q Root Complex - Found
    • PLX Switch - Found
    • Marvel PHY - Found
  – User LED
    • /sys/class/led/led/user1 - Exported and writable
  – Accelerated Video Out - Failed

4.3 Test Analysis

The test results in Section 4.2 show what was tested with what results in a list form. As
seen, I have tested both ethernet ports, USB Host and OTG, the PCIe, user LED, and
accelerated video out.

Of these, all passed except USB OTG and Accelerated Video Out. USB OTG could not
act as device mode due to limitations in Chromium OS’s implementation of it. It could,
however, act as a USB Host device. Accelerated video out failed due to Freescale’s closed
system for their Vivante GPU driver. Because Chromium OS currently only supports
ARM Mali GPU’s, I had a difficult time getting the Vivante GPU driver into Chromium
OS. This will be a future goal of mine, if I continue this project.
Appendix A

A.1  Budget

This section will lay out any part requests I had.

A.1.1  Requests and Justifications

This project had no requests for parts, other than what Gateworks© had donated.

A.2  Bill of Materials

This project does not have a Bill of Materials (BOM) as all hardware was provided by Gateworks©. However, there were several development costs, shown in Table A.1

<table>
<thead>
<tr>
<th>Equipment</th>
<th>Cost</th>
</tr>
</thead>
<tbody>
<tr>
<td>AMD FX-8350 Processor</td>
<td>183.59</td>
</tr>
<tr>
<td>Class 10 SD Card</td>
<td>16.11</td>
</tr>
<tr>
<td>Total</td>
<td>199.70</td>
</tr>
</tbody>
</table>

Table A.1: This table denotes this projects development costs
Appendix B

B.1 Actual Development Time

Figure B.1 shows how my developmental time was actually used.

![Timeline](image_url)

Figure B.1: This is my final timeline
Bibliography


