# Crosstalk and image uniformity in passive matrix polymer LED displays

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### Abstract

Passive matrix displays based on polymer and organic light-emitting diodes suffer from crosstalk, when the emission of light from one pixel depends on the operation of another pixel. We use circuit simulation in order to explain the causes of crosstalk and to quantify the consequences. This work analyzes how crosstalk arises from electrode resistance, pixel leakage current, and the location of faulty pixels. All factors influence both image uniformity and the power consumption of the display.

Keywords: Electroluminescent displays, Poly(phenylene vinylene) derivatives, Electrical circuit simulation

#### 1. Introduction

Much interest in polymer and organic light-emitting diodes [LEDs] derives from their potential to deliver the next generation of flat passive matrix displays [1-4]. The devices are relatively easy to make, because they consist of an electroluminescent layer sandwiched between an anode, usually transparent, and a cathode. A passive matrix display results by patterning the anode into columns and the cathode into rows to form an array of pixels from the intersections between the cathode and anode electrodes. Applying a positive bias to one anode and a negative bias to one cathode causes light emission from the pixel shared by the two electrodes. In many cases, activating one pixel influences light emission from one or more other pixels. The crosstalk has several origins. Earlier work describes how crosstalk depends on display resolution, the degree of rectification of the individual LEDs, and, most significantly, on the reverse leakage current of the pixels [5]. This work explores how electrode resistance and the location of faulty pixels can degrade image uniformity in row scanned passive matrix displays.

### 2. Electrode resistance

Fig. 2 shows how resistance in the row electrode decreases image uniformity along one row. The approach used is to insert the electrical data for 1 cm<sup>2</sup> LEDs from Fig. 1 directly into the PSpice circuit simulation tool along with a circuit net list for the 10 pixel row circuit shown in the inset to Fig. 2. Each pixel contains one LED and a row (cathode) resistance of 10 m $\Omega$ /pixel.

Fig. 3 shows how resistance in the column (anode) electrode decreases image uniformity along one column. The circuit in the inset to Fig. 3 illustrates that the top pixel in the column sees a

larger series resistance than the bottom pixel and therefore receives a smaller current density at the same applied bias.



Fig. 1. Current as a function of voltage recorded for a polymer LED made from MEH-PPV.

#### 3. Location of pixel faults

In Fig. 4, the location of a leaky pixel in a row determines the severity of non-uniformity. With a pixel fault of 100  $\Omega$  in the pixel closest to the voltage source, the first pixel diverts the

excess current, so other pixels in the row suffer only a minor drop in intensity. A pixel fault further down the row sends the excess current further down the row, creating a larger voltage drop along the cathode electrode and dropping the intensity of more pixels in the row.

# 4. Conclusion

Degraded image uniformity due to electrode resistance implies that drivers should supply current instead of voltage to column contacts. In addition, electrode resistance increases the power consumption of the display. Because the problems increase with drive current, higher efficiency LEDs can also decrease problems of uniformity and power consumption that arise from electrode resistance and pixel faults. Quantitatively, the above simulation results based on rows and columns 10 pixels long illustrate that a 100 cm<sup>2</sup> display will suffer dramatically from poor uniformity.

# 5. References

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Fig. 3. Pixel intensity ratio in one column as a function of column (anode) resistance and applied bias. In a column of 10 pixels, the anode resistance of the top pixel is 10 times that of the bottom pixel.



Fig. 2. Pixel intensity in one row as a function of pixel position with applied bias ranging from 2 V to 10 V. The row (cathode) resistance is 10 m $\Omega$ /pixel. With the anode of each pixel in the row biased to the same voltage, current flow in the row electrode produces voltage drops that create unequal voltages at the cathodes of each pixel.



Fig. 4. Pixel intensity in one row as a function of pixel position and a faulty pixel. The row (cathode) resistance is 10 m $\Omega$ /pixel, and each cathode has a bias of 6 V. With the anode of each pixel in the row biased to the same voltage, current flow in the row electrode produces voltage drops that create unequal voltages at the cathodes of each pixel. The voltage drops are most severe "upstream" of the leak.